SPEC® CFP2006 Result

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Huawei

Huawei RH2288H V3 (Intel Xeon E5-2667 v4)

SPECfp®2006 = Not Run

SPECfp_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CP Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">

Non-Compliant

410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
[illegible]
470.lbm
481.wrf
482.sphinx3

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Hardware

CPU Name:	Intel Xeon E5-2667 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-)
Disk Subsystem:	1 x 500 GB SATA, 720 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 4.12.49-11-default
Compiler:	C/C Version 16.0.0.101 of Intel C++ Studio XE for Li Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

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Non-Compliant

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	NC	NC	NC	NC	NC	NC						
416.gamess	NC	NC	NC	NC	NC	NC						
433.milc	NC	NC	NC	NC	NC	NC						
434.zeusmp	NC	NC	NC	NC	NC	NC						
435.gromacs	NC	NC	NC	NC	NC	NC						
436.cactusADM	NC	NC	NC	NC	NC	NC						
437.leslie3d	NC	NC	NC	NC	NC	NC						
444.namd	NC	NC	NC	NC	NC	NC						
447.dealII	NC	NC	NC	NC	NC	NC						
450.soplex	NC	NC	NC	NC	NC	NC						
453.povray	NC	NC	NC	NC	NC	NC						
454.calculix	NC	NC	NC	NC	NC	NC						
459.GemsFDTD	NC	NC	NC	NC	NC	NC						
465.tonto	NC	NC	NC	NC	NC	NC						
470.lbm	NC	NC	NC	NC	NC	NC						
481.wrf	NC	NC	NC	NC	NC	NC						
482.sphinx3	NC	NC	NC	NC	NC	NC						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-8kvv Tue Mar  8 08:08:27 2016
```

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Non-Compliant

Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2667 v4 @ 3.20GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 3 4 8 10 11 12
      physical 1: cores 0 2 3 4 8 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       264063264 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
```

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Non-Compliant

Platform Notes (Continued)

```
      Linux linux-8kvv 3.12.49-11-default #1 SMP Wed Nov 11 10:52:43 UTC 2015
      (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Mar 8 08:06 last=5

   SPEC is set to: /spec16
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda2      xfs   922G   13G  909G   2% /
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended" to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS Insyde Corp. 3.09 02/23/2016
     Memory:
      8x NO DIMM NO DIMM   3 rank
      8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
      8x Samsung M393A2G40EB2-CRC 16 GB 2 rank 2400 MHz

   (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "16"

 Compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc   -m64
```

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Non-Compliant

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP
416.gamess: -DSPEC_CPU_LP
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP -nofor_main
437.leslie3d: -DSPEC_CPU_LP
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPE CPU_L 64
450.soplex: -DSPE CPU_LP
453.povray: -DSPEC_ _LP64
454.calculix: - CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC CPU_LP64
465.tonto: -DSPEC_ PU_LP64
470.lbm: -DSPEC_ PU_LP64
481.wrf: - CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Continued on next page

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Base Optimization Flags (Continued)

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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