



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint®2006 = 70.6

SPECint_base2006 = 68.1

CPU2006 license: 19

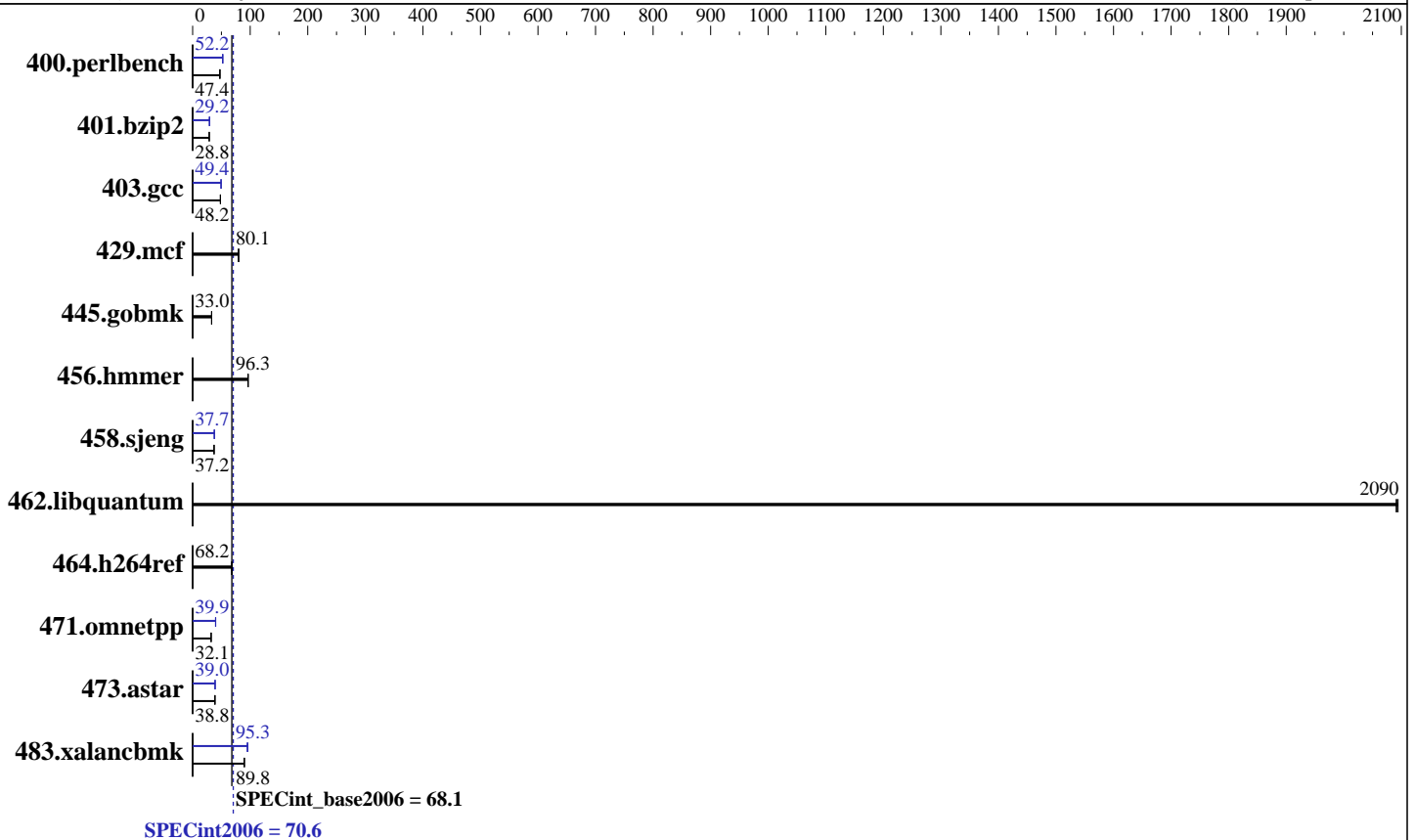
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2016

Hardware Availability: Feb-2016

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E3-1225 v5
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-E)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 Kernel 3.12.48-52.27-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint2006 = **70.6**

SPECint_base2006 = **68.1**

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2016
Hardware Availability: Feb-2016
Software Availability: Sep-2015

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>206</u>	<u>47.4</u>	208	46.9	205	47.7	<u>187</u>	<u>52.2</u>	186	52.6	187	52.1
401.bzip2	336	28.8	334	28.9	<u>335</u>	<u>28.8</u>	<u>330</u>	<u>29.2</u>	330	29.2	329	29.3
403.gcc	166	48.4	167	48.1	<u>167</u>	<u>48.2</u>	163	49.4	<u>163</u>	<u>49.4</u>	163	49.4
429.mcf	115	79.3	<u>114</u>	<u>80.1</u>	114	80.2	115	79.3	<u>114</u>	<u>80.1</u>	114	80.2
445.gobmk	318	33.0	<u>318</u>	<u>33.0</u>	318	33.0	318	33.0	<u>318</u>	<u>33.0</u>	318	33.0
456.hammer	<u>96.8</u>	<u>96.3</u>	97.1	96.1	96.6	96.6	<u>96.8</u>	<u>96.3</u>	97.1	96.1	96.6	96.6
458.sjeng	325	37.2	325	37.2	<u>325</u>	<u>37.2</u>	322	37.6	321	37.7	<u>321</u>	<u>37.7</u>
462.libquantum	9.91	2090	9.89	2090	<u>9.90</u>	<u>2090</u>	9.91	2090	9.89	2090	<u>9.90</u>	<u>2090</u>
464.h264ref	326	67.8	<u>325</u>	<u>68.2</u>	324	68.3	326	67.8	<u>325</u>	<u>68.2</u>	324	68.3
471.omnetpp	<u>195</u>	<u>32.1</u>	194	32.1	195	32.0	156	40.1	157	39.7	<u>157</u>	<u>39.9</u>
473.astar	<u>181</u>	<u>38.8</u>	182	38.5	181	38.9	<u>180</u>	<u>39.0</u>	180	38.9	179	39.1
483.xalancbmk	<u>76.8</u>	<u>89.8</u>	76.9	89.7	76.8	89.9	72.7	94.9	72.1	95.7	<u>72.4</u>	<u>95.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

```
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on TX1320M2 Mon Jan 11 11:04:20 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1225 v5 @ 3.30GHz
1 "physical id"s (chips)
4 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint2006 = 70.6

SPECint_base2006 = 68.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2016
Hardware Availability: Feb-2016
Software Availability: Sep-2015

Platform Notes (Continued)

cache size : 8192 KB

From /proc/meminfo

MemTotal: 65906620 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 0

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12"

VERSION_ID="12"

PRETTY_NAME="SUSE Linux Enterprise Server 12"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12"

uname -a:

Linux TX1320M2 3.12.48-52.27-default #1 SMP Mon Oct 5 10:08:10 UTC 2015
(314f0e3) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Jan 11 10:58

SPEC is set to: /home/SPECcpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	1.6T	8.0G	1.6T	1%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.1.0 for D3373-Alx
10/30/2015

Memory:

4x SK Hynix HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint2006 = 70.6

SPECint_base2006 = 68.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2016
Hardware Availability: Feb-2016
Software Availability: Sep-2015

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint2006 = 70.6

SPECint_base2006 = 68.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2016
Hardware Availability: Feb-2016
Software Availability: Sep-2015

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint2006 = 70.6

SPECint_base2006 = 68.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2016
Hardware Availability: Feb-2016
Software Availability: Sep-2015

Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmarheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1225 v5, 3.30 GHz

SPECint2006 = 70.6

SPECint_base2006 = 68.1

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2016

Hardware Availability: Feb-2016

Software Availability: Sep-2015

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Feb 9 17:22:05 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 February 2016.