



# SPEC® CINT2006 Result

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## Supermicro

Supermicro X11SSM-F motherboard  
(X11SSM-F , Intel Xeon E3-1270 v5)

**SPECint\_rate2006 = 264**

**SPECint\_rate\_base2006 = 255**

CPU2006 license: 001176

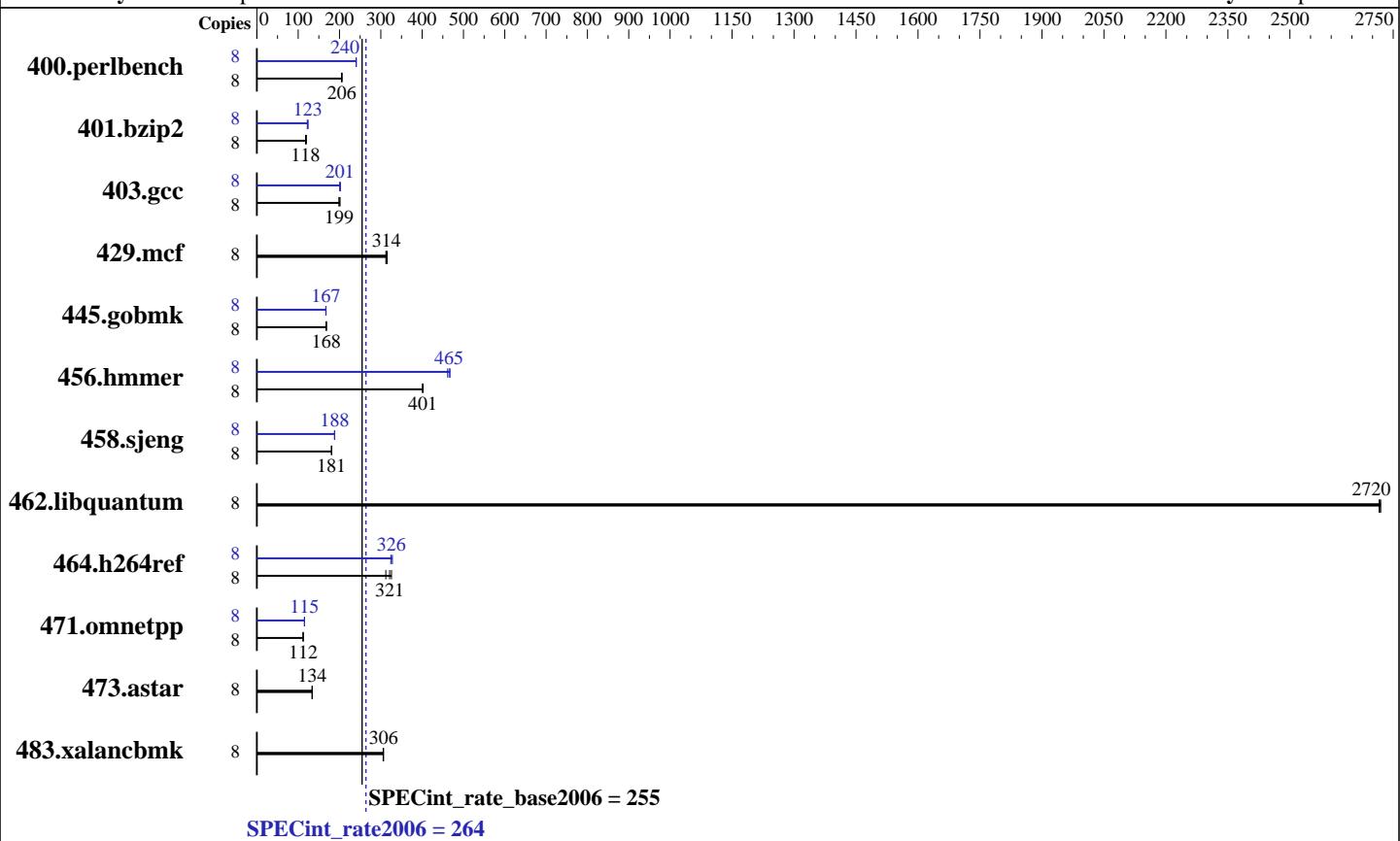
Test sponsor: Supermicro

Tested by: Supermicro

**Test date:** Jan-2016

**Hardware Availability:** Oct-2015

**Software Availability:** Sep-2015



### Hardware

CPU Name:	Intel Xeon E3-1270 v5
CPU Characteristics:	Intel Turbo Boost Technology up to 4.00 GHz
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (2 x 16 GB 2Rx8 PC4-2133P-E)
Disk Subsystem:	1 x 400 GB SATA III SSD
Other Hardware:	None

### Software

Operating System:	Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>380</b>	<b>206</b>	382	205	379	206	8	325	240	325	241	<b>325</b>	<b>240</b>
401.bzip2	8	644	120	<b>654</b>	<b>118</b>	655	118	8	624	124	633	122	<b>627</b>	<b>123</b>
403.gcc	8	324	199	<b>323</b>	<b>199</b>	320	201	8	<b>320</b>	<b>201</b>	321	201	318	202
429.mcf	8	234	312	<b>233</b>	<b>314</b>	231	315	8	234	312	<b>233</b>	<b>314</b>	231	315
445.gobmk	8	<b>499</b>	<b>168</b>	499	168	500	168	8	503	167	<b>502</b>	<b>167</b>	501	167
456.hmmer	8	185	402	186	401	<b>186</b>	<b>401</b>	8	160	468	162	462	<b>160</b>	<b>465</b>
458.sjeng	8	534	181	536	181	<b>536</b>	<b>181</b>	8	<b>515</b>	<b>188</b>	514	188	<b>515</b>	188
462.libquantum	8	60.9	2720	61.0	2720	<b>61.0</b>	<b>2720</b>	8	60.9	2720	61.0	2720	<b>61.0</b>	<b>2720</b>
464.h264ref	8	<b>551</b>	<b>321</b>	543	326	567	312	8	<b>544</b>	<b>326</b>	540	328	546	324
471.omnetpp	8	447	112	<b>446</b>	<b>112</b>	446	112	8	<b>433</b>	<b>115</b>	433	115	435	115
473.astar	8	421	134	418	134	<b>419</b>	<b>134</b>	8	421	134	418	134	<b>419</b>	<b>134</b>
483.xalancbmk	8	181	306	180	307	<b>180</b>	<b>306</b>	8	181	306	180	307	<b>180</b>	<b>306</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-113MFAC2-R606CB chassis. The chassis is configured with 2 PWS-606P-1R redundant power supply, 1 SNK-P0046P heatsink, as well as 4 FAN-0154L4 middle cooling fan.

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$
e3fbb8667b5a285932ceab81e28219e1
running on X10SRA-01 Sun Jan 10 05:22:52 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1270 v5 @ 3.60GHz
 1 "physical id"s (chips)
 8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



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## Platform Notes (Continued)

```
caution.)  
    cpu cores : 4  
    siblings   : 8  
    physical 0: cores 0 1 2 3  
    cache size : 8192 KB  
  
From /proc/meminfo  
MemTotal:      32768216 kB  
HugePages_Total:       0  
Hugepagesize:     2048 kB  
  
From /etc/*release* /etc/*version*  
os-release:  
  NAME="Red Hat Enterprise Linux Server"  
  VERSION="7.1 (Maipo)"  
  ID="rhel"  
  ID_LIKE="fedora"  
  VERSION_ID="7.1"  
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"  
  ANSI_COLOR="0;31"  
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server  
  
uname -a:  
Linux X10SRA-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015  
x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Jan 10 01:22  
  
SPEC is set to: /usr/cpu2006  
Filesystem      Type  Size  Used Avail Use% Mounted on  
 /dev/sda2        xfs   183G   5.2G  178G   3% /  
Additional information from dmidecode:  
  
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.  
  
BIOS American Megatrends Inc. 1.0b 12/22/2015  
Memory:  
 2x Not Specified Not Specified  
 2x Samsung M391A2K43BB1-CPB 16 GB 2 rank 2133 MHz  
  
(End of data from sysinfo program)
```



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hammer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jan 26 15:12:06 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 26 January 2016.