



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F , Intel Xeon E3-1230 v5)

SPECint®2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

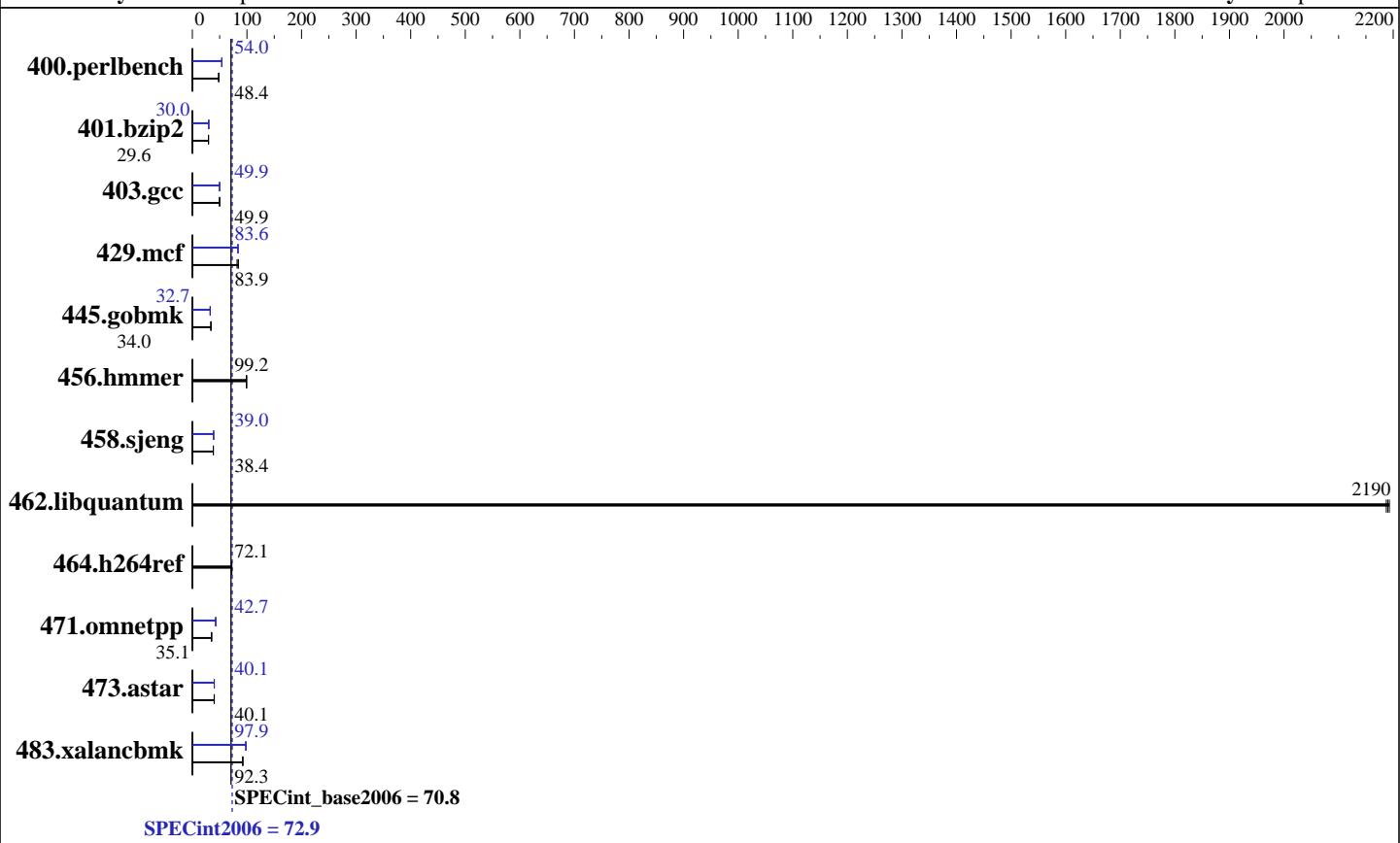
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E3-1230 v5
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)
Disk Subsystem: 1 x 600 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F, Intel Xeon E3-1230 v5)

SPECint2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

Test date: Dec-2015

Test sponsor: Supermicro

Hardware Availability: Oct-2015

Tested by: Supermicro

Software Availability: Sep-2015

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	202	48.5	202	48.4	203	48.1	182	53.8	181	54.0	181	54.0
401.bzip2	327	29.5	326	29.6	326	29.6	322	30.0	321	30.1	321	30.0
403.gcc	161	50.0	161	49.9	162	49.8	161	49.9	161	49.9	162	49.7
429.mcf	109	83.9	108	84.2	111	81.9	109	83.6	109	83.5	109	83.6
445.gobmk	308	34.0	308	34.0	308	34.0	321	32.7	321	32.7	321	32.7
456.hmmer	94.1	99.1	93.8	99.4	94.1	99.2	94.1	99.1	93.8	99.4	94.1	99.2
458.sjeng	315	38.4	315	38.4	315	38.4	310	39.0	310	39.0	311	38.9
462.libquantum	9.45	2190	9.46	2190	9.47	2190	9.45	2190	9.46	2190	9.47	2190
464.h264ref	307	72.1	307	72.1	306	72.4	307	72.1	307	72.1	306	72.4
471.omnetpp	178	35.1	176	35.5	178	35.0	146	42.9	147	42.6	146	42.7
473.astar	176	39.8	175	40.1	175	40.2	175	40.0	175	40.1	174	40.3
483.xalancbmk	74.8	92.3	74.8	92.3	74.5	92.6	70.2	98.2	70.4	97.9	70.5	97.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Hyper-threading = Disabled

Sysinfo program /home/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Dec 21 09:06:21 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1230 v5 @ 3.40GHz
1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4
siblings : 4

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F , Intel Xeon E3-1230 v5)

SPECint2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015

Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:       32603060 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.1 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.1"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 21 09:05

SPEC is set to: /home/cpu2006
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   412G  164G  248G  40%  /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0b 11/20/2015
Memory:
 4x Micron 16ATF1G64AZ-2G1A2 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "4"

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F , Intel Xeon E3-1230 v5)

SPECint2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015

General Notes (Continued)

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F , Intel Xeon E3-1230 v5)

SPECint2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

Test date: Dec-2015

Test sponsor: Supermicro

Hardware Availability: Oct-2015

Tested by: Supermicro

Software Availability: Sep-2015

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch

-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div

-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc

-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel

-opt-prefetch -auto-p32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F , Intel Xeon E3-1230 v5)

SPECint2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Oct-2015

Software Availability: Sep-2015

Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5019S-L
(X11SSL-F , Intel Xeon E3-1230 v5)

SPECint2006 = 72.9

SPECint_base2006 = 70.8

CPU2006 license: 001176

Test date: Dec-2015

Test sponsor: Supermicro

Hardware Availability: Oct-2015

Tested by: Supermicro

Software Availability: Sep-2015

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jan 12 15:46:17 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 January 2016.