SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

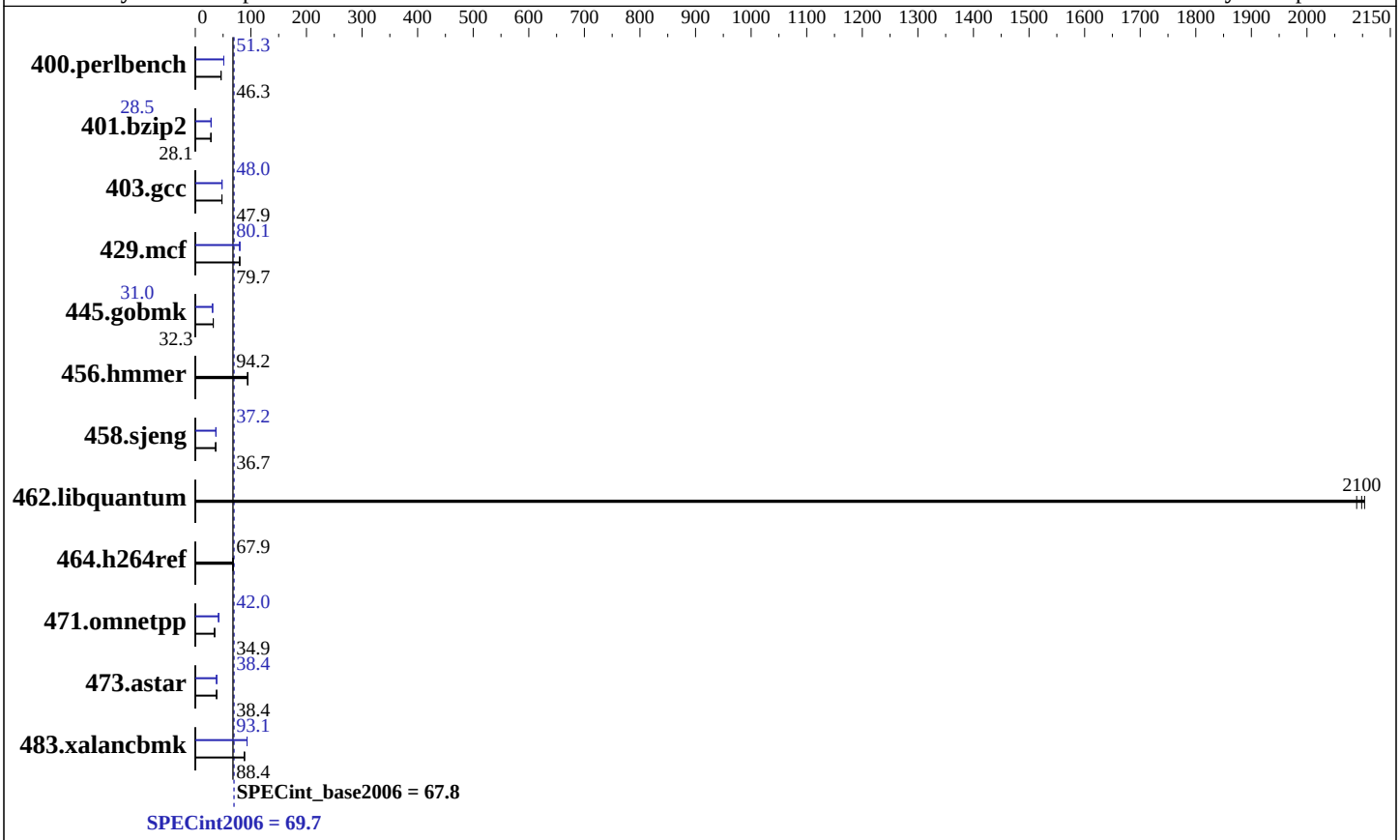
Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i7-6700T)

SPECint®2006 = 69.7

SPECint_base2006 = 67.8

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2015
Hardware Availability: Sep-2015
Software Availability: Sep-2015



Hardware

CPU Name:	Intel Core i7-6700T
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 1Rx8 PC4-2133P-U)
Disk Subsystem:	1 x 400 GB SATA III SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.e17.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i7-6700T)

SPECint2006 = 69.7

SPECint_base2006 = 67.8

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2015
Hardware Availability: Sep-2015
Software Availability: Sep-2015

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	211	46.4	**211**	**46.3**	211	46.3	**190**	**51.3**	191	51.3	190	51.3
401.bzip2	343	28.1	342	28.2	**343**	**28.1**	339	28.5	339	28.5	**339**	**28.5**
403.gcc	168	48.0	**168**	**47.9**	168	47.9	168	48.0	167	48.1	**168**	**48.0**
429.mcf	114	80.3	**114**	**79.7**	115	79.6	**114**	**80.1**	114	79.7	114	80.2
445.gobmk	325	32.3	**325**	**32.3**	325	32.3	338	31.0	**338**	**31.0**	338	31.0
456.hmmer	98.9	94.4	99.1	94.1	**99.0**	**94.2**	98.9	94.4	99.1	94.1	**99.0**	**94.2**
458.sjeng	330	36.7	330	36.7	**330**	**36.7**	326	37.1	325	37.2	**326**	**37.2**
462.libquantum	9.85	2100	9.92	2090	**9.87**	**2100**	9.85	2100	9.92	2090	**9.87**	**2100**
464.h264ref	326	67.9	**326**	**67.9**	327	67.7	326	67.9	**326**	**67.9**	327	67.7
471.omnetpp	182	34.3	179	34.9	**179**	**34.9**	**149**	**42.0**	149	41.9	149	42.0
473.astar	184	38.2	183	38.4	**183**	**38.4**	183	38.5	**183**	**38.4**	184	38.2
483.xalancbmk	78.4	88.0	77.7	88.8	**78.0**	**88.4**	74.1	93.1	74.1	93.1	**74.1**	**93.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
As tested, the system used a Supermicro CSE-732G-903B chassis.
The chassis is configured with a PWS-903-PQ power supply, 1 SNK-P0051AP4 heatsink, as well as 1 FAN-0124L4 rear cooling fan.
BIOS Settings:
Hyper-threading = Disabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Thu Nov 26 10:05:52 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Core(TM) i7-6700T CPU @ 2.80GHz
      1 "physical id"s (chips)
      4 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

Continued on next page

Platform Notes (Continued)

```
        cpu cores : 4
        siblings  : 4
        physical 0: cores 0 1 2 3
     cache size : 8192 KB

  From /proc/meminfo
     MemTotal:       16169696 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.1 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="7.1"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
        ANSI_COLOR="0;31"
        CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
     redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
     system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

  uname -a:
     Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
     EST 2015 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Nov 26 10:03

  SPEC is set to: /usr/cpu2006
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda2      xfs   369G   24G  346G   7% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS American Megatrends Inc. T20151015150001 10/15/2015
    Memory:
     4x Micron 8ATF51264AZ-2G1A2 4 GB 1 rank 2133 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
```

Continued on next page

General Notes (Continued)

```
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

Continued on next page

Base Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

445.gobmk: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-opt-prefetch`
`-ansi-alias`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Dec 16 10:40:14 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 December 2015.