SPEC® CFP2006 Result

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ACTION S.A.

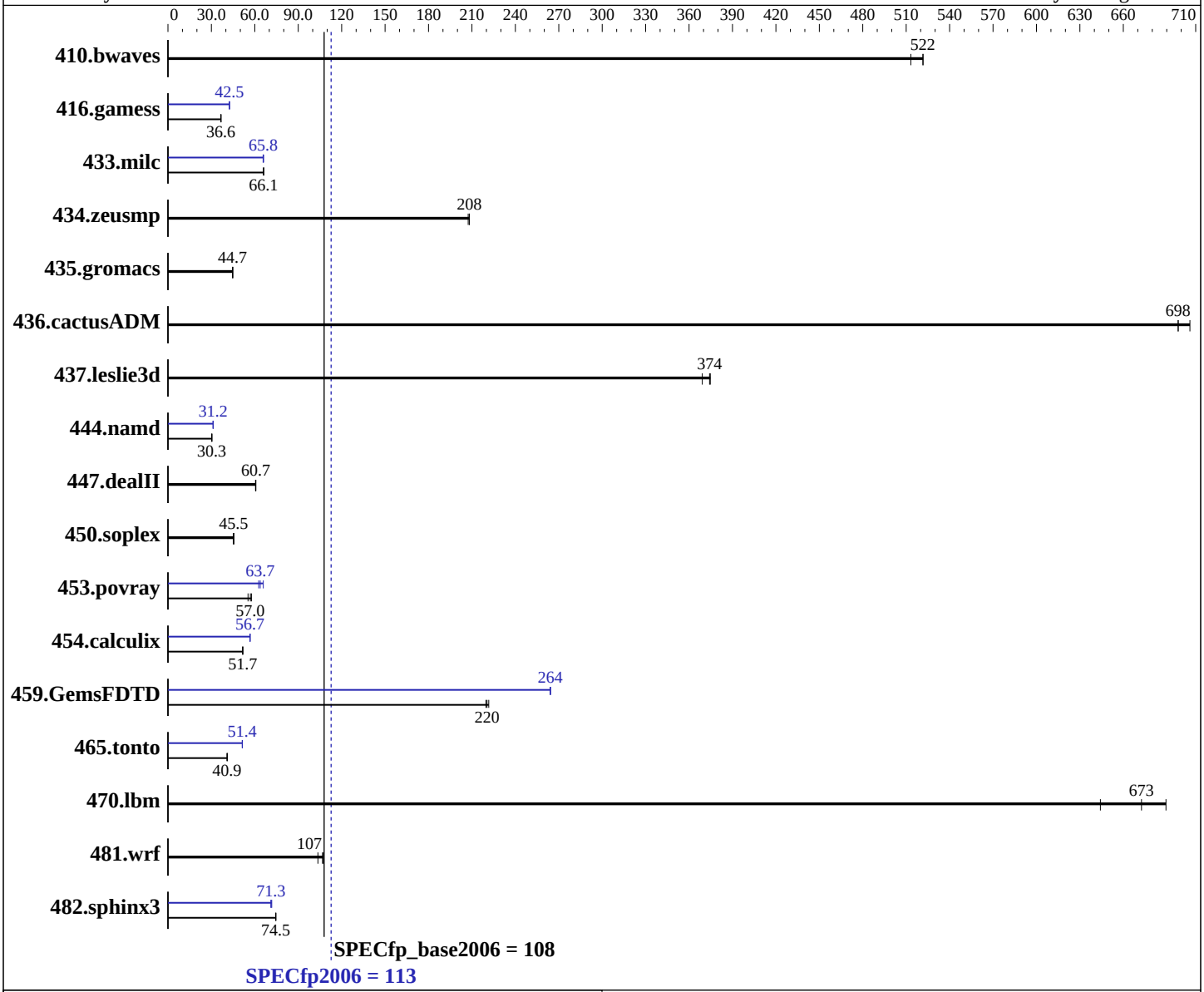
ACTINA SOLAR 210 X6 (Intel Xeon E5-2697 v3, 2.60 GHz)

SPECfp®2006 = 113

SPECfp_base2006 = 108

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Nov-2015
Hardware Availability: Sep-2014
Software Availability: Aug-2015



Hardware

CPU Name:	Intel Xeon E5-2697 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	28 cores, 2 chips, 14 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.1 (Maipo) 3.10.0-229.11.1.el7.x86_64
Compiler:	C/C++: Version 16.0.0.047 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.047 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	35 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 240 GB SATA II SSD
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.5	513	26.0	522	**26.1**	**522**	26.5	513	26.0	522	**26.1**	**522**
416.gamess	534	36.7	536	36.5	**535**	**36.6**	**461**	**42.5**	461	42.5	461	42.5
433.milc	139	66.1	139	66.1	**139**	**66.1**	140	65.7	**140**	**65.8**	139	66.1
434.zeusmp	**43.7**	**208**	43.9	207	43.7	208	**43.7**	**208**	43.9	207	43.7	208
435.gromacs	160	44.7	160	44.8	**160**	**44.7**	160	44.7	160	44.8	**160**	**44.7**
436.cactusADM	**17.1**	**698**	16.9	706	17.1	698	**17.1**	**698**	16.9	706	17.1	698
437.leslie3d	25.5	369	**25.1**	**374**	25.1	375	25.5	369	**25.1**	**374**	25.1	375
444.namd	**265**	**30.3**	264	30.3	265	30.3	**257**	**31.2**	257	31.1	257	31.2
447.dealII	**189**	**60.7**	189	60.5	188	60.7	**189**	**60.7**	189	60.5	188	60.7
450.soplex	183	45.5	**183**	**45.5**	185	45.1	183	45.5	**183**	**45.5**	185	45.1
453.povray	**93.4**	**57.0**	96.2	55.3	92.2	57.7	80.8	65.9	**83.5**	**63.7**	84.9	62.7
454.calculix	160	51.6	159	51.8	**160**	**51.7**	145	56.8	146	56.7	**146**	**56.7**
459.GemsFDTD	48.3	220	**48.1**	**220**	47.9	222	40.2	264	40.1	264	**40.2**	**264**
465.tonto	**241**	**40.9**	239	41.1	241	40.8	**192**	**51.4**	192	51.3	191	51.4
470.lbm	19.9	689	21.3	644	**20.4**	**673**	19.9	689	21.3	644	**20.4**	**673**
481.wrf	108	104	**105**	**107**	104	107	108	104	**105**	**107**	104	107
482.sphinx3	261	74.6	**262**	**74.5**	262	74.4	272	71.7	274	71.1	**273**	**71.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Bios Settings:
Intel(R) Hyper-Threading Tech = Disabled
Power & Performance = Performance
Enforce POR = Disabled
Memory Operating Speed Selection = 2133
Cluster-on-Die = Disabled
Set Fan Profile = Performance
Fan PWM Offset = 0
```

Continued on next page

Platform Notes (Continued)

```
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Thu Nov  5 19:58:09 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2697 v3 @ 2.60GHz
      2 "physical id"s (chips)
      28 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 14
      siblings  : 14
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
   cache size : 35840 KB

From /proc/meminfo
   MemTotal:       263873456 kB
   HugePages_Total:       1
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   os-release.rpmnew:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.1 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.1"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

uname -a:
   Linux SUT 3.10.0-229.11.1.el7.x86_64 #5 SMP Mon Sep 14 17:11:19 CEST 2015
   x86_64 x86_64 x86_64 GNU/Linux
```

Continued on next page

Platform Notes (Continued)

```
 run-level 3 Nov 5 14:44

 SPEC is set to: /cpu2006.1.2
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda1      ext4  212G   38G  163G  19% /

 Additional information from dmidecode:
   BIOS Intel Corporation SE5C610.86B.01.01.0009.060120151350 06/01/2015
   Memory:
    16x   16 GB
    16x Micron 36ASF2G72PZ-2G1A2 16 GB 2134 MHz 2 rank
    8x NO DIMM NO DIMM

 (End of data from sysinfo program)
 dmidecode does not properly detect memory modules
 16 modules of 16 GB were used to run the test (256 GB total)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"
OMP_NUM_THREADS = "28"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory
 using RedHat EL 7.1
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

459.GemsFDTD: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2` `-inline-level=0 -opt-prefetch -parallel`

465.tonto: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-inline-calloc -opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.xml`

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