



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 107

Huawei CH226 V3 (Intel Xeon E5-2640 v3)

SPECfp\_base2006 = 102

CPU2006 license: 3175

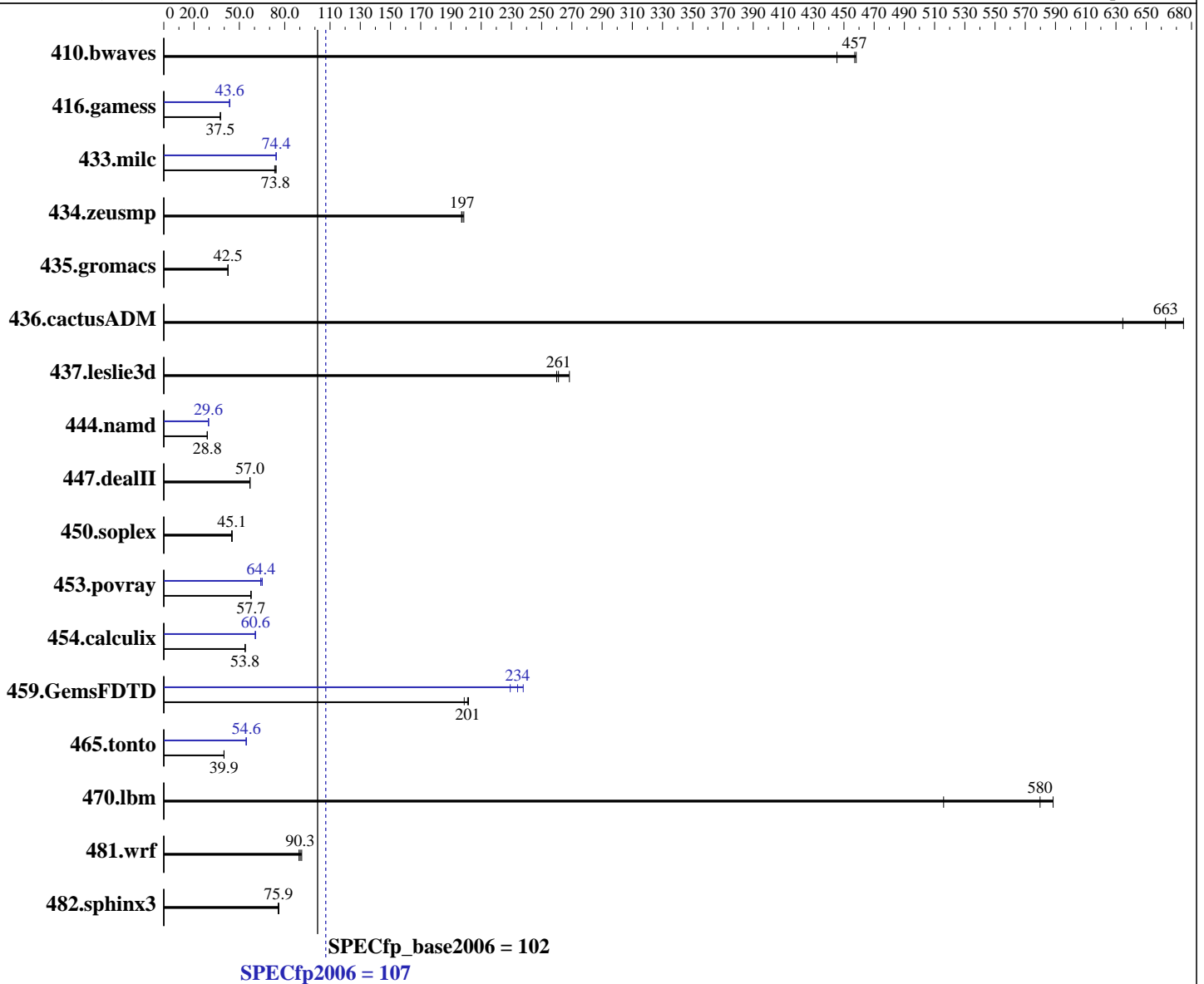
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



**Hardware**

CPU Name: Intel Xeon E5-2640 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	30.5	445	<b>29.7</b>	<b>457</b>	29.7	458	30.5	445	<b>29.7</b>	<b>457</b>	29.7	458
416.gamess	522	37.5	523	37.4	<b>522</b>	<b>37.5</b>	450	43.5	<b>450</b>	<b>43.6</b>	449	43.6
433.milc	125	73.4	<b>124</b>	<b>73.8</b>	123	74.4	123	74.4	<b>123</b>	<b>74.4</b>	124	74.3
434.zeusmp	45.8	199	46.2	197	<b>46.1</b>	<b>197</b>	45.8	199	46.2	197	<b>46.1</b>	<b>197</b>
435.gromacs	<b>168</b>	<b>42.5</b>	168	42.5	168	42.4	<b>168</b>	<b>42.5</b>	168	42.5	168	42.4
436.cactusADM	17.7	675	<b>18.0</b>	<b>663</b>	18.8	635	17.7	675	<b>18.0</b>	<b>663</b>	18.8	635
437.leslie3d	35.0	268	36.2	260	<b>36.0</b>	<b>261</b>	35.0	268	36.2	260	<b>36.0</b>	<b>261</b>
444.namd	278	28.8	279	28.8	<b>279</b>	<b>28.8</b>	271	29.6	<b>271</b>	<b>29.6</b>	271	29.6
447.dealII	200	57.1	201	56.9	<b>201</b>	<b>57.0</b>	200	57.1	201	56.9	<b>201</b>	<b>57.0</b>
450.soplex	184	45.2	186	44.9	<b>185</b>	<b>45.1</b>	184	45.2	186	44.9	<b>185</b>	<b>45.1</b>
453.povray	<b>92.2</b>	<b>57.7</b>	92.4	57.6	92.1	57.8	83.0	64.1	81.6	65.2	<b>82.6</b>	<b>64.4</b>
454.calculix	153	53.7	<b>153</b>	<b>53.8</b>	153	54.0	<b>136</b>	<b>60.6</b>	136	60.7	136	60.5
459.GemsFDTD	52.6	202	<b>52.8</b>	<b>201</b>	53.4	199	44.6	238	46.3	229	<b>45.3</b>	<b>234</b>
465.tonto	247	39.9	247	39.8	<b>247</b>	<b>39.9</b>	180	54.7	181	54.5	<b>180</b>	<b>54.6</b>
470.lbm	<b>23.7</b>	<b>580</b>	23.4	588	26.6	516	<b>23.7</b>	<b>580</b>	23.4	588	26.6	516
481.wrf	125	89.5	<b>124</b>	<b>90.3</b>	122	91.2	125	89.5	<b>124</b>	<b>90.3</b>	122	91.2
482.sphinx3	<b>257</b>	<b>75.9</b>	256	76.1	258	75.7	<b>257</b>	<b>75.9</b>	256	76.1	258	75.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Snoop Mode to HS mode  
Set Patrol Scrub to Disable  
Set Hyper-Threading to Disable  
Sysinfo program /spec/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Mon Aug 31 15:13:23 2015

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 v3 @ 2.60GHz
 2 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      263579372 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 31 15:12

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  443G   67G  353G  16% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS Insyde Corp. 1.36 04/09/2015

Memory:

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz, configured at 1867 MHz

8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1867 MHz

8x NO DIMM NO DIMM 3 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

## Peak Portability Flags

Same as Base Portability Flags



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Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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