



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 107

Huawei CH226 V3 (Intel Xeon E5-2637 v3)

SPECfp_base2006 = 104

CPU2006 license: 3175

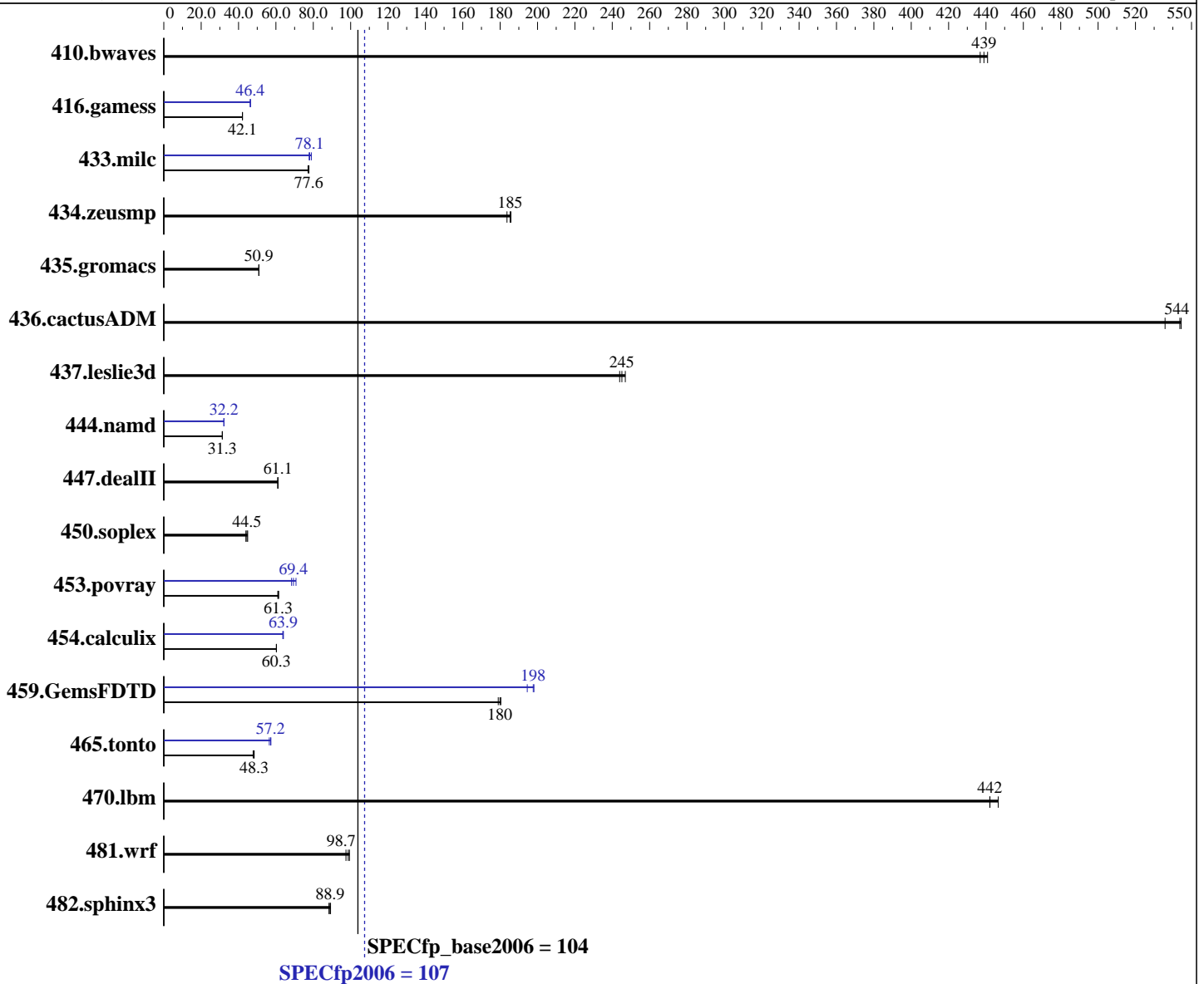
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2637 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

Continued on next page



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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 256 GB SATA, SSD
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>31.0</u>	<u>439</u>	31.1	437	30.8	441	<u>31.0</u>	<u>439</u>	31.1	437	30.8	441
416.gamess	<u>465</u>	<u>42.1</u>	466	42.1	465	42.1	423	46.3	422	46.4	<u>422</u>	<u>46.4</u>
433.milc	118	77.7	119	77.2	<u>118</u>	<u>77.6</u>	116	79.0	<u>118</u>	<u>78.1</u>	118	77.8
434.zeusmp	49.5	184	49.0	186	<u>49.1</u>	<u>185</u>	49.5	184	49.0	186	<u>49.1</u>	<u>185</u>
435.gromacs	<u>140</u>	<u>50.9</u>	140	50.8	140	50.9	<u>140</u>	<u>50.9</u>	140	50.8	140	50.9
436.cactusADM	21.9	544	<u>22.0</u>	<u>544</u>	22.3	536	21.9	544	<u>22.0</u>	<u>544</u>	22.3	536
437.leslie3d	38.5	244	38.1	247	<u>38.3</u>	<u>245</u>	38.5	244	38.1	247	<u>38.3</u>	<u>245</u>
444.namd	<u>256</u>	<u>31.3</u>	256	31.3	256	31.3	249	32.2	<u>249</u>	<u>32.2</u>	249	32.2
447.dealII	<u>187</u>	<u>61.1</u>	187	61.0	187	61.1	<u>187</u>	<u>61.1</u>	187	61.0	187	61.1
450.soplex	190	43.9	<u>187</u>	<u>44.5</u>	186	44.9	190	43.9	<u>187</u>	<u>44.5</u>	186	44.9
453.povray	<u>86.8</u>	<u>61.3</u>	86.6	61.5	87.2	61.0	77.8	68.4	<u>76.7</u>	<u>69.4</u>	75.2	70.8
454.calculix	<u>137</u>	<u>60.3</u>	137	60.3	137	60.2	129	64.0	<u>129</u>	<u>63.9</u>	130	63.7
459.GemsFDTD	<u>59.0</u>	<u>180</u>	58.8	180	59.3	179	53.6	198	54.5	195	<u>53.7</u>	<u>198</u>
465.tonto	<u>204</u>	<u>48.3</u>	206	47.8	204	48.4	174	56.5	172	57.3	<u>172</u>	<u>57.2</u>
470.lbm	31.1	442	30.8	447	<u>31.1</u>	<u>442</u>	31.1	442	30.8	447	<u>31.1</u>	<u>442</u>
481.wrf	115	97.5	112	99.4	<u>113</u>	<u>98.7</u>	115	97.5	112	99.4	<u>113</u>	<u>98.7</u>
482.sphinx3	<u>219</u>	<u>88.9</u>	221	88.4	218	89.2	<u>219</u>	<u>88.9</u>	221	88.4	218	89.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to ES
 Set Hyper-Threading to Disabled
 Set Patrol Scrub to Disable
 Baseboard Management Controller used to adjust the fan speed to 100%
 Sysinfo program /spec/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost.localdomain Mon Aug 31 15:09:10 2015

Continued on next page



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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2637 v3 @ 3.50GHz
 2 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 4
  siblings     : 4
  physical 0   : cores 0 1 4 5
  physical 1   : cores 0 1 4 5
cache size    : 15360 KB

```

```

From /proc/meminfo
MemTotal:      263580296 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 31 13:47

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb1       ext4  443G   54G  367G  13% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS Insyde Corp. 1.39 05/06/2015

Memory:

8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40DB0-CPB 16 GB 1 rank 2133 MHz
8x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64

Continued on next page



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Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page



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Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Sep 23 11:03:58 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 22 September 2015.