



# SPEC<sup>®</sup> CFP2006 Result

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## Lenovo Group Limited

### SPECfp<sup>®</sup>\_rate2006 = 155

Lenovo System x3100 M5  
(Intel Xeon E3-1285 v3, 4.00 GHz)

### SPECfp\_rate\_base2006 = 150

CPU2006 license: 9017

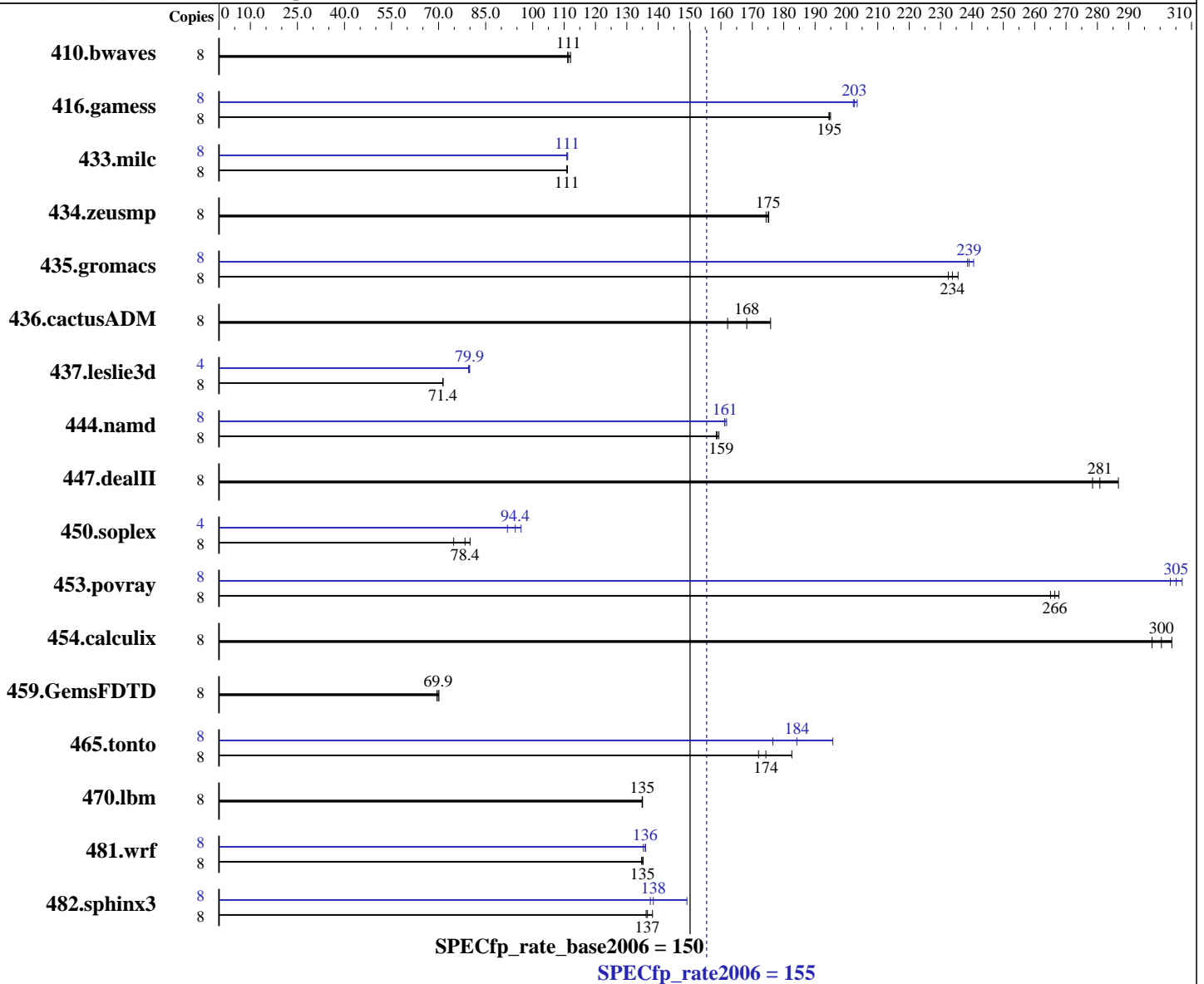
Test date: Aug-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: May-2014

Tested by: Lenovo Group Limited

Software Availability: Jun-2014



### Hardware

CPU Name: Intel Xeon E3-1285 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
 3.10.0-229.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (4 x 4 GB 2Rx8 PC3L-12800E-11, ECC)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	970	112	<b>976</b>	<b>111</b>	978	111	8	970	112	<b>976</b>	<b>111</b>	978	111
416.gamess	8	806	194	804	195	<b>805</b>	<b>195</b>	8	<b>773</b>	<b>203</b>	775	202	770	203
433.milc	8	661	111	662	111	<b>662</b>	<b>111</b>	8	661	111	662	111	<b>662</b>	<b>111</b>
434.zeusmp	8	<b>416</b>	<b>175</b>	415	175	417	174	8	<b>416</b>	<b>175</b>	415	175	417	174
435.gromacs	8	<b>244</b>	<b>234</b>	242	236	246	232	8	237	241	239	239	<b>239</b>	<b>239</b>
436.cactusADM	8	544	176	<b>568</b>	<b>168</b>	590	162	8	544	176	<b>568</b>	<b>168</b>	590	162
437.leslie3d	8	1054	71.4	1053	71.4	<b>1053</b>	<b>71.4</b>	4	473	79.5	<b>471</b>	<b>79.9</b>	471	79.9
444.namd	8	405	159	<b>404</b>	<b>159</b>	403	159	8	397	162	<b>398</b>	<b>161</b>	398	161
447.dealII	8	319	287	<b>326</b>	<b>281</b>	329	278	8	319	287	<b>326</b>	<b>281</b>	329	278
450.soplex	8	<b>851</b>	<b>78.4</b>	892	74.8	834	80.0	4	<b>353</b>	<b>94.4</b>	346	96.3	363	91.9
453.povray	8	161	265	159	268	<b>160</b>	<b>266</b>	8	139	307	<b>139</b>	<b>305</b>	140	303
454.calculix	8	217	304	222	297	<b>220</b>	<b>300</b>	8	217	304	222	297	<b>220</b>	<b>300</b>
459.GemsFDTD	8	1222	69.5	<b>1214</b>	<b>69.9</b>	1211	70.1	8	1222	69.5	<b>1214</b>	<b>69.9</b>	1211	70.1
465.tonto	8	458	172	431	183	<b>452</b>	<b>174</b>	8	402	196	446	177	<b>427</b>	<b>184</b>
470.lbm	8	<b>814</b>	<b>135</b>	815	135	814	135	8	<b>814</b>	<b>135</b>	815	135	814	135
481.wrf	8	661	135	663	135	<b>662</b>	<b>135</b>	8	657	136	661	135	<b>658</b>	<b>136</b>
482.sphinx3	8	1145	136	1128	138	<b>1142</b>	<b>137</b>	8	1045	149	1134	137	<b>1126</b>	<b>138</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS setting:  
Operating Mode set to "Efficiency-Favor Performance"  
Sysinfo program /root/cpu2006\_icl5/config/sysinfo.rev6914

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### Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on x3100m5.labs.lenovo.com Tue Aug 25 16:24:11 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1285 v3 @ 3.60GHz
 1 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
 cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal: 16100780 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux x3100m5.labs.lenovo.com 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29
18:37:38 EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 25 07:06
```

```
SPEC is set to: /root/cpu2006_ic15
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs 50G 25G 26G 49% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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## Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[J9E113LUS-1.05]- 07/06/2014

Memory:

4x Hynix/Hyundai HMT351U7EFR8A-PB 4 GB 2 rank 1600 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006\_ic15/libs/32:/root/cpu2006\_ic15/libs/64:/root/cpu2006\_ic15/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB  
memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

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## Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

### C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -prof-gen(pass 1) -ipo -O3 -no-prec-div
            -prof-use(pass 2) -unroll2

```

### C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
         -ansi-alias

```

### Fortran benchmarks:

```

410.bwaves: basepeak = yes

```

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## Peak Optimization Flags (Continued)

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.20150909.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.20150909.xml>

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