



# SPEC® CFP2006 Result

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## Lenovo Group Limited

Lenovo Flex System x240 M5  
(Intel Xeon E5-2603 v3, 1.60 GHz)

**SPECfp®\_rate2006 = 282**

**SPECfp\_rate\_base2006 = 276**

CPU2006 license: 9017

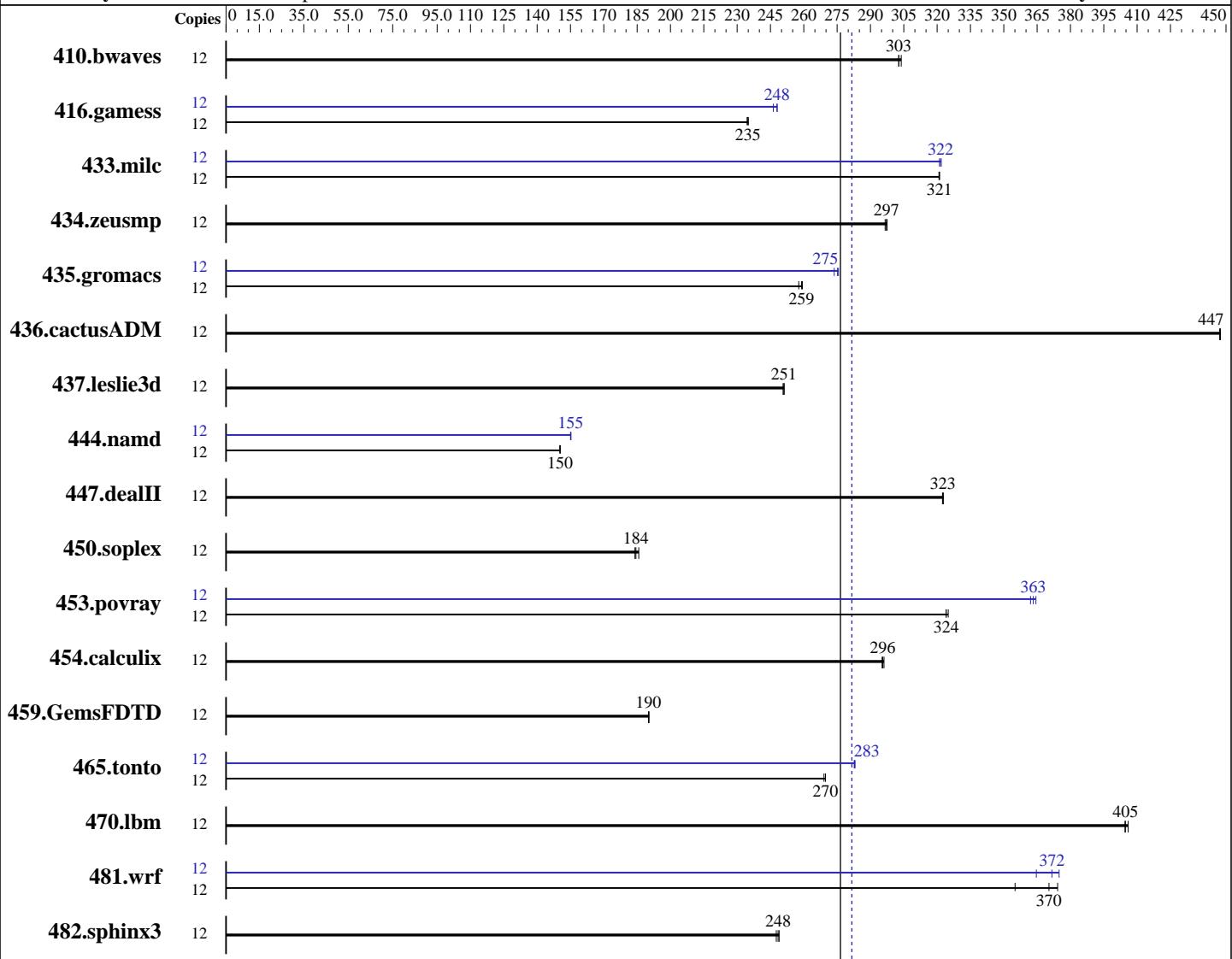
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Aug-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013



**SPECfp\_rate\_base2006 = 276**

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### Hardware

CPU Name:	Intel Xeon E5-2603 v3
CPU Characteristics:	
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

### Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	1 x 300 GB SAS, 10000 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	537	304	539	303	<b><u>539</u></b>	<b><u>303</u></b>	12	537	304	539	303	<b><u>539</u></b>	<b><u>303</u></b>		
416.gamess	12	1000	235	1002	234	<b><u>1001</u></b>	<b><u>235</u></b>	12	954	246	<b><u>948</u></b>	<b><u>248</u></b>	947	248		
433.milc	12	343	321	343	321	<b><u>343</u></b>	<b><u>321</u></b>	12	<b><u>343</u></b>	<b><u>322</u></b>	343	321	342	322		
434.zeusmp	12	367	297	<b><u>367</u></b>	<b><u>297</u></b>	368	297	12	367	297	<b><u>367</u></b>	<b><u>297</u></b>	368	297		
435.gromacs	12	332	258	330	259	<b><u>331</u></b>	<b><u>259</u></b>	12	311	276	<b><u>311</u></b>	<b><u>275</u></b>	313	274		
436.cactusADM	12	<b><u>321</u></b>	<b><u>447</u></b>	321	447	320	447	12	<b><u>321</u></b>	<b><u>447</u></b>	321	447	320	447		
437.leslie3d	12	450	251	<b><u>449</u></b>	<b><u>251</u></b>	449	251	12	450	251	<b><u>449</u></b>	<b><u>251</u></b>	449	251		
444.namd	12	641	150	640	150	<b><u>640</u></b>	<b><u>150</u></b>	12	621	155	620	155	<b><u>620</u></b>	<b><u>155</u></b>		
447.dealII	12	425	323	<b><u>425</u></b>	<b><u>323</u></b>	426	322	12	425	323	<b><u>425</u></b>	<b><u>323</u></b>	426	322		
450.soplex	12	544	184	539	186	<b><u>543</u></b>	<b><u>184</u></b>	12	544	184	539	186	<b><u>543</u></b>	<b><u>184</u></b>		
453.povray	12	<b><u>197</u></b>	<b><u>324</u></b>	196	325	197	324	12	176	362	<b><u>176</u></b>	<b><u>363</u></b>	175	364		
454.calculix	12	334	296	<b><u>335</u></b>	<b><u>296</u></b>	335	295	12	334	296	<b><u>335</u></b>	<b><u>296</u></b>	335	295		
459.GemsFDTD	12	<b><u>670</u></b>	<b><u>190</u></b>	670	190	669	190	12	<b><u>670</u></b>	<b><u>190</u></b>	670	190	669	190		
465.tonto	12	439	269	438	270	<b><u>438</u></b>	<b><u>270</u></b>	12	417	283	<b><u>418</u></b>	<b><u>283</u></b>	418	283		
470.lbm	12	<b><u>407</u></b>	<b><u>405</u></b>	406	406	408	405	12	<b><u>407</u></b>	<b><u>405</u></b>	406	406	408	405		
481.wrf	12	<b><u>362</u></b>	<b><u>370</u></b>	378	355	358	374	12	<b><u>361</u></b>	<b><u>372</u></b>	368	365	358	375		
482.sphinx3	12	940	249	945	248	<b><u>941</u></b>	<b><u>248</u></b>	12	940	249	945	248	<b><u>941</u></b>	<b><u>248</u></b>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS

Enabled COD Preference in BIOS

Disable Early Snoop Preference in BIOS

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## Platform Notes (Continued)

```
Sysinfo program /cpu2006.1.2_14.0_jan2014/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on newport-rhel6.5 Sat Aug 15 10:27:21 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings : 6
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
    cache size : 15360 KB
```

```
From /proc/meminfo
    MemTotal:      264124364 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux newport-rhel6.5 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 14 23:19 last=5
```

```
SPEC is set to: /cpu2006.1.2_14.0_jan2014
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_newportrhel6-lv_root ext4  265G   86G  166G  35% /
```

Additional information from dmidecode:

BIOS IBM -[C4E105JUS-1.10]- 04/11/2015

Memory:

8x NO DIMM Unknown

16x Samsung M393A2G40DB0-CPB 16 GB 1600 MHz 2 rank

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2\_14.0\_jan2014/libs/32:/cpu2006.1.2\_14.0\_jan2014/libs/64:/cpu2006.1.2\_14.0\_jan2014/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32
```

```
470.lbm: basepeak = yes
```

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## Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2)
            -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
            -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-BB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-BB.xml>

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