SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

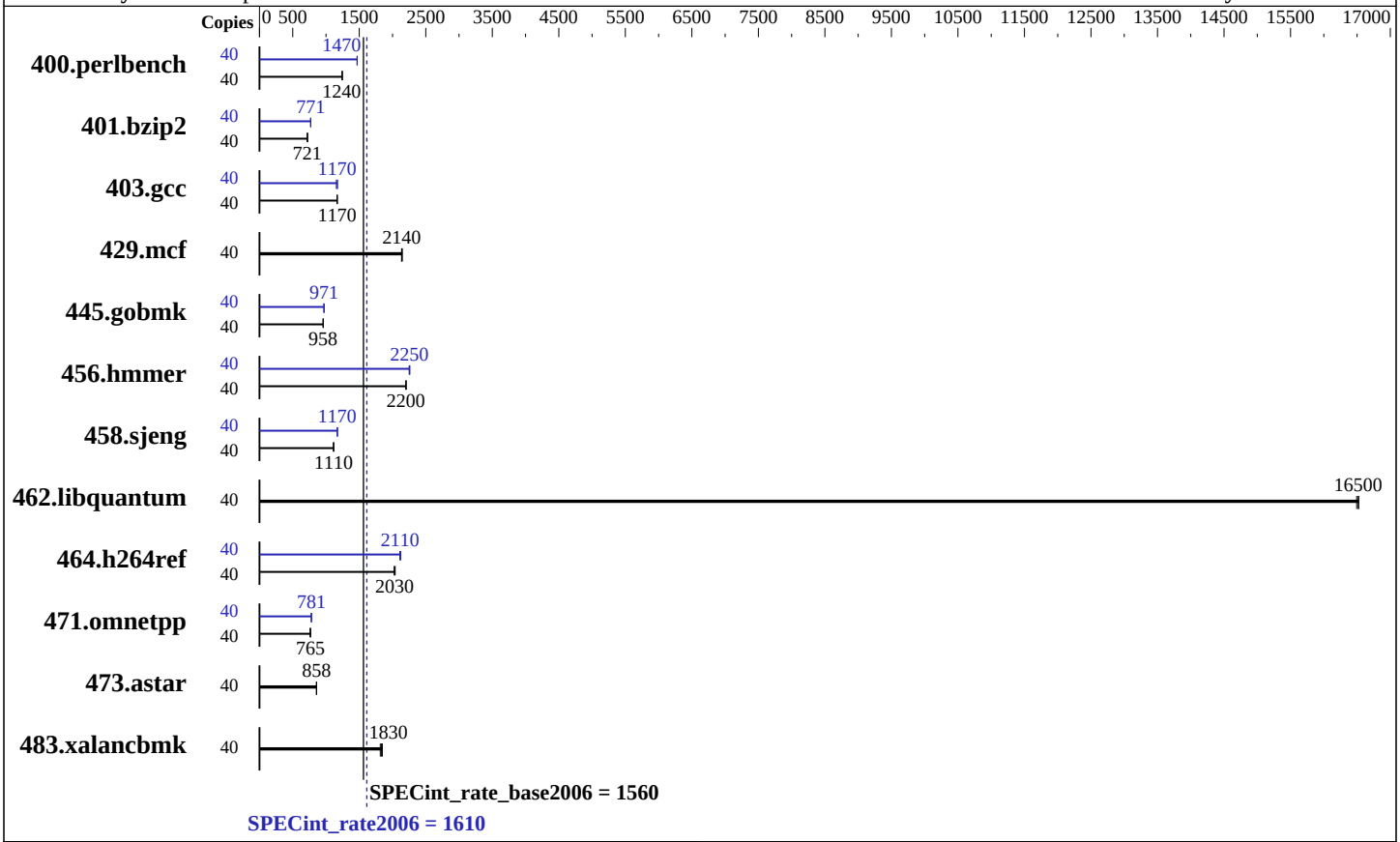
SuperServer 2048U-RTR4
(X10QRH+ , Intel Xeon E5-4627 v3)

SPECint®_rate2006 = 1610

SPECint_rate_base2006 = 1560

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014



Hardware

CPU Name:	Intel Xeon E5-4627 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 512 GB SATA III, SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12, Kernel 3.12.28-4-default
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+ , Intel Xeon E5-4627 v3)

SPECint_rate2006 = 1610

SPECint_rate_base2006 = 1560

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	314	1240	**314**	**1240**	314	1240	40	**266**	**1470**	266	1470	266	1470
401.bzip2	40	537	718	535	722	**535**	**721**	40	501	771	**501**	**771**	502	769
403.gcc	40	**276**	**1170**	275	1170	276	1170	40	**275**	**1170**	275	1170	279	1160
429.mcf	40	**171**	**2140**	171	2140	170	2150	40	**171**	**2140**	171	2140	170	2150
445.gobmk	40	**438**	**958**	438	958	438	958	40	432	971	433	970	**432**	**971**
456.hmmer	40	**169**	**2200**	169	2200	169	2200	40	**166**	**2250**	166	2250	165	2260
458.sjeng	40	434	1110	435	1110	**434**	**1110**	40	413	1170	414	1170	**413**	**1170**
462.libquantum	40	**50.2**	**16500**	50.2	16500	50.2	16500	40	**50.2**	**16500**	50.2	16500	50.2	16500
464.h264ref	40	**436**	**2030**	434	2040	437	2030	40	420	2110	**419**	**2110**	417	2120
471.omnetpp	40	326	768	328	763	**327**	**765**	40	320	782	321	778	**320**	**781**
473.astar	40	328	856	**327**	**858**	327	859	40	328	856	**327**	**858**	327	859
483.xalancbmk	40	152	1820	**150**	**1830**	149	1850	40	152	1820	**150**	**1830**	149	1850

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
COD Enable = Enable
Early Snoop = Disable
Enforce POR = Disabled
Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on 21-234 Wed Jun 17 09:39:06 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4627 v3 @ 2.60GHz
      4 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
```

Continued on next page

Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 10
         siblings  : 10
         physical 0: cores 0 1 2 3 4 8 9 10 11 12
         physical 1: cores 0 1 2 3 4 8 9 10 11 12
         physical 2: cores 0 1 2 3 4 8 9 10 11 12
         physical 3: cores 0 1 2 3 4 8 9 10 11 12
      cache size : 25600 KB

   From /proc/meminfo
      MemTotal:       529336776 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      SUSE Linux Enterprise Server 12

   From /etc/*release* /etc/*version*
      SuSE-release:
         SUSE Linux Enterprise Server 12 (x86_64)
         VERSION = 12
         PATCHLEVEL = 0
         # This file is deprecated and will be removed in a future service pack or
         release.
         # Please check /etc/os-release for details about this release.
      os-release:
         NAME="SLES"
         VERSION="12"
         VERSION_ID="12"
         PRETTY_NAME="SUSE Linux Enterprise Server 12"
         ID="sles"
         ANSI_COLOR="0;32"
         CPE_NAME="cpe:/o:suse:sles:12"

   uname -a:
      Linux 21-234 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 5 Jun 17 09:34

   SPEC is set to: /home/SPEC2K6/SPEC2006-V12
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda3      ext4  458G  7.2G  450G   2% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS American Megatrends Inc. 1.00 06/01/2015
```

Continued on next page

Platform Notes (Continued)

```
      Memory:
       16x NO DIMM NO DIMM
       1x Samsung(data:13/44) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       1x Samsung(data:13/48) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       3x Samsung(data:13/51) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       2x Samsung(data:14/13) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       1x Samsung(data:14/16) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       5x Samsung(data:14/17) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       1x Samsung(data:14/25) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       14x Samsung(data:14/26) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
       4x Samsung(data:14/47) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

    (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Aug 6 13:25:35 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 August 2015.