# SPEC® CINT2006 Result



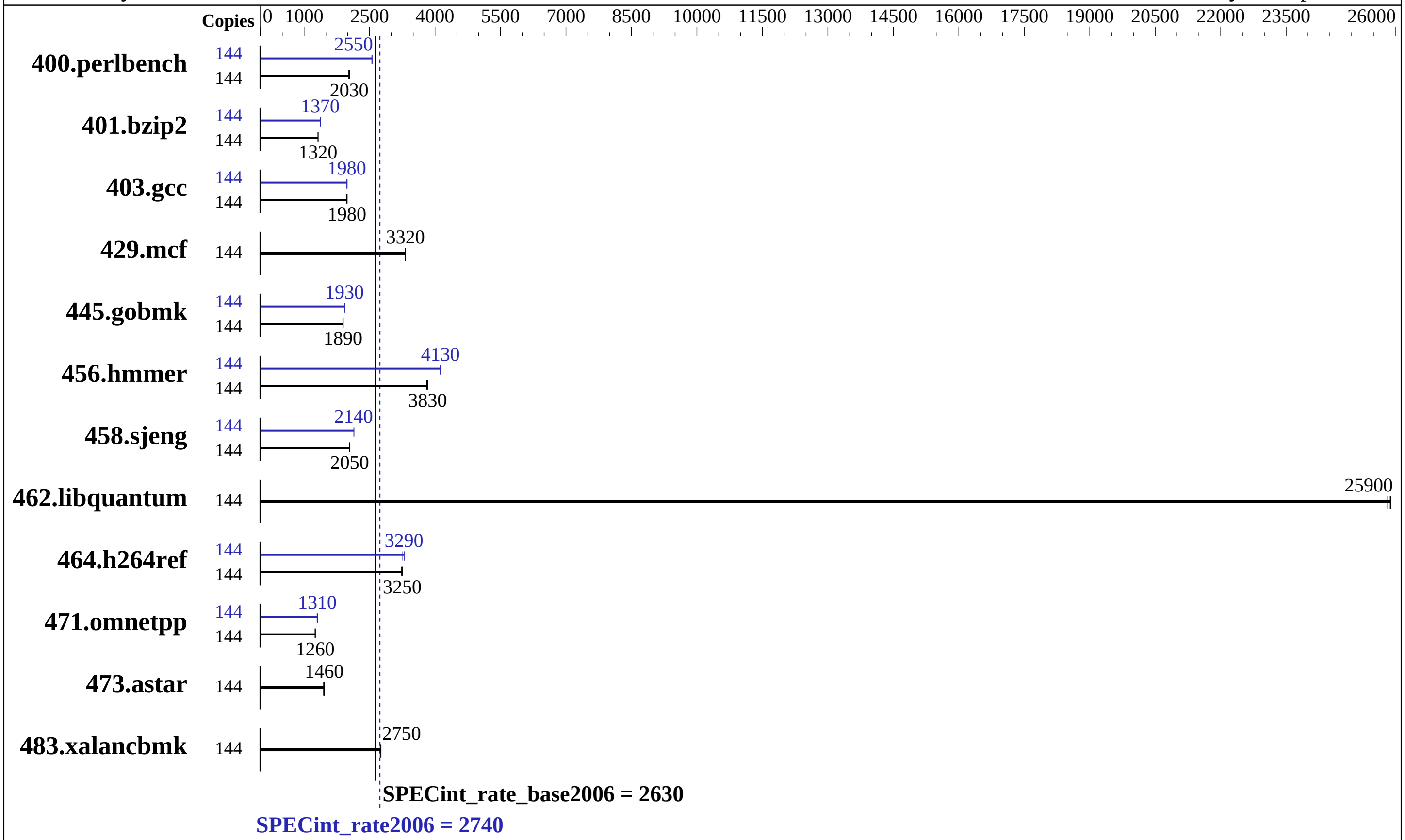
## Huawei

Huawei RH5885H V3 (Intel Xeon E7-8880 v3)

SPECint®_rate2006 = 2740

SPECint_rate_base2006 = 2630

**CPU2006 license:** 3175
**Test sponsor:** Huawei
**Tested by:** Huawei
**Test date:** Jun-2015
**Hardware Availability:** May-2015
**Software Availability:** Sep-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7-8880 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.10 GHz |
| CPU MHz: | 2300 |
| FPU: | Integrated |
| CPU(s) enabled: | 72 cores, 4 chips, 18 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 45 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz) |
| Disk Subsystem: | 3 x 300 GB SAS, 10K RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64 |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 144 | 695 | 2020 | 690 | 2040 | **692** | **2030** | 144 | 549 | 2560 | 551 | 2550 | **551** | **2550** |
| 401.bzip2 | 144 | **1052** | **1320** | 1056 | 1320 | 1050 | 1320 | 144 | **1013** | **1370** | 1016 | 1370 | 1012 | 1370 |
| 403.gcc | 144 | **584** | **1980** | 587 | 1980 | 583 | 1990 | 144 | **586** | **1980** | 589 | 1970 | 583 | 1990 |
| 429.mcf | 144 | 396 | 3320 | 395 | 3330 | **395** | **3320** | 144 | 396 | 3320 | 395 | 3330 | **395** | **3320** |
| 445.gobmk | 144 | 800 | 1890 | **798** | **1890** | 796 | 1900 | 144 | **784** | **1930** | 785 | 1920 | 784 | 1930 |
| 456.hmmer | 144 | **351** | **3830** | 350 | 3840 | 353 | 3810 | 144 | 325 | 4140 | **326** | **4130** | 326 | 4120 |
| 458.sjeng | 144 | 853 | 2040 | **852** | **2050** | 849 | 2050 | 144 | 814 | 2140 | 813 | 2140 | **813** | **2140** |
| 462.libquantum | 144 | 116 | 25800 | **115** | **25900** | 115 | 25900 | 144 | 116 | 25800 | **115** | **25900** | 115 | 25900 |
| 464.h264ref | 144 | 985 | 3230 | **981** | **3250** | 978 | 3260 | 144 | **968** | **3290** | 967 | 3290 | 982 | 3250 |
| 471.omnetpp | 144 | **716** | **1260** | 720 | 1250 | 714 | 1260 | 144 | **689** | **1310** | 688 | 1310 | 694 | 1300 |
| 473.astar | 144 | 699 | 1450 | **691** | **1460** | 690 | 1470 | 144 | 699 | 1450 | **691** | **1460** | 690 | 1470 |
| 483.xalancbmk | 144 | 362 | 2740 | **361** | **2750** | 359 | 2760 | 144 | 362 | 2740 | **361** | **2750** | 359 | 2760 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with:
 cpupower -c all frequency-set -g performance
```

## Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set DRAM Maintenace to Manual
Set DRAM Maintenace Mode to pTRR
Set Patrol Scrub to Enabled
Set Memory Power Saving to disabled
Set C-State to C0/C1
Set CPU C1E Support to Enabled
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on RH5885HV3 Wed Jun 17 13:16:24 2015

This section contains SUT (System Under Test) info as seen by
```

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## Platform Notes (Continued)

```
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-8880 v3 @ 2.30GHz
      4 "physical id"s (chips)
      144 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 18
      siblings  : 36
      physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
   cache size : 46080 KB

From /proc/meminfo
   MemTotal:       1056462228 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux RH5885HV3 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 17 13:11

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda3      xfs   537G   92G  445G  18% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.
```

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## Platform Notes (Continued)

```
    BIOS American Megatrends Inc. BLISV705 03/30/2015
    Memory:
     32x NO DIMM NO DIMM
     64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

  (End of data from sysinfo program)
  Regarding the sysinfo display about the memory installed, the correct amount of
  memory is 1 TB and the dmidecode description should have two lines reading as:
     32x NO DIMM NO DIMM
     64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
`icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks:
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`
`-opt-mem-layout-trans=3`

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## Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-HSW-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-HSW-RevG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jul 14 16:21:30 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 July 2015.