SPEC® CINT2006 Result

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NEC Corporation

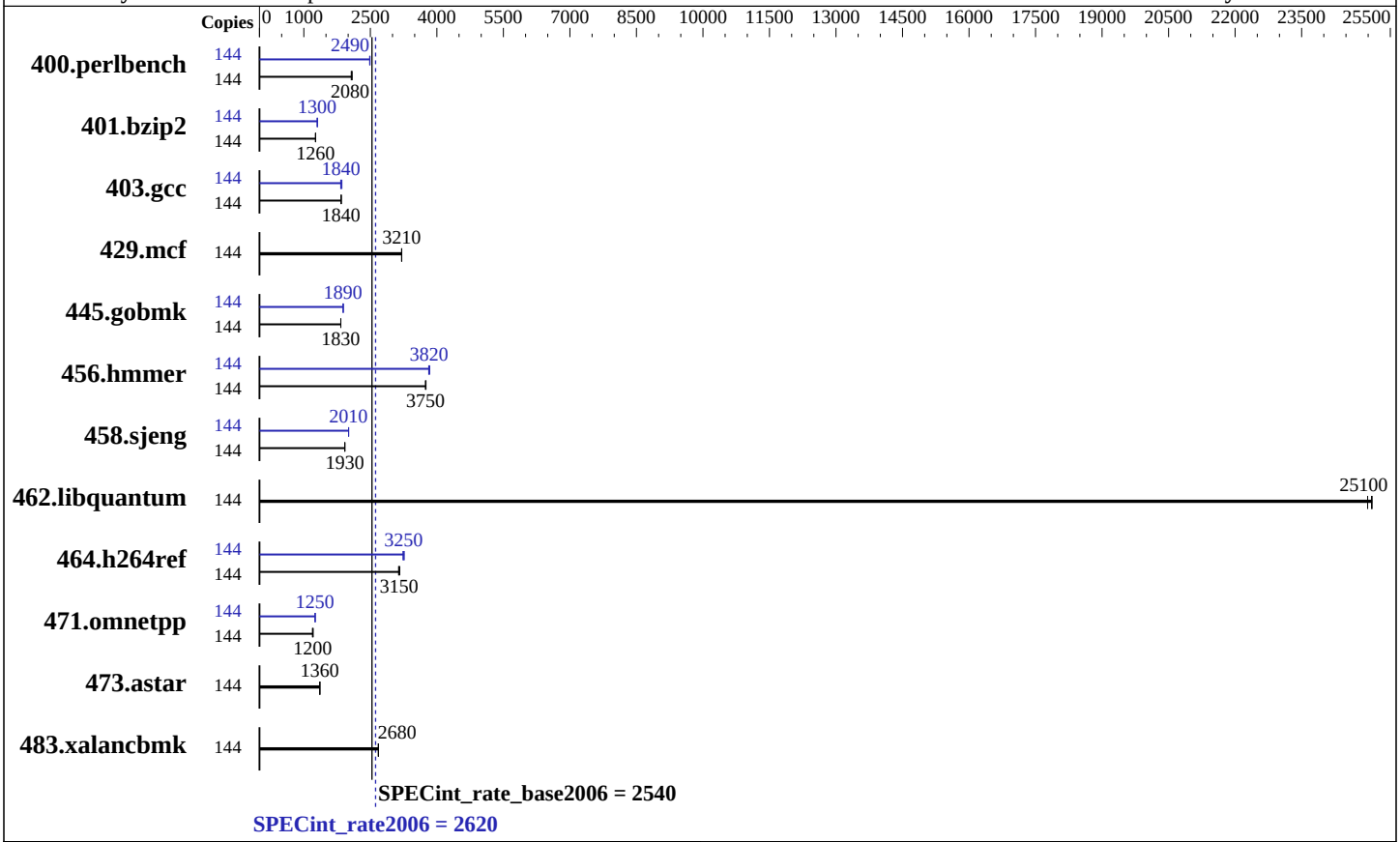
Express5800/A2040c (Intel Xeon E7-8870 v3)

SPECint®_rate2006 = 2620

SPECint_rate_base2006 = 2540

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jun-2015
Hardware Availability: Jul-2015
Software Availability: Mar-2015



Hardware

CPU Name:	Intel Xeon E7-8870 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	72 cores, 4 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	45 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	3 x 300 GB SAS, 15000 RPM, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.6 (Santiago) Kernel 2.6.32-504.12.2.el6.x86_64
Compiler:	C/C++: Version 14.0.3.174 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Multi-Core V10.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	144	**676**	**2080**	680	2070	673	2090	144	566	2490	**566**	**2490**	564	2490
401.bzip2	144	1100	1260	**1099**	**1260**	1097	1270	144	1067	1300	1063	1310	**1066**	**1300**
403.gcc	144	630	1840	**630**	**1840**	628	1840	144	**629**	**1840**	633	1830	624	1860
429.mcf	144	409	3210	**410**	**3210**	410	3200	144	409	3210	**410**	**3210**	410	3200
445.gobmk	144	824	1830	823	1830	**824**	**1830**	144	801	1890	**801**	**1890**	799	1890
456.hmmer	144	359	3740	358	3760	**359**	**3750**	144	350	3840	**351**	**3820**	352	3820
458.sjeng	144	**905**	**1930**	907	1920	905	1930	144	867	2010	868	2010	**867**	**2010**
462.libquantum	144	119	25000	**119**	**25100**	119	25100	144	119	25000	**119**	**25100**	119	25100
464.h264ref	144	1016	3140	**1013**	**3150**	1007	3160	144	985	3240	**981**	**3250**	976	3260
471.omnetpp	144	743	1210	753	1200	**750**	**1200**	144	718	1250	**717**	**1250**	716	1260
473.astar	144	742	1360	**743**	**1360**	744	1360	144	742	1360	**743**	**1360**	744	1360
483.xalancbmk	144	371	2680	370	2680	**371**	**2680**	144	371	2680	370	2680	**371**	**2680**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

```
BIOS Settings:
 Memory RAS Mode: Independent
 VT-x : Disabled
 Processor C6 Report : Disabled
 OS Performance Tuning : Disabled
 Energy Performance : Performance
 Patrol Scrub : Disabled
 Demand Scrub : Disabled
 Memory P.E. Retry : Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/SmartHeap_10mc/lib:/opt/SmartHeap_10mc/lib64:/opt/intel/composer_xe_2013_sp1.3.174/compiler/lib/ia32:/opt/intel/composer_xe_2013_sp1.3.174/compiler/lib/intel64"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs
-L/opt/SmartHeap_10mc/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_10mc/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040c-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040c-RevA.xml

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For other inquiries, please contact webmaster@spec.org.

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