SPEC® CINT2006 Result

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Fujitsu

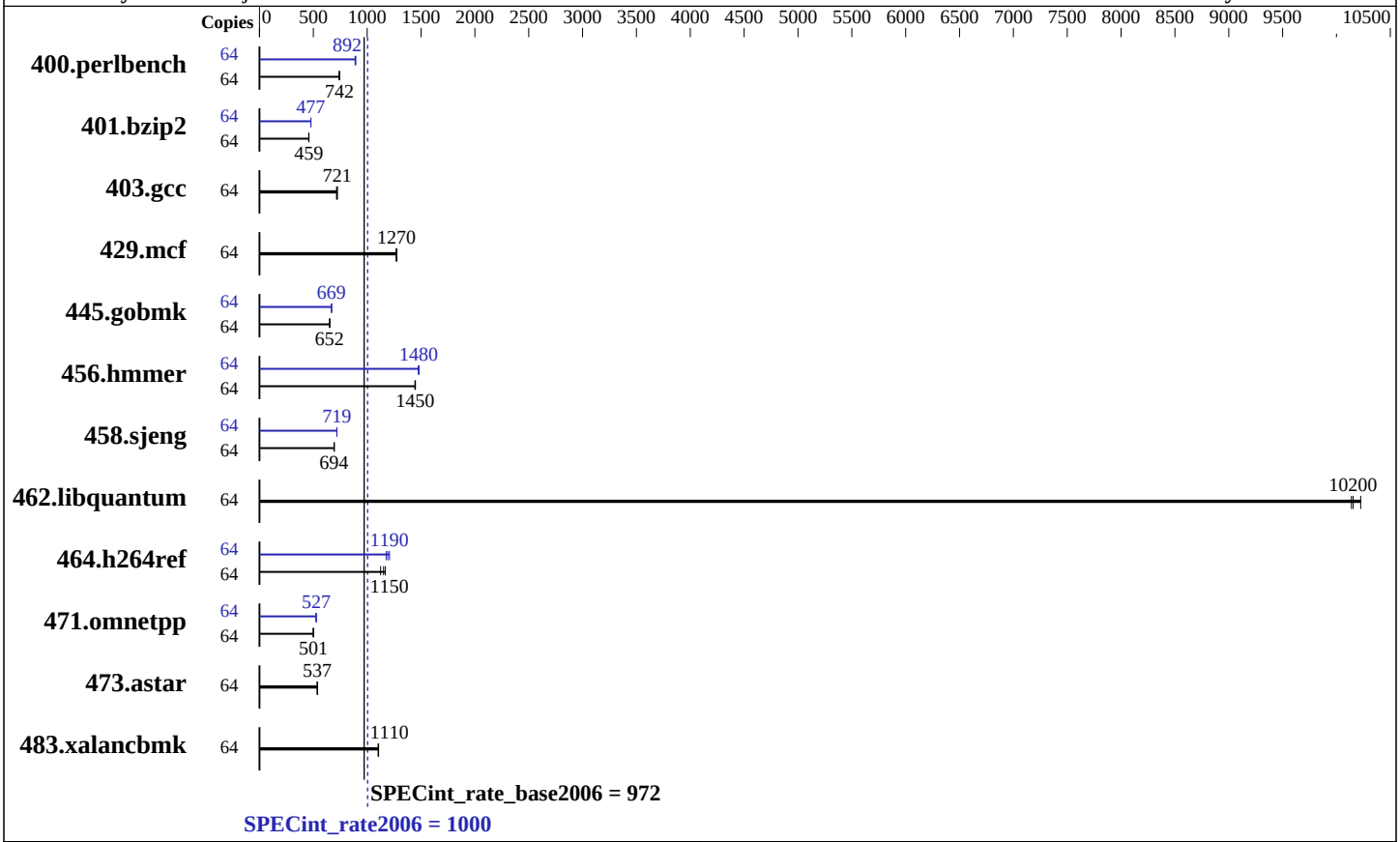
PRIMERGY RX4770 M2, Intel Xeon E7-4809 v3, 2.00 GHz

SPECint®_rate2006 = 1000

SPECint_rate_base2006 = 972

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2015
Hardware Availability: May-2015
Software Availability: Oct-2014



Hardware

CPU Name:	Intel Xeon E7-4809 v3
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
Disk Subsystem:	1 x SATA, 600 GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.6 (Santiago) 2.6.32-504.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	**843**	**742**	841	743	846	739	64	700	894	**701**	**892**	705	887
401.bzip2	64	**1345**	**459**	1345	459	1351	457	64	**1295**	**477**	1291	478	1295	477
403.gcc	64	713	723	**715**	**721**	720	716	64	713	723	**715**	**721**	720	716
429.mcf	64	458	1280	**459**	**1270**	460	1270	64	458	1280	**459**	**1270**	460	1270
445.gobmk	64	1031	651	1030	652	**1030**	**652**	64	1005	668	1002	670	**1003**	**669**
456.hmmer	64	412	1450	413	1440	**413**	**1450**	64	405	1470	**404**	**1480**	403	1480
458.sjeng	64	**1117**	**694**	1116	694	1117	693	64	1076	719	**1077**	**719**	1078	718
462.libquantum	64	130	10200	**131**	**10200**	131	10100	64	130	10200	**131**	**10200**	131	10100
464.h264ref	64	1259	1130	1212	1170	**1229**	**1150**	64	**1189**	**1190**	1173	1210	1204	1180
471.omnetpp	64	797	502	**799**	**501**	802	499	64	759	527	**759**	**527**	762	525
473.astar	64	834	539	**837**	**537**	838	536	64	834	539	**837**	**537**	838	536
483.xalancbmk	64	**400**	**1110**	399	1110	401	1100	64	**400**	**1110**	399	1110	401	1100

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Energy Performance = Performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

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General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

Continued on next page

Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Jun 17 10:49:11 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 June 2015.