SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

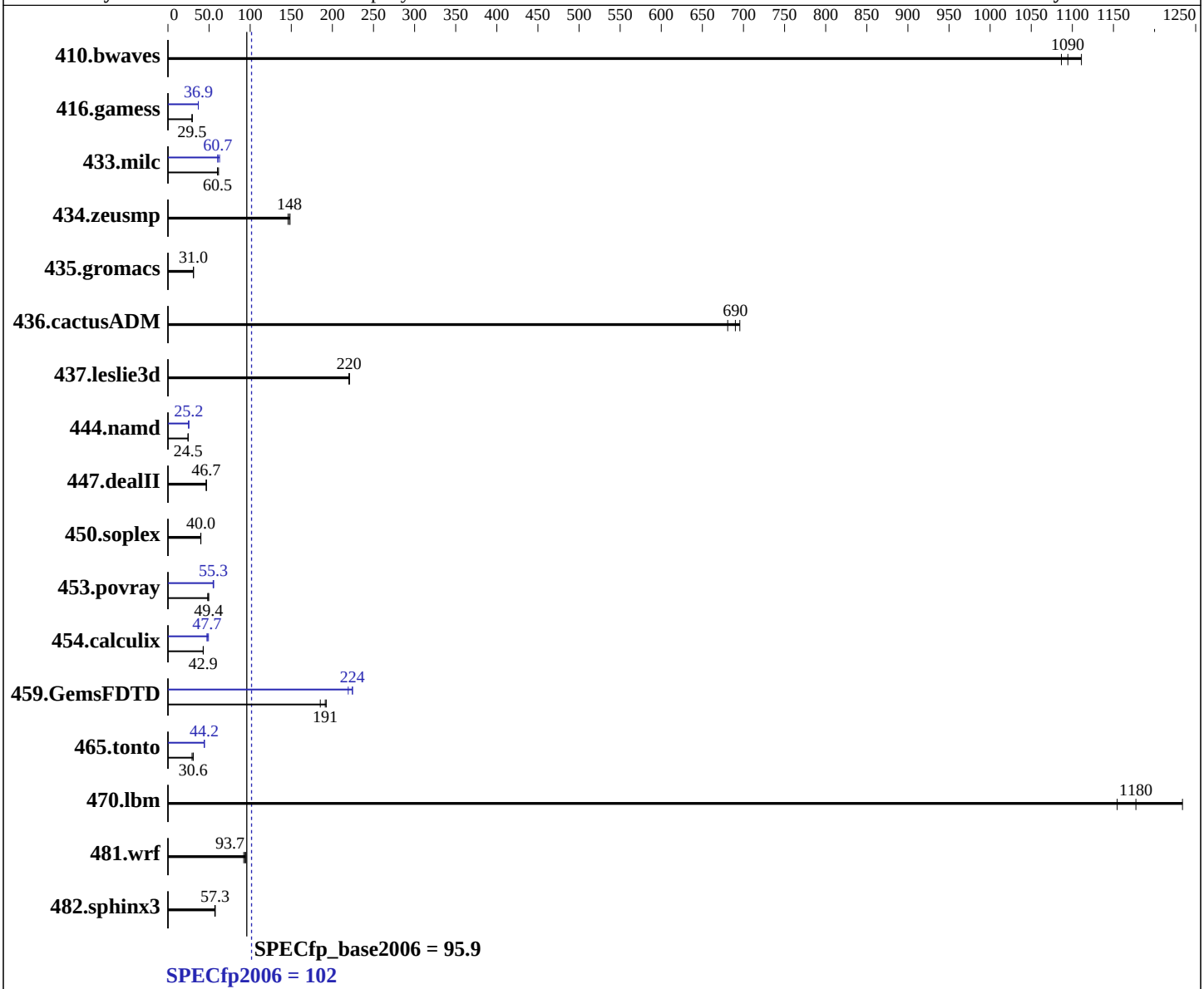
ProLiant DL560 Gen9
(2.00 GHz, Intel Xeon E5-4667 v3)

SPECfp®2006 = 102

SPECfp_base2006 = 95.9

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Mar-2015



Hardware

CPU Name:	Intel Xeon E5-4667 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	64 cores, 4 chips, 16 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) Kernel 3.12.28-4-default
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL560 Gen9
(2.00 GHz, Intel Xeon E5-4667 v3)

SPECfp2006 = 102

SPECfp_base2006 = 95.9

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Mar-2015

L3 Cache: 40 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**12.4**	**1090**	12.5	1090	12.2	1110	**12.4**	**1090**	12.5	1090	12.2	1110
416.gamess	664	29.5	**664**	**29.5**	668	29.3	529	37.0	**530**	**36.9**	531	36.9
433.milc	**152**	**60.5**	149	61.6	152	60.3	146	62.8	151	60.7	**151**	**60.7**
434.zeusmp	61.3	148	62.3	146	**61.6**	**148**	61.3	148	62.3	146	**61.6**	**148**
435.gromacs	**230**	**31.0**	231	30.9	230	31.0	**230**	**31.0**	231	30.9	230	31.0
436.cactusADM	17.6	681	**17.3**	**690**	17.2	696	17.6	681	**17.3**	**690**	17.2	696
437.leslie3d	**42.7**	**220**	42.6	221	42.7	220	**42.7**	**220**	42.6	221	42.7	220
444.namd	327	24.5	**327**	**24.5**	327	24.5	318	25.2	318	25.2	**318**	**25.2**
447.dealII	**245**	**46.7**	245	46.6	245	46.7	**245**	**46.7**	245	46.6	245	46.7
450.soplex	209	39.8	208	40.1	**209**	**40.0**	209	39.8	208	40.1	**209**	**40.0**
453.povray	**108**	**49.4**	110	48.3	107	49.5	**96.3**	**55.3**	96.8	55.0	95.7	55.6
454.calculix	192	43.1	**192**	**42.9**	193	42.8	173	47.6	**173**	**47.7**	167	49.3
459.GemsFDTD	57.3	185	**55.5**	**191**	55.1	193	47.3	225	**47.3**	**224**	48.4	219
465.tonto	336	29.3	**321**	**30.6**	320	30.8	**223**	**44.2**	223	44.1	222	44.3
470.lbm	**11.7**	**1180**	11.9	1150	11.1	1230	**11.7**	**1180**	11.9	1150	11.1	1230
481.wrf	121	92.1	118	94.4	**119**	**93.7**	121	92.1	118	94.4	**119**	**93.7**
482.sphinx3	342	57.0	**340**	**57.3**	339	57.5	342	57.0	**340**	**57.3**	339	57.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

```
BIOS Configuration
  Intel Hyperthreading options set to Disabled
  Power Profile set to Custom
  Power Regulator set to Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C6 State
  Minimum Processor Idle Power Package C-State set to C6 (retention) State
  Energy/Performance Bias set to Maximum Performance
  Collaborative Power Control set to Enabled
```

Continued on next page

Platform Notes (Continued)

```
      Thermal Configuration set to Maximum Cooling
      Processor Power and Utilization Monitoring set to Disabled
      Memory Refresh Rate set to 1x Refresh

    Sysinfo program /home/cpu2006/config/sysinfo.rev6914
    $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
    running on dl560gen9jks Fri May 15 01:31:10 2015

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-4667 v3 @ 2.00GHz
          4 "physical id"s (chips)
          64 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 16
          siblings  : 16
          physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
          physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
          physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
          physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
       cache size : 40960 KB

    From /proc/meminfo
       MemTotal:       529310208 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    /usr/bin/lsb_release -d
       SUSE Linux Enterprise Server 12

    From /etc/*release* /etc/*version*
       SuSE-release:
          SUSE Linux Enterprise Server 12 (x86_64)
          VERSION = 12
          PATCHLEVEL = 0
          # This file is deprecated and will be removed in a future service pack or
          release.
          # Please check /etc/os-release for details about this release.
       os-release:
          NAME="SLES"
          VERSION="12"
          VERSION_ID="12"
          PRETTY_NAME="SUSE Linux Enterprise Server 12"
          ID="sles"
          ANSI_COLOR="0;32"
          CPE_NAME="cpe:/o:suse:sles:12"
```

Continued on next page

Platform Notes (Continued)

```
 uname -a:
    Linux dl560gen9jks 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
    (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 May 14 19:18 last=5

 SPEC is set to: /home/cpu2006
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda4      xfs   331G  6.0G  325G   2% /home
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP P85 03/05/2015
   Memory:
    24x HP 752369-081 16 GB 2 rank 2133 MHz
    16x UNKNOWN NOT AVAILABLE
    8x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

 (End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 512 GB and the dmidecode description should have two lines reading as:
    24x HP 752369-081 16 GB 2 rank 2133 MHz
    8x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "64"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Continued on next page

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

Continued on next page

Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 2 13:49:05 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 June 2015.