



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 840

PRIMERGY TX2560 M1, Intel Xeon E5-2667 v3, 3.2 GHz

### SPECint\_rate\_base2006 = 811

CPU2006 license: 19

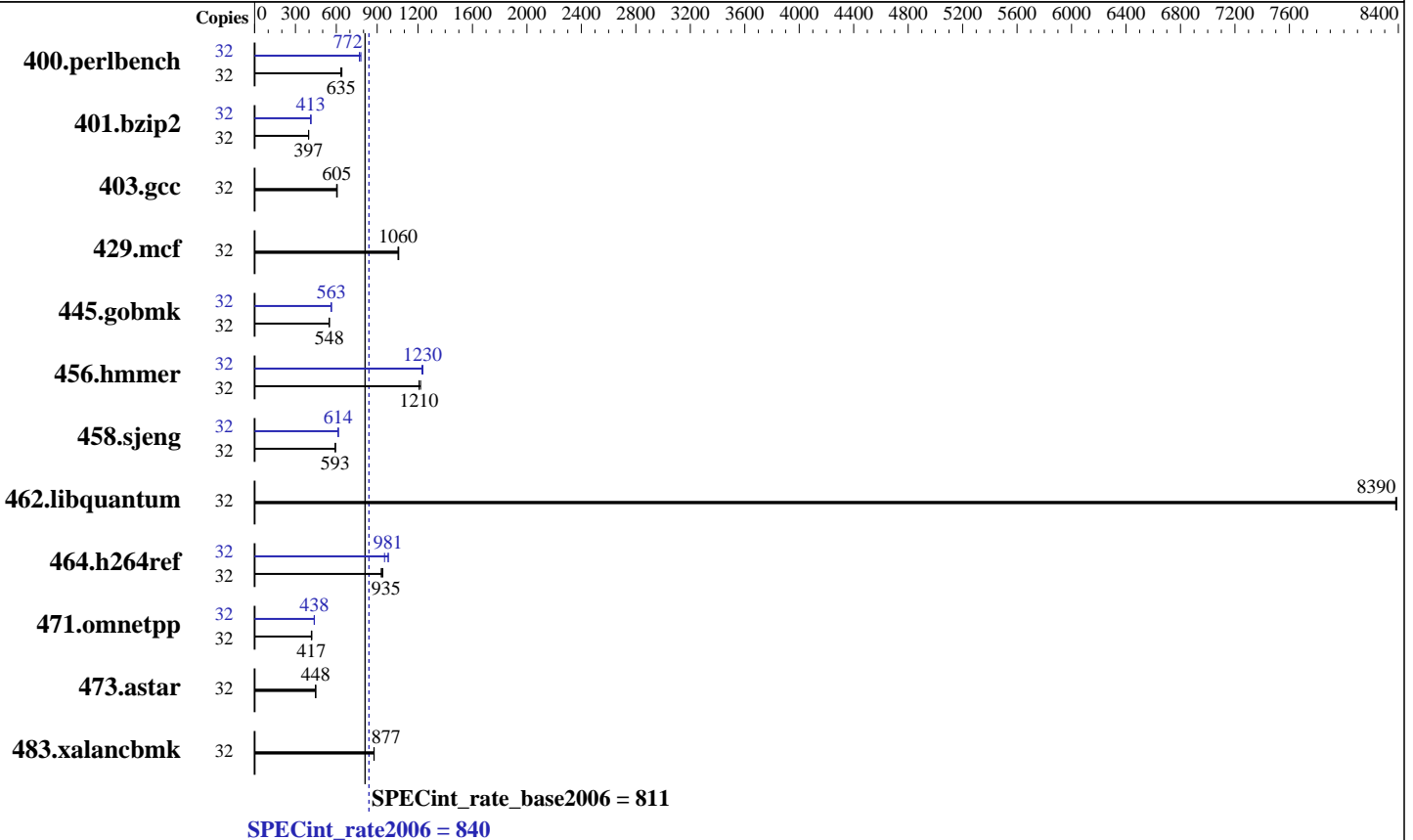
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Apr-2015

Hardware Availability: Apr-2015

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2667 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)  
 2.6.32-504.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	488	641	493	634	<b><u>492</u></b>	<b><u>635</u></b>	32	400	781	406	770	<b><u>405</u></b>	<b><u>772</u></b>
401.bzip2	32	<b><u>779</u></b>	<b><u>397</u></b>	779	396	778	397	32	748	413	748	413	<b><u>748</u></b>	<b><u>413</u></b>
403.gcc	32	<b><u>426</u></b>	<b><u>605</u></b>	425	606	428	602	32	<b><u>426</u></b>	<b><u>605</u></b>	425	606	428	602
429.mcf	32	<b><u>277</u></b>	<b><u>1060</u></b>	277	1050	276	1060	32	<b><u>277</u></b>	<b><u>1060</u></b>	277	1050	276	1060
445.gobmk	32	613	548	612	549	<b><u>612</u></b>	<b><u>548</u></b>	32	596	563	<b><u>596</u></b>	<b><u>563</u></b>	596	563
456.hammer	32	<b><u>247</u></b>	<b><u>1210</u></b>	245	1220	247	1210	32	242	1240	<b><u>242</u></b>	<b><u>1230</u></b>	243	1230
458.sjeng	32	<b><u>653</u></b>	<b><u>593</u></b>	653	593	653	593	32	631	614	<b><u>630</u></b>	<b><u>614</u></b>	630	614
462.libquantum	32	79.0	8390	<b><u>79.1</u></b>	<b><u>8390</u></b>	79.1	8380	32	79.0	8390	<b><u>79.1</u></b>	<b><u>8390</u></b>	79.1	8380
464.h264ref	32	762	930	752	941	<b><u>757</u></b>	<b><u>935</u></b>	32	<b><u>722</u></b>	<b><u>981</u></b>	721	982	742	954
471.omnetpp	32	475	421	480	417	<b><u>480</u></b>	<b><u>417</u></b>	32	456	438	456	439	<b><u>456</u></b>	<b><u>438</u></b>
473.astar	32	<b><u>501</u></b>	<b><u>448</u></b>	501	449	502	447	32	<b><u>501</u></b>	<b><u>448</u></b>	501	449	502	447
483.xalancbmk	32	251	878	252	876	<b><u>252</u></b>	<b><u>877</u></b>	32	251	878	252	876	<b><u>252</u></b>	<b><u>877</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy Performance = Performance  
Utilization Profile = Unbalanced  
QPI snoop mode: Early Snoop  
COD Enable = Disabled, Early Snoop = Enabled  
CPU C1E Support = Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

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## General Notes (Continued)

```
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>
```

This result was measured on the PRIMERGY RX2560 M1. The PRIMERGY RX2560 M1 and the PRIMERGY TX2560 M1 are electronically equivalent.  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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