SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX2580 M1, Intel Xeon E5-2690 v3, 2.6 GHz

$SPECint^{®}\_rate2006 =$ 1100
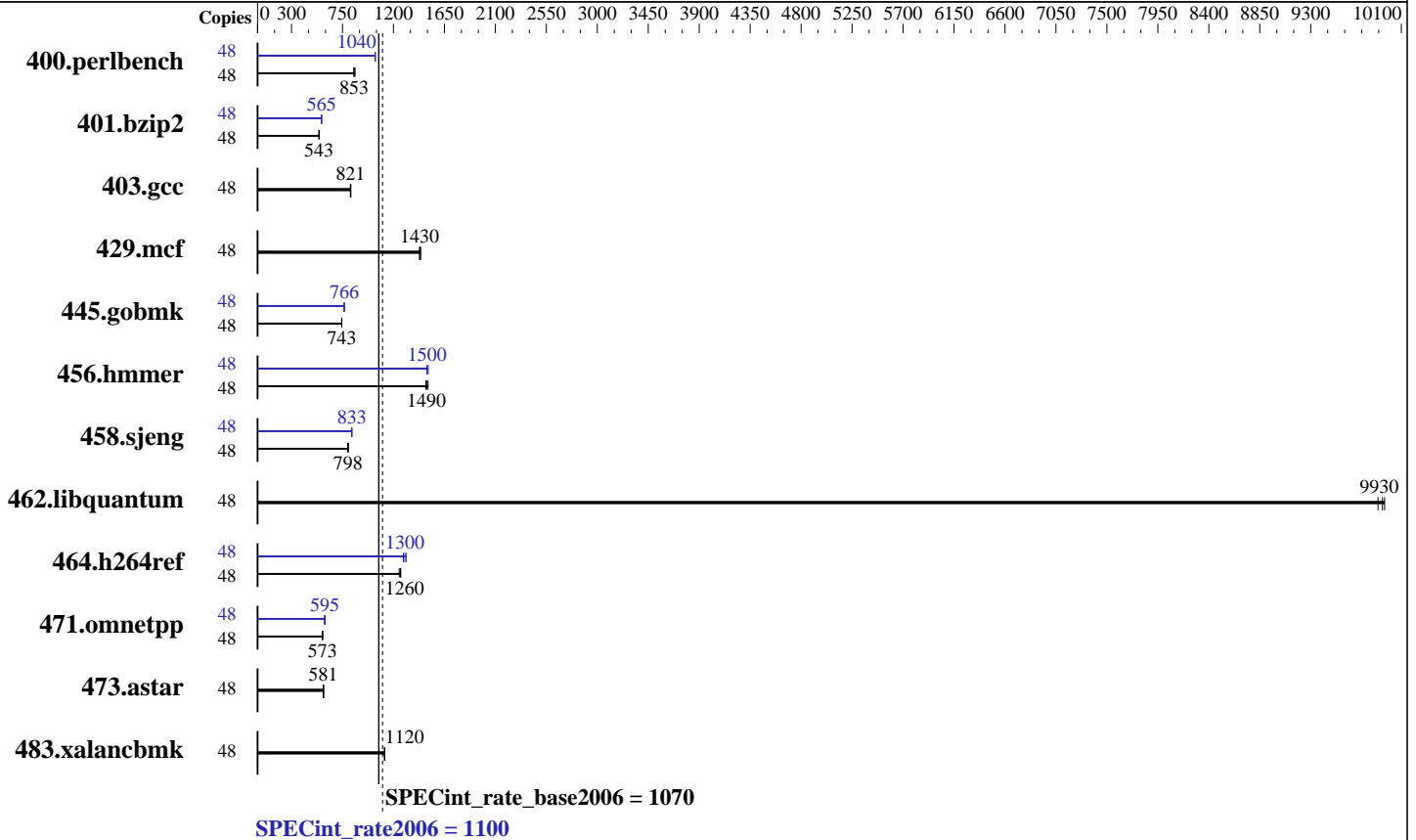
SPECint_rate_base2006 = 1070

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2015
Hardware Availability: Mar-2015
Software Availability: Nov-2013

Benchmark	Copies		
400.perlbench	48	1040	
	48	853	
401.bzip2	48	565	
	48	543	
403.gcc	48	821	
429.mcf	48	1430	
445.gobmk	48	766	
	48	743	
456.hmmer	48	1500	
	48	1490	
458.sjeng	48	833	
	48	798	
462.libquantum	48	9930	
464.h264ref	48	1300	
	48	1260	
471.omnetpp	48	595	
	48	573	
473.astar	48	581	
483.xalancbmk	48	1120	

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Hardware

CPU Name: Intel Xeon E5-2690 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)
2.6.32-504.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	545	861	**550**	**853**	552	850	48	**452**	**1040**	451	1040	452	1040
401.bzip2	48	**853**	**543**	854	542	852	544	48	820	565	**820**	**565**	821	565
403.gcc	48	472	819	**470**	**821**	469	824	48	472	819	**470**	**821**	469	824
429.mcf	48	**306**	**1430**	306	1430	304	1440	48	**306**	**1430**	306	1430	304	1440
445.gobmk	48	676	744	678	742	**677**	**743**	48	658	765	657	766	**658**	**766**
456.hmmer	48	301	1490	**300**	**1490**	298	1510	48	299	1500	297	1510	**299**	**1500**
458.sjeng	48	723	804	**728**	**798**	729	796	48	700	830	**697**	**833**	697	833
462.libquantum	48	101	9890	99.9	9950	**100**	**9930**	48	101	9890	99.9	9950	**100**	**9930**
464.h264ref	48	850	1250	840	1260	**842**	**1260**	48	**817**	**1300**	825	1290	810	1310
471.omnetpp	48	**524**	**573**	520	577	526	571	48	508	590	**504**	**595**	504	596
473.astar	48	576	585	581	580	**580**	**581**	48	576	585	581	580	**580**	**581**
483.xalancbmk	48	**295**	**1120**	295	1120	296	1120	48	**295**	**1120**	295	1120	296	1120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
QPI snoop mode: Cluster on Die
    COD Enable = Enabled, Early Snoop = Disabled
CPU C1E Support = Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

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General Notes (Continued)

```
Filesystem page cache cleared with:
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
    `icpc -m32`

Peak Portability Flags

    400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
       401.bzip2: `-DSPEC_CPU_LP64`
      456.hmmer: `-DSPEC_CPU_LP64`
       458.sjeng: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LINUX`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
            `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
            `-auto-ilp32`

     401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
            `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
            `-opt-prefetch -auto-ilp32 -ansi-alias`

      403.gcc: `basepeak = yes`

      429.mcf: `basepeak = yes`

    445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
            `-ansi-alias -opt-mem-layout-trans=3`

    456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

     458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
            `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
            `-unroll4 -auto-ilp32`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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