# SPEC® CINT2006 Result

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## Lenovo Group Limited

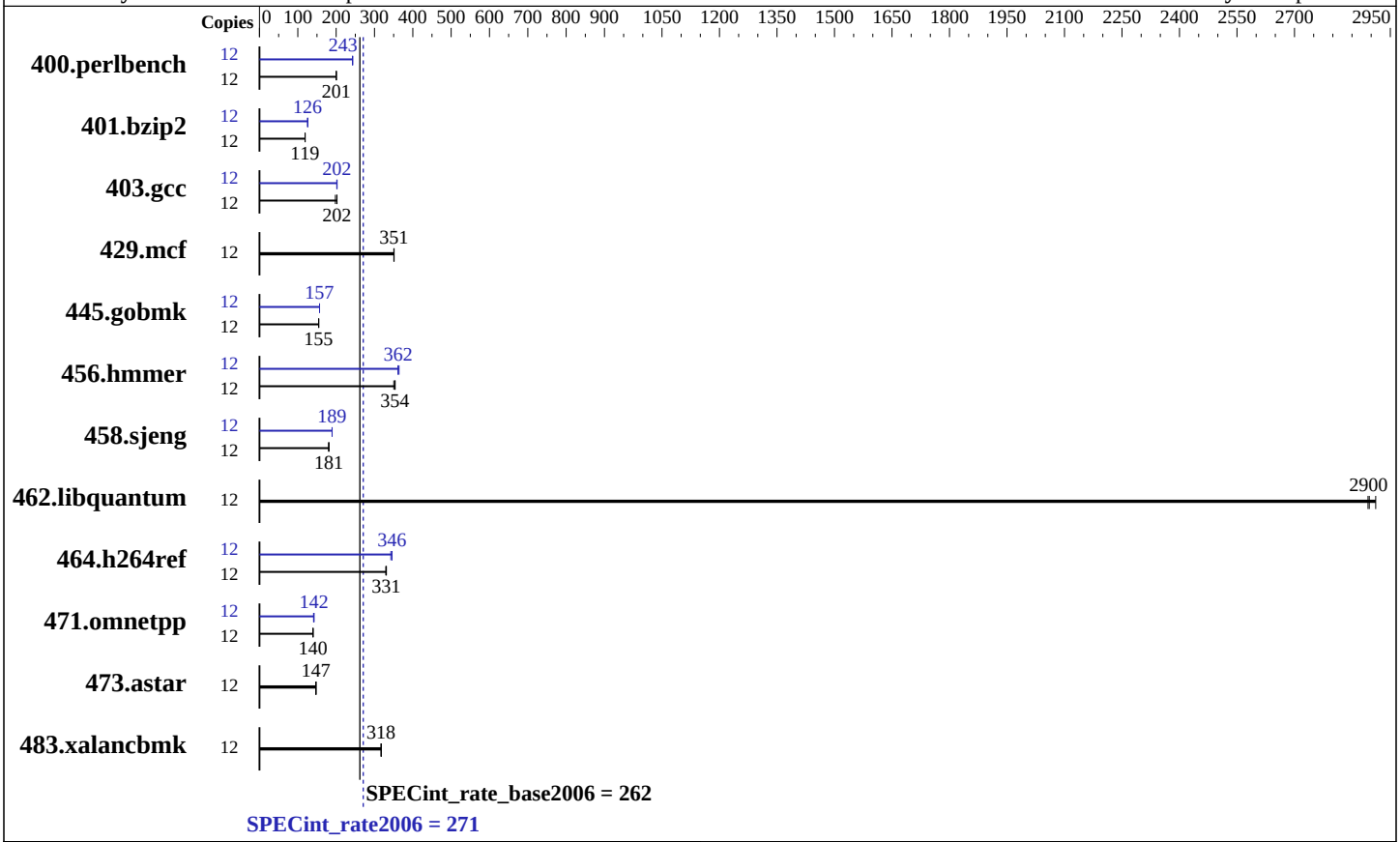
Lenovo System x3550 M5 (Intel Xeon E5-2603 v3, 1.60 GHz)

SPECint®_rate2006 = 271

SPECint_rate_base2006 = 262

**CPU2006 license:** 9017
**Test sponsor:** Lenovo Group Limited
**Tested by:** Lenovo Group Limited

**Test date:** Mar-2015
**Hardware Availability:** Oct-2014
**Software Availability:** Sep-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2603 v3 |
| CPU Characteristics: | |
| CPU MHz: | 1600 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz) |
| Disk Subsystem: | 1 x 300 GB SAS, 10000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.0 (Maipo)<br>3.10.0-123.el7.x86_64 |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 12 | 587 | 200 | **585** | **201** | 584 | 201 | 12 | 482 | 243 | **482** | **243** | 483 | 243 |
| 401.bzip2 | 12 | **974** | **119** | 970 | 119 | 975 | 119 | 12 | 921 | 126 | **921** | **126** | 920 | 126 |
| 403.gcc | 12 | 488 | 198 | 478 | 202 | **479** | **202** | 12 | 477 | 202 | 479 | 202 | **478** | **202** |
| 429.mcf | 12 | **312** | **351** | 312 | 351 | 312 | 351 | 12 | **312** | **351** | 312 | 351 | 312 | 351 |
| 445.gobmk | 12 | **815** | **155** | 816 | 154 | 814 | 155 | 12 | 803 | 157 | **803** | **157** | 803 | 157 |
| 456.hmmer | 12 | 320 | 350 | **317** | **354** | 316 | 354 | 12 | **310** | **362** | 310 | 361 | 308 | 364 |
| 458.sjeng | 12 | 803 | 181 | **803** | **181** | 802 | 181 | 12 | **766** | **189** | 767 | 189 | 766 | 190 |
| 462.libquantum | 12 | 85.4 | 2910 | 86.0 | 2890 | **85.9** | **2900** | 12 | 85.4 | 2910 | 86.0 | 2890 | **85.9** | **2900** |
| 464.h264ref | 12 | 806 | 329 | 802 | 331 | **803** | **331** | 12 | **768** | **346** | 775 | 343 | 768 | 346 |
| 471.omnetpp | 12 | **537** | **140** | 538 | 139 | 536 | 140 | 12 | 532 | 141 | 528 | 142 | **528** | **142** |
| 473.astar | 12 | 571 | 148 | 573 | 147 | **573** | **147** | 12 | 571 | 148 | 573 | 147 | **573** | **147** |
| 483.xalancbmk | 12 | 261 | 317 | 261 | 318 | **261** | **318** | 12 | 261 | 317 | 261 | 318 | **261** | **318** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS setting:
Operating Mode set to "Efficiency-Favor Performance"
Sysinfo program /home/SPEC/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on x3550m5 Mon Mar 23 16:49:24 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
      2 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

Continued on next page

## Platform Notes (Continued)

```
        cpu cores : 6
        siblings  : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
     cache size : 15360 KB

  From /proc/meminfo
     MemTotal:       263635396 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.0 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="7.0"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
        ANSI_COLOR="0;31"
        CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
     redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
     system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

  uname -a:
     Linux x3550m5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
     x86_64 x86_64 GNU/Linux


  SPEC is set to: /home/SPEC
     Filesystem            Type  Size  Used Avail Use% Mounted on
     /dev/mapper/rhel-root xfs   275G   11G  265G   4% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS IBM -[TBE105FUS-1.10]- 03/04/2015
    Memory:
     14x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1600 MHz
     2x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz, configured at 1600 MHz
     8x NO DIMM Unknown

  (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPEC/libs/32:/home/SPEC/libs/64:/home/SPEC/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-auto-ilp32`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ansi-alias`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto-ilp32`

Continued on next page

## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.20141230.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.20141230.xml


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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Apr 21 18:21:03 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 April 2015.