SPEC® CFP2006 Result

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Huawei

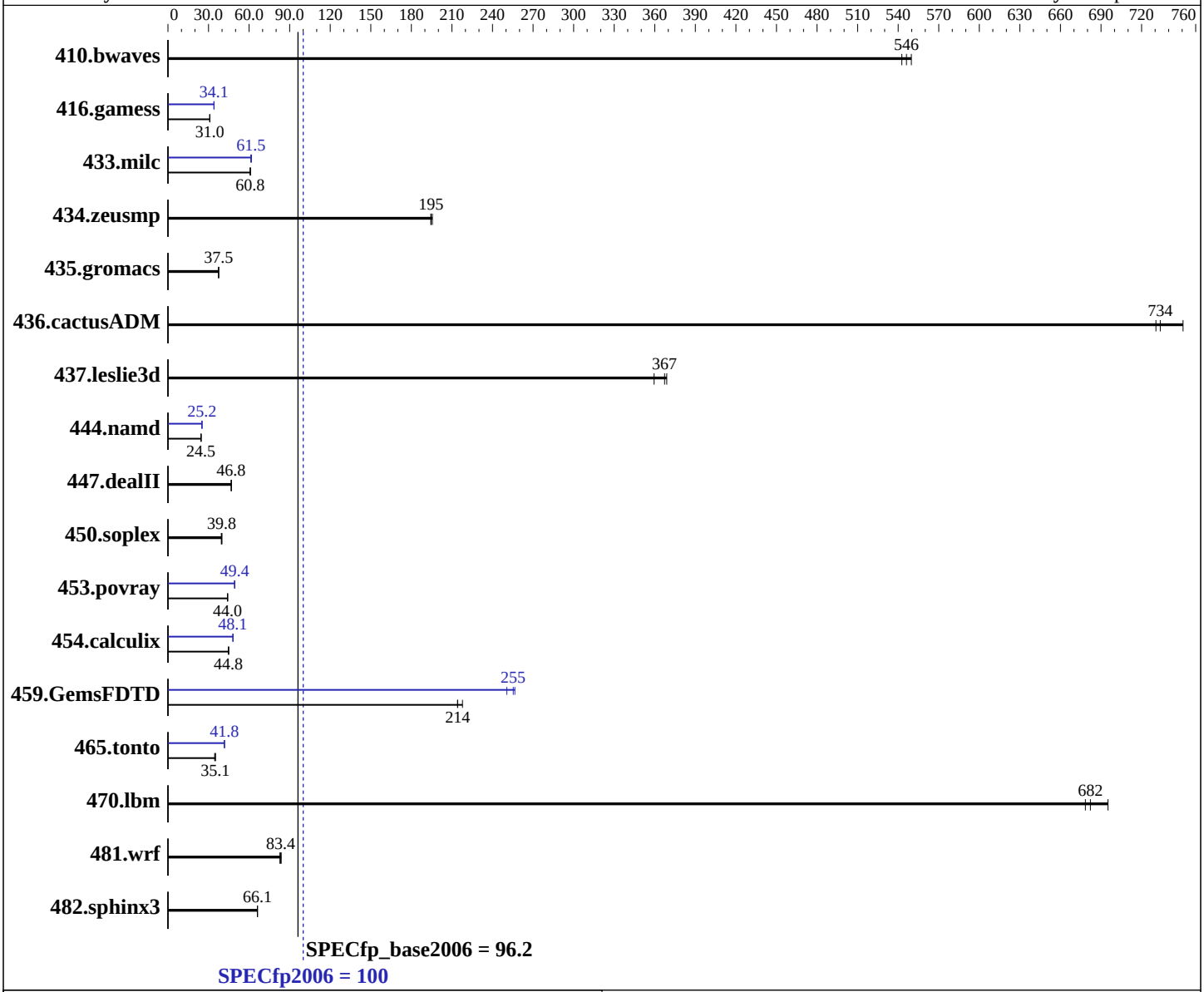
Huawei XH622 V3 (Intel Xeon E5-2658 v3)

SPECfp®2006 = 100

SPECfp_base2006 = 96.2

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2015
Hardware Availability: Feb-2015
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E5-2658 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24.7	550	25.0	543	**24.9**	**546**	24.7	550	25.0	543	**24.9**	**546**
416.gamess	631	31.0	**632**	**31.0**	635	30.8	**574**	**34.1**	573	34.1	577	33.9
433.milc	**151**	**60.8**	150	61.0	151	60.8	149	61.8	150	61.3	**149**	**61.5**
434.zeusmp	46.5	196	46.8	194	**46.8**	**195**	46.5	196	46.8	194	**46.8**	**195**
435.gromacs	191	37.4	190	37.5	**191**	**37.5**	191	37.4	190	37.5	**191**	**37.5**
436.cactusADM	15.9	751	**16.3**	**734**	16.4	731	15.9	751	**16.3**	**734**	16.4	731
437.leslie3d	25.5	369	26.1	360	**25.6**	**367**	25.5	369	26.1	360	**25.6**	**367**
444.namd	328	24.5	327	24.5	**327**	**24.5**	**319**	**25.2**	319	25.2	319	25.2
447.dealII	244	46.8	244	46.9	**244**	**46.8**	244	46.8	244	46.9	**244**	**46.8**
450.soplex	209	39.9	**210**	**39.8**	211	39.4	209	39.9	**210**	**39.8**	211	39.4
453.povray	121	43.9	**121**	**44.0**	120	44.3	107	49.5	108	49.1	**108**	**49.4**
454.calculix	184	44.9	**184**	**44.8**	184	44.7	171	48.1	172	48.0	**172**	**48.1**
459.GemsFDTD	48.7	218	49.5	214	**49.5**	**214**	42.3	251	**41.6**	**255**	41.4	257
465.tonto	285	34.5	281	35.1	**281**	**35.1**	236	41.7	**236**	**41.8**	235	41.8
470.lbm	**20.1**	**682**	19.8	695	20.3	678	**20.1**	**682**	19.8	695	20.3	678
481.wrf	135	82.8	133	83.7	**134**	**83.4**	135	82.8	133	83.7	**134**	**83.4**
482.sphinx3	294	66.3	295	66.1	**295**	**66.1**	294	66.3	295	66.1	**295**	**66.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Intel Hyper-threading Technology to Disable
Sysinfo program /spec15/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Mar 16 05:24:33 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2658 v3 @ 2.20GHz
          2 "physical id"s (chips)
          24 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 12
          siblings  : 12
          physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
          physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
       cache size : 30720 KB

    From /proc/meminfo
       MemTotal:       263720560 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       os-release:
          NAME="Red Hat Enterprise Linux Server"
          VERSION="7.0 (Maipo)"
          ID="rhel"
          ID_LIKE="fedora"
          VERSION_ID="7.0"
          PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
          ANSI_COLOR="0;31"
          CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
       redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
       system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

    uname -a:
       Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
       EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Mar 16 05:20

    SPEC is set to: /spec15
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda1      ext4  458G   38G  397G   9% /
    Additional information from dmidecode:

       Warning: Use caution when you interpret this section. The 'dmidecode' program
       reads system data which is "intended to allow hardware to be accurately
       determined", but the intent may not be met, as there are frequent changes to
       hardware, firmware, and the "DMTF SMBIOS" standard.

      BIOS Insyde Corp. 1.17 09/03/2014
      Memory:
```

Continued on next page

Platform Notes (Continued)

```
      8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz
      8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

   (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 The Huawei XH622 V3 and Huawei XH628 V3
 are electronically equivalent.
 The results have been measured on a Huawei XH628 V3 model.
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
```

Continued on next page

Base Portability Flags (Continued)

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml`

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