



SPEC® CFP2006 Result

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Lenovo Group Limited

Lenovo System x3650 M5
(Intel Xeon E5-2623 v3, 3.00 GHz)

SPECfp®_rate2006 = 390

SPECfp_rate_base2006 = 381

CPU2006 license: 9017

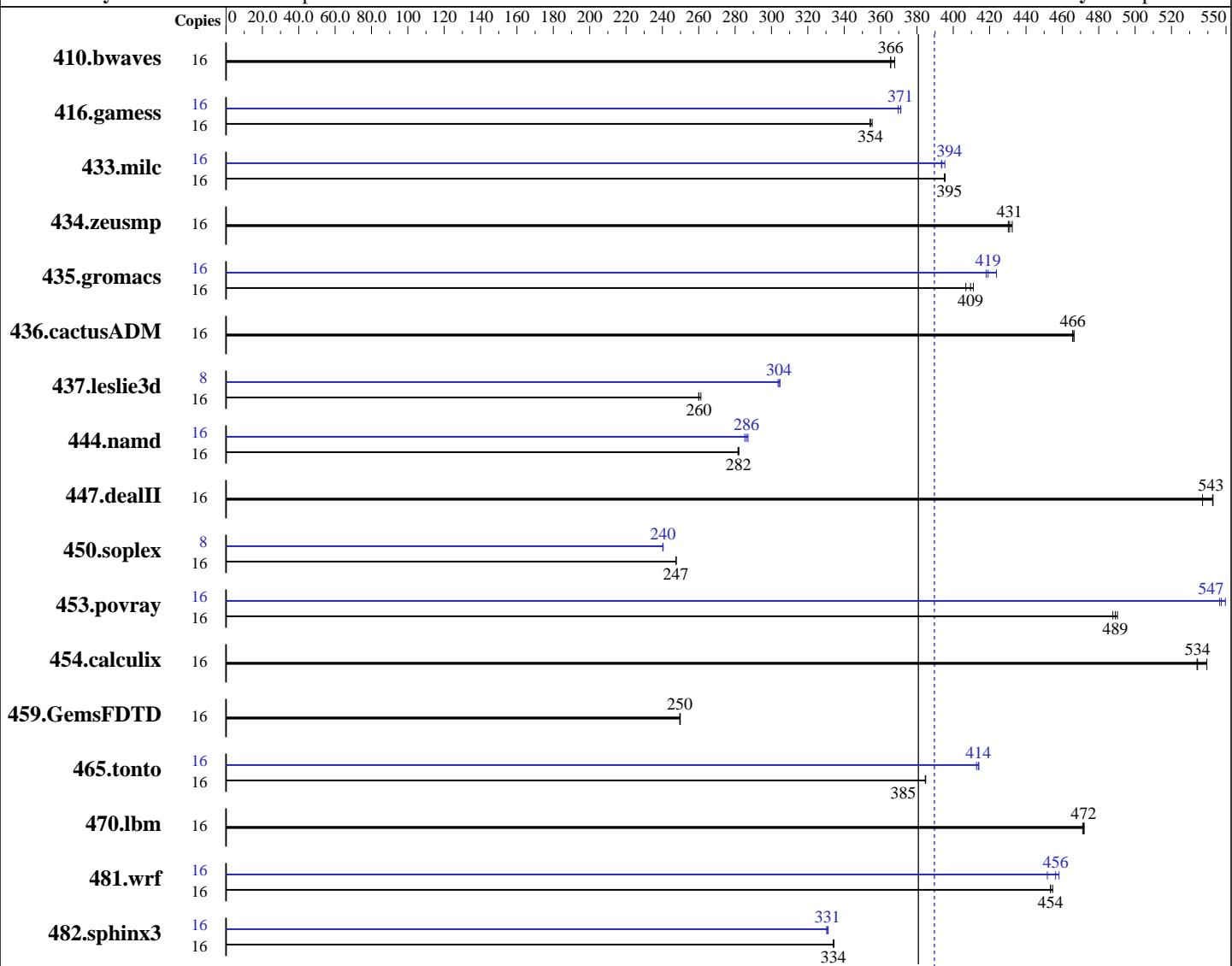
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Feb-2015

Hardware Availability: Oct-2014

Software Availability: Sep-2014



SPECfp_rate_base2006 = 381

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Hardware

CPU Name: Intel Xeon E5-2623 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: 3.10.0-123.el7.x86_64
C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x 300 GB SAS
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	591	368	595	366	595	365	16	591	368	595	366	595	365		
416.gamess	16	884	354	882	355	884	354	16	847	370	844	371	844	371		
433.milc	16	371	395	371	395	372	395	16	373	394	373	393	372	395		
434.zeusmp	16	338	430	338	431	337	432	16	338	430	338	431	337	432		
435.gromacs	16	279	409	278	411	281	407	16	270	424	273	418	273	419		
436.cactusADM	16	410	466	411	466	410	466	16	410	466	411	466	410	466		
437.leslie3d	16	578	260	576	261	579	260	8	247	304	247	305	248	304		
444.namd	16	455	282	456	282	455	282	16	450	285	448	286	447	287		
447.dealII	16	337	543	341	537	337	543	16	337	543	341	537	337	543		
450.soplex	16	539	248	539	247	539	247	8	278	240	278	240	278	240		
453.povray	16	175	488	174	490	174	489	16	156	546	155	547	155	550		
454.calculix	16	245	539	247	534	247	534	16	245	539	247	534	247	534		
459.GemsFDTD	16	679	250	680	250	680	250	16	679	250	680	250	680	250		
465.tonto	16	409	385	409	385	409	385	16	381	413	380	414	380	414		
470.lbm	16	467	471	466	472	466	472	16	467	471	466	472	466	472		
481.wrf	16	393	455	394	453	394	454	16	396	452	392	456	390	458		
482.sphinx3	16	934	334	933	334	932	334	16	944	330	943	331	942	331		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:

Operating Mode set to "Efficiency-Favor Performance"

Sysinfo program /SPECcpu/config/sysinfo.rev6914

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Platform Notes (Continued)

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on x3650M5 Sat Feb 7 01:12:46 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2623 v3 @ 3.00GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings : 8
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:       263585792 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux x3650M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 6 15:41
```

```
SPEC is set to: /SPECcpu
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda2        xfs   239G   30G  209G  13% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
Continued on next page



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Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TCE103EUS-1.01]- 10/21/2014

Memory:

16x Hynix 484D4134324752374D4652344E2D54462020 16 GB 2 rank 2133 MHz,
configured at 1866 MHz
8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
`LD_LIBRARY_PATH = "/SPECcpu/libs/32:/SPECcpu/libs/64:/SPECcpu/sh"`

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

416.gamess: `-DSPEC_CPU_LP64`

433.milc: `-DSPEC_CPU_LP64`

434.zeusmp: `-DSPEC_CPU_LP64`

435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`

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Base Portability Flags (Continued)

```
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -auto-ilp32

```

470.lbm: basepeak = yes

```

482.sphinx3: -xCORE-AVX2 -prof-gen(pass 1) -ipo -O3 -no-prec-div
    -prof-use(pass 2) -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
    -ansi-alias

```

Fortran benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-B.xml>

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