SPEC® CINT2006 Result

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Lenovo Group Limited

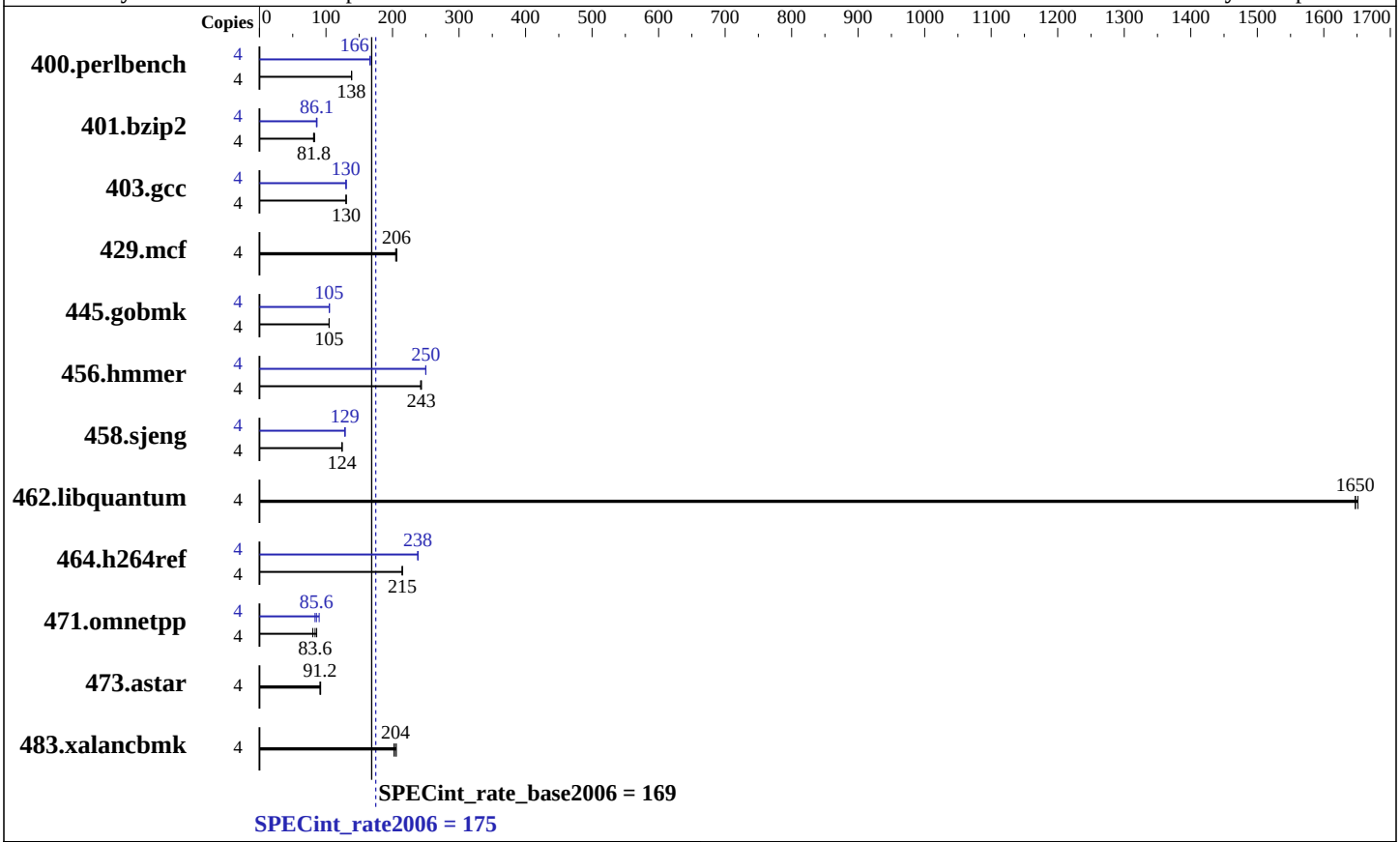
Lenovo ThinkServer TS140 (Intel Xeon E3-1220 v3, 3.10 GHz)

SPECint®_rate2006 = 175

SPECint_rate_base2006 = 169

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Jan-2015
Hardware Availability: Sep-2013
Software Availability: Sep-2014



Hardware

CPU Name:	Intel Xeon E3-1220 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem:	1 x 800 GB SATA SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	282	138	282	139	**282**	**138**	4	235	167	235	166	**235**	**166**
401.bzip2	4	466	82.8	475	81.3	**472**	**81.8**	4	**448**	**86.1**	451	85.6	447	86.4
403.gcc	4	248	130	247	130	**247**	**130**	4	248	130	**247**	**130**	247	130
429.mcf	4	177	206	178	205	**177**	**206**	4	177	206	178	205	**177**	**206**
445.gobmk	4	**401**	**105**	401	105	401	105	4	**400**	**105**	399	105	400	105
456.hmmer	4	153	243	**153**	**243**	154	242	4	**149**	**250**	149	250	149	250
458.sjeng	4	390	124	**390**	**124**	390	124	4	**377**	**129**	376	129	377	128
462.libquantum	4	50.2	1650	50.3	1650	**50.3**	**1650**	4	50.2	1650	50.3	1650	**50.3**	**1650**
464.h264ref	4	413	214	412	215	**412**	**215**	4	372	238	372	238	**372**	**238**
471.omnetpp	4	**299**	**83.6**	312	80.2	291	86.0	4	279	89.7	299	83.5	**292**	**85.6**
473.astar	4	**308**	**91.2**	308	91.2	305	92.0	4	**308**	**91.2**	308	91.2	305	92.0
483.xalancbmk	4	134	206	**135**	**204**	137	202	4	134	206	**135**	**204**	137	202

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration
ICE Performance Modes set to Full Speed
C1E Support set to Disabled
C State Support set to C1C3C6
 Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on TS140 Tue Jan 13 01:27:43 2015

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E3-1220 v3 @ 3.10GHz
       1 "physical id"s (chips)
       4 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
```

Continued on next page

Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 4
         siblings  : 4
         physical 0: cores 0 1 2 3
      cache size : 8192 KB

   From /proc/meminfo
      MemTotal:       16190088 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   From /etc/*release* /etc/*version*
      os-release:
         NAME="Red Hat Enterprise Linux Server"
         VERSION="7.0 (Maipo)"
         ID="rhel"
         ID_LIKE="fedora"
         VERSION_ID="7.0"
         PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
         ANSI_COLOR="0;31"
         CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
      redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
      system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

   uname -a:
      Linux TS140 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
      x86_64 x86_64 GNU/Linux

   run-level 3 Jan 13 01:27

   SPEC is set to: /usr/cpu2006
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda2      xfs   741G   24G  718G   4% /
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS LENOVO FBKT99AUS 09/19/2014
     Memory:
      2x Samsung M391B1G73QH0-YK0 8 GB 2 rank 1600 MHz
      2x [Empty] [Empty]

    (End of data from sysinfo program)
  TS140 support 2 channels and 2 DIMMs per channel, total 4 DIMMs.
  2 DIMM slots installed with 8 GB DIMM for this run.
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:
```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Continued on next page

Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TS140-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TS140-revA.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Feb 10 18:34:24 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 February 2015.