# SPEC® CINT2006 Result

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## Lenovo Group Limited

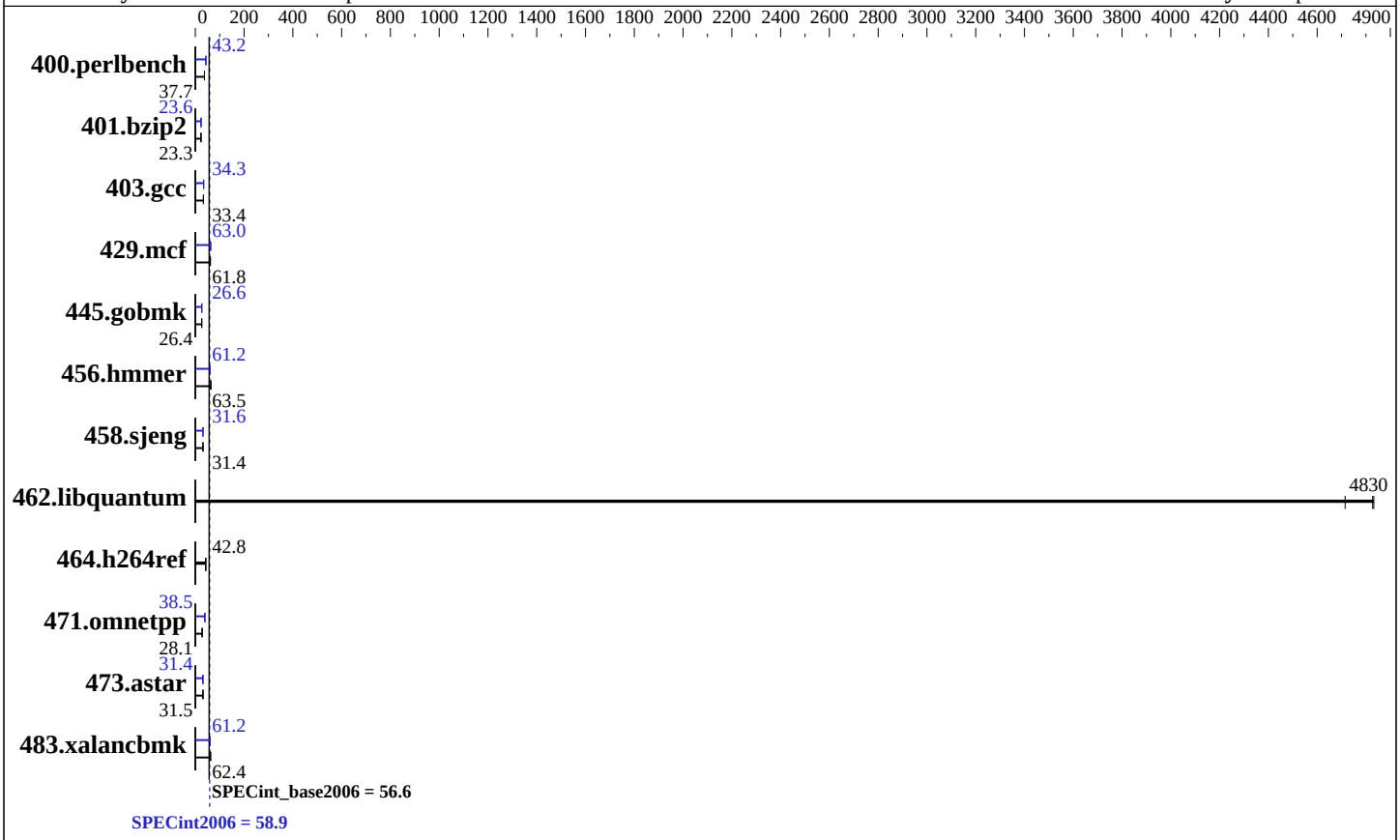
Lenovo ThinkServer RD450 (Intel Xeon E5-2630 v3, 2.40 GHz)

SPECint®2006 = 58.9

SPECint_base2006 = 56.6

**CPU2006 license:** 9017
**Test sponsor:** Lenovo Group Limited
**Tested by:** Lenovo Group Limited

**Test date:** Nov-2014
**Hardware Availability:** Dec-2014
**Software Availability:** Sep-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2630 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.20 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz) |
| Disk Subsystem: | 1 x 800 GB SATA SSD |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.0 (Maipo)<br>3.10.0-123.el7.x86_64 |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **259** | **37.7** | 260 | 37.6 | 259 | 37.7 | 225 | 43.4 | **226** | **43.2** | 227 | 43.1 |
| 401.bzip2 | **414** | **23.3** | 413 | 23.4 | 414 | 23.3 | **409** | **23.6** | 409 | 23.6 | 409 | 23.6 |
| 403.gcc | 241 | 33.4 | 241 | 33.4 | **241** | **33.4** | 235 | 34.3 | 235 | 34.2 | **235** | **34.3** |
| 429.mcf | **148** | **61.8** | 148 | 61.8 | 147 | 61.9 | **145** | **63.0** | 145 | 63.1 | 146 | 62.3 |
| 445.gobmk | **398** | **26.4** | 397 | 26.4 | 399 | 26.3 | **395** | **26.6** | 395 | 26.6 | 395 | 26.5 |
| 456.hmmer | **147** | **63.5** | 147 | 63.5 | 147 | 63.5 | **153** | **61.2** | 154 | 60.8 | 152 | 61.2 |
| 458.sjeng | 385 | 31.4 | 385 | 31.4 | **385** | **31.4** | **383** | **31.6** | 383 | 31.6 | 383 | 31.6 |
| 462.libquantum | **4.29** | **4830** | 4.29 | 4830 | 4.39 | 4710 | **4.29** | **4830** | 4.29 | 4830 | 4.39 | 4710 |
| 464.h264ref | 518 | 42.7 | **517** | **42.8** | 516 | 42.9 | 518 | 42.7 | **517** | **42.8** | 516 | 42.9 |
| 471.omnetpp | 222 | 28.2 | 224 | 27.9 | **223** | **28.1** | **162** | **38.5** | 163 | 38.3 | 155 | 40.3 |
| 473.astar | **223** | **31.5** | 225 | 31.1 | 223 | 31.6 | 226 | 31.0 | 223 | 31.5 | **224** | **31.4** |
| 483.xalancbmk | 110 | 62.6 | **110** | **62.4** | 110 | 62.4 | 112 | 61.3 | **113** | **61.2** | 113 | 61.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Hyper-Threading set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
 Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on RD450 Fri Nov  7 18:02:56 2014

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2630 v3 @ 2.40GHz
       2 "physical id"s (chips)
```

Continued on next page

## Platform Notes (Continued)

```
        16 "processors"
     cores, siblings (Caution: counting these is hw and system dependent.  The
     following excerpts from /proc/cpuinfo might not be reliable.  Use with
     caution.)
        cpu cores : 8
        siblings  : 8
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
     cache size : 20480 KB

  From /proc/meminfo
     MemTotal:       263860132 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.0 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="7.0"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
        ANSI_COLOR="0;31"
        CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
     redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
     system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

  uname -a:
     Linux RD450 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
     x86_64 x86_64 GNU/Linux

  run-level 3 Nov 7 18:01

  SPEC is set to: /usr/cpu2006
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda2      xfs   741G   84G  658G  12% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS LENOVO VB3TS110 10/05/2014
    Memory:
     16x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1866 MHz

  (End of data from sysinfo program)
RD450 support 4 channels and 8 DIMMs per processor, total 8 channels and
16 DIMMs. All 16 DIMM slots installed with 16 GB DIMM for this run.
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32 -opt-prefetch -ansi-alias`

Continued on next page

## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Feb 10 18:33:05 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 February 2015.