



SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2630L v3, 1.8 GHz

SPECfp®_rate2006 = 482

SPECfp_rate_base2006 = 470

CPU2006 license: 19

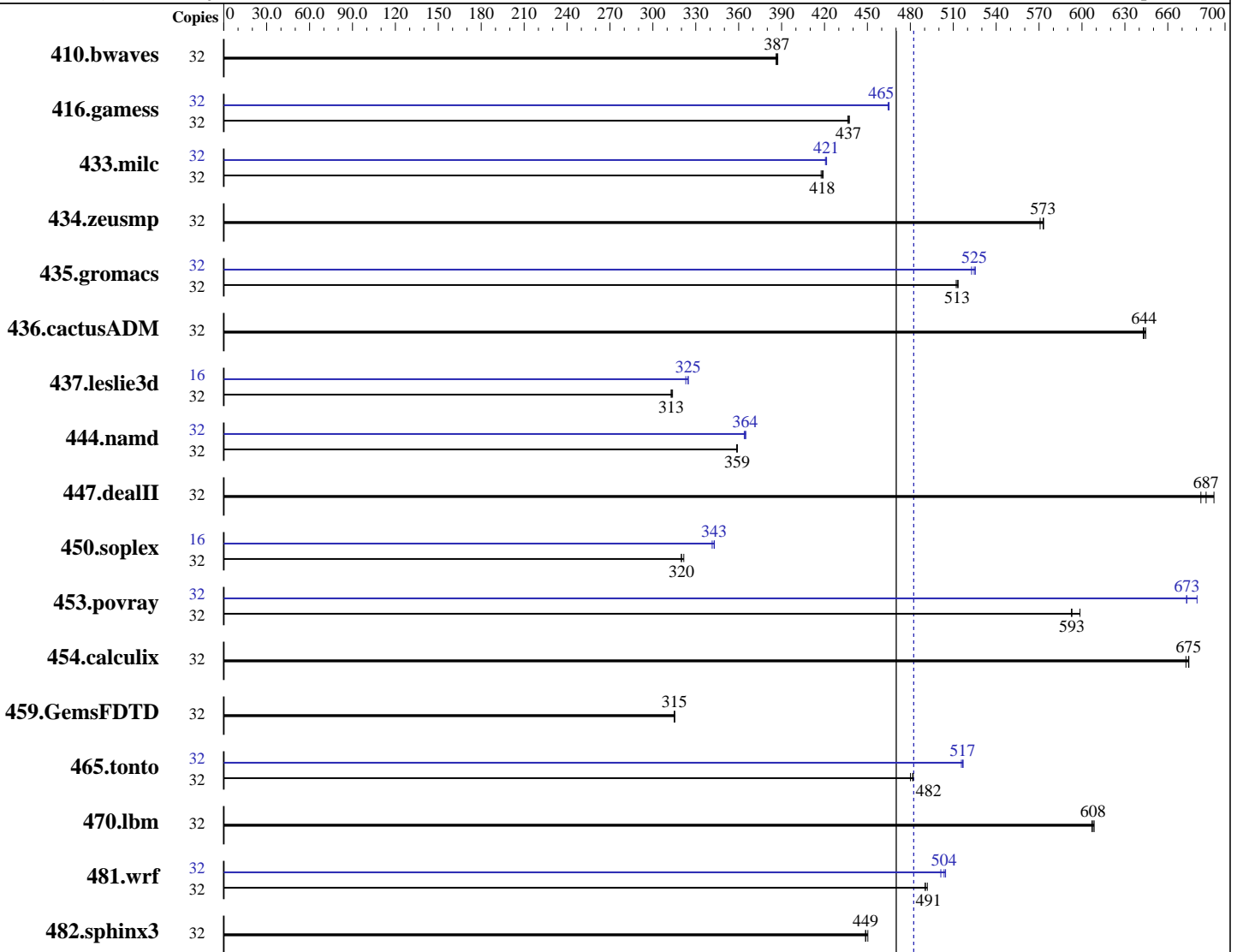
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



SPECfp_rate_base2006 = 470

SPECfp_rate2006 = 482

Hardware

CPU Name: Intel Xeon E5-2630L v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 Kernel 3.10.0-123.8.1.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2630L v3, 1.8 GHz

SPECfp_rate2006 = 482

SPECfp_rate_base2006 = 470

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 32 | 1123 | 387 | 1126 | 386 | 1124 | 387 | 32 | 1123 | 387 | 1126 | 386 | 1124 | 387 | | |
| 416.gamess | 32 | 1436 | 436 | 1432 | 437 | 1434 | 437 | 32 | 1347 | 465 | 1349 | 464 | 1347 | 465 | | |
| 433.milc | 32 | 701 | 419 | 703 | 418 | 702 | 418 | 32 | 698 | 421 | 697 | 421 | 697 | 421 | | |
| 434.zeusmp | 32 | 508 | 573 | 508 | 573 | 510 | 571 | 32 | 508 | 573 | 508 | 573 | 510 | 571 | | |
| 435.gromacs | 32 | 446 | 513 | 445 | 513 | 446 | 512 | 32 | 437 | 523 | 435 | 525 | 435 | 525 | | |
| 436.cactusADM | 32 | 593 | 645 | 594 | 644 | 595 | 643 | 32 | 593 | 645 | 594 | 644 | 595 | 643 | | |
| 437.leslie3d | 32 | 961 | 313 | 962 | 313 | 959 | 314 | 16 | 463 | 325 | 463 | 325 | 466 | 323 | | |
| 444.namd | 32 | 715 | 359 | 715 | 359 | 716 | 359 | 32 | 703 | 365 | 705 | 364 | 704 | 364 | | |
| 447.dealII | 32 | 529 | 692 | 533 | 687 | 536 | 683 | 32 | 529 | 692 | 533 | 687 | 536 | 683 | | |
| 450.soplex | 32 | 834 | 320 | 834 | 320 | 830 | 322 | 16 | 389 | 343 | 391 | 341 | 389 | 343 | | |
| 453.povray | 32 | 287 | 593 | 287 | 593 | 284 | 599 | 32 | 253 | 673 | 250 | 680 | 253 | 673 | | |
| 454.calculix | 32 | 392 | 673 | 391 | 675 | 391 | 675 | 32 | 392 | 673 | 391 | 675 | 391 | 675 | | |
| 459.GemsFDTD | 32 | 1078 | 315 | 1077 | 315 | 1078 | 315 | 32 | 1078 | 315 | 1077 | 315 | 1078 | 315 | | |
| 465.tonto | 32 | 653 | 482 | 656 | 480 | 654 | 482 | 32 | 609 | 517 | 609 | 517 | 611 | 516 | | |
| 470.lbm | 32 | 723 | 608 | 723 | 609 | 724 | 607 | 32 | 723 | 608 | 723 | 609 | 724 | 607 | | |
| 481.wrf | 32 | 729 | 491 | 729 | 490 | 727 | 492 | 32 | 713 | 501 | 708 | 505 | 710 | 504 | | |
| 482.sphinx3 | 32 | 1385 | 450 | 1390 | 449 | 1390 | 449 | 32 | 1385 | 450 | 1390 | 449 | 1390 | 449 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2630L v3, 1.8 GHz

SPECfp_rate2006 = 482

SPECfp_rate_base2006 = 470

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Platform Notes (Continued)

QPI snoop mode: Early Snoop
COD Enable = Disabled, Early Snoop = Enabled
CPU C1E Support = Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

This result was measured on the PRIMERGY CX2550 M1. The PRIMERGY CX2550 M1 and the PRIMERGY CX2570 M1 are electronically equivalent.
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2630L v3, 1.8 GHz

SPECfp_rate2006 = 482

SPECfp_rate_base2006 = 470

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Base Portability Flags (Continued)

```
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2630L v3, 1.8 GHz

SPECfp_rate2006 = 482

SPECfp_rate_base2006 = 470

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Nov-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -ansi-alias

```

Fortran benchmarks:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2630L v3, 1.8 GHz

SPECfp_rate2006 = 482

SPECfp_rate_base2006 = 470

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Nov-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 27 13:33:23 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 January 2015.