Huawei RH1288 V3 (Intel Xeon E5-2650 v3)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Hardware

CPU Name: Intel Xeon E5-2650 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1.2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler: C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

SPECint®_rate2006 = 856
SPECint_rate_base2006 = 821

Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Jun-2014
**SPEC CINT2006 Result**

**Huawei**

Huawei RH1288 V3 (Intel Xeon E5-2650 v3)

**CPU2006 license:** 3175  
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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>40</td>
<td>665</td>
<td>588</td>
<td>665</td>
<td>588</td>
<td>666</td>
<td>587</td>
<td>40</td>
<td>527</td>
<td>742</td>
<td>530</td>
<td>738</td>
<td>528</td>
<td>741</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>40</td>
<td>965</td>
<td>400</td>
<td>966</td>
<td>400</td>
<td>968</td>
<td>399</td>
<td>40</td>
<td>918</td>
<td>420</td>
<td>921</td>
<td>419</td>
<td>922</td>
<td>419</td>
</tr>
<tr>
<td>403.gcc</td>
<td>40</td>
<td>497</td>
<td>647</td>
<td>497</td>
<td>647</td>
<td>496</td>
<td>649</td>
<td>40</td>
<td>497</td>
<td>648</td>
<td>497</td>
<td>648</td>
<td>498</td>
<td>647</td>
</tr>
<tr>
<td>429.mcf</td>
<td>40</td>
<td>319</td>
<td>1140</td>
<td>320</td>
<td>1140</td>
<td>318</td>
<td>1150</td>
<td>40</td>
<td>319</td>
<td>1140</td>
<td>320</td>
<td>1140</td>
<td>318</td>
<td>1150</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>40</td>
<td>772</td>
<td>544</td>
<td>774</td>
<td>542</td>
<td>772</td>
<td>543</td>
<td>40</td>
<td>766</td>
<td>548</td>
<td>767</td>
<td>547</td>
<td>769</td>
<td>546</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>40</td>
<td>317</td>
<td>1180</td>
<td>313</td>
<td>1190</td>
<td>317</td>
<td>1180</td>
<td>40</td>
<td>286</td>
<td>1310</td>
<td>287</td>
<td>1300</td>
<td>287</td>
<td>1300</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>40</td>
<td>845</td>
<td>573</td>
<td>844</td>
<td>573</td>
<td>845</td>
<td>573</td>
<td>40</td>
<td>809</td>
<td>598</td>
<td>809</td>
<td>598</td>
<td>809</td>
<td>598</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>40</td>
<td>102</td>
<td>8140</td>
<td>102</td>
<td>8130</td>
<td>102</td>
<td>8120</td>
<td>40</td>
<td>102</td>
<td>8140</td>
<td>102</td>
<td>8130</td>
<td>102</td>
<td>8120</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>40</td>
<td>962</td>
<td>920</td>
<td>959</td>
<td>923</td>
<td>957</td>
<td>925</td>
<td>40</td>
<td>929</td>
<td>953</td>
<td>948</td>
<td>934</td>
<td>938</td>
<td>944</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>40</td>
<td>533</td>
<td>469</td>
<td>530</td>
<td>471</td>
<td>533</td>
<td>469</td>
<td>40</td>
<td>513</td>
<td>488</td>
<td>508</td>
<td>492</td>
<td>509</td>
<td>491</td>
</tr>
<tr>
<td>473.astar</td>
<td>40</td>
<td>607</td>
<td>462</td>
<td>607</td>
<td>463</td>
<td>611</td>
<td>460</td>
<td>40</td>
<td>607</td>
<td>462</td>
<td>607</td>
<td>463</td>
<td>611</td>
<td>460</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>40</td>
<td>304</td>
<td>907</td>
<td>305</td>
<td>906</td>
<td>304</td>
<td>907</td>
<td>40</td>
<td>304</td>
<td>907</td>
<td>305</td>
<td>906</td>
<td>304</td>
<td>907</td>
</tr>
</tbody>
</table>

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**Submit Notes**

The `numactl` mechanism was used to bind copies to processors. The config file option 'submit' was used to generate `numactl` commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**Platform Notes**

BIOS configuration:
- Set Power Efficiency Mode to Custom
- Set Snoop Mode to COD
- Set Patrol Scrub to Disable
- Baseboard Management Controller used to adjust the fan speed to 100%

Baseboard:
- Model: Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
- 2 "physical id"s (chips)
- 40 "processors"

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From `/proc/cpuinfo`
- model name : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
- 2 "physical id"s (chips)
- 40 "processors"

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 5
    siblings : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 12800 KB

From /proc/meminfo
    MemTotal:       263718200 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
    os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.0 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="7.0"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
        ANSI_COLOR="0;31"
        CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
    redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
    system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
    Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
    EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 6 20:09

SPEC is set to: /spec15
    Filesystem    Type   Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-root  ext4  241G 114G 114G 51% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.18 09/17/2014
Memory:
    8x Samsung M393A2G40DB0-CPB 16 GB 1 rank 2133 MHz
    8x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
Huawei
Huawei RH1288 V3 (Intel Xeon E5-2650 v3) SPECint_rate2006 = 856
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CPU2006 license: 3175
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Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Jun-2014

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

- Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
- Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
- Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
- runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca
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### Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

---

### Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

---

### Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: basepeak = yes

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

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Huawei RH1288 V3 (Intel Xeon E5-2650 v3)  

SPECint\_rate2006 = 856  
SPECint\_rate\_base2006 = 821

<table>
<thead>
<tr>
<th>SPEC CINT2006 Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>Huawei</td>
</tr>
<tr>
<td>SPECint_rate2006</td>
</tr>
<tr>
<td>SPECint_rate_base2006</td>
</tr>
<tr>
<td>CPU2006 license: 3175</td>
</tr>
<tr>
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</tr>
<tr>
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</tr>
</tbody>
</table>

**Huawei**

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**Peak Optimization Flags (Continued)**

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

---

**C++ benchmarks:**

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-rt-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

---

**Peak Other Flags**

**C benchmarks:**

403.gcc: -Dalloca=_alloca

---

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html  
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.2.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml  
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.2.xml

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