



# SPEC® CFP2006 Result

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Huawei

**SPECfp®2006 = 105**

Huawei RH2288 V3 (Intel Xeon E5-2640 v3)

**SPECfp\_base2006 = 99.5**

CPU2006 license: 3175

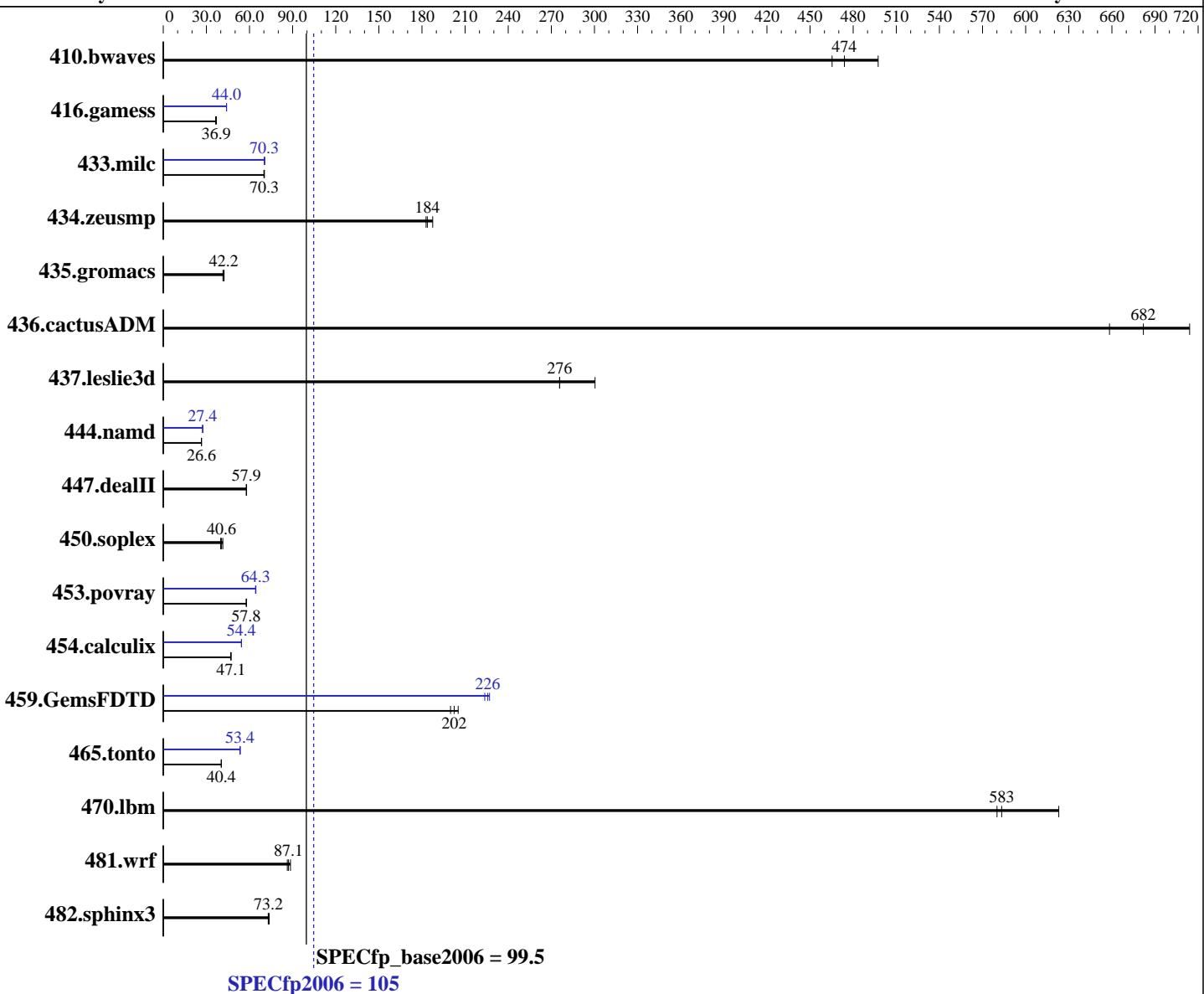
Test date: Dec-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Jun-2014



## Hardware

CPU Name: Intel Xeon E5-2640 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

## Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Compiler: 3.10.0-123.el7.x86\_64  
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs

Continued on next page



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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>28.7</b>	<b>474</b>	29.2	465	27.3	497	<b>28.7</b>	<b>474</b>	29.2	465	27.3	497
416.gamess	538	36.4	<b>531</b>	<b>36.9</b>	529	37.0	<b>445</b>	<b>44.0</b>	445	44.0	445	44.0
433.milc	131	70.0	<b>131</b>	<b>70.3</b>	131	70.3	<b>131</b>	<b>70.2</b>	<b>131</b>	<b>70.3</b>	130	70.7
434.zeusmp	48.5	187	49.8	183	<b>49.5</b>	<b>184</b>	48.5	187	49.8	183	<b>49.5</b>	<b>184</b>
435.gromacs	172	41.5	169	42.3	<b>169</b>	<b>42.2</b>	172	41.5	169	42.3	<b>169</b>	<b>42.2</b>
436.cactusADM	<b>17.5</b>	<b>682</b>	18.2	658	16.7	714	<b>17.5</b>	<b>682</b>	18.2	658	16.7	714
437.leslie3d	31.3	300	34.1	276	<b>34.1</b>	<b>276</b>	31.3	300	34.1	276	<b>34.1</b>	<b>276</b>
444.namd	<b>302</b>	<b>26.6</b>	302	26.6	302	26.6	<b>292</b>	<b>27.4</b>	<b>292</b>	<b>27.4</b>	292	27.4
447.dealII	<b>197</b>	<b>57.9</b>	198	57.7	197	58.0	<b>197</b>	<b>57.9</b>	198	57.7	197	58.0
450.soplex	201	41.4	209	39.9	<b>206</b>	<b>40.6</b>	201	41.4	209	39.9	<b>206</b>	<b>40.6</b>
453.povray	92.1	57.7	<b>92.1</b>	<b>57.8</b>	91.8	57.9	<b>82.7</b>	<b>64.3</b>	82.9	64.2	82.5	64.5
454.calculix	176	46.9	175	47.2	<b>175</b>	<b>47.1</b>	<b>152</b>	<b>54.4</b>	152	54.4	152	54.4
459.GemsFDTD	<b>52.4</b>	<b>202</b>	51.7	205	53.1	200	46.7	227	<b>47.0</b>	<b>226</b>	47.4	224
465.tonto	<b>244</b>	<b>40.4</b>	243	40.4	244	40.3	184	53.4	184	53.5	<b>184</b>	<b>53.4</b>
470.lbm	<b>23.6</b>	<b>583</b>	22.1	623	23.7	580	<b>23.6</b>	<b>583</b>	22.1	623	23.7	580
481.wrf	129	86.3	126	88.5	<b>128</b>	<b>87.1</b>	129	86.3	126	88.5	<b>128</b>	<b>87.1</b>
482.sphinx3	264	73.7	<b>266</b>	<b>73.2</b>	267	73.0	<b>264</b>	<b>73.7</b>	<b>266</b>	<b>73.2</b>	267	73.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to HS

Set Hyper-Threading to Disabled

Set Patrol Scrub to Disable

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec14/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on localhost.localdomain Tue Dec 2 12:28:29 2014

Continued on next page



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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 v3 @ 2.60GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 8
    siblings   : 8
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      263721496 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Dec 2 07:34

```
SPEC is set to: /spec14
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda1        xfs   98G   17G   81G  18%  /
```

Additional information from dmidecode:

BIOS Insyde Corp. 1.16 09/02/2014

Memory:

8x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 1 rank
8x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 2 rank

Continued on next page



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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec14/libs/32:/spec14/libs/64:/spec14/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

    icc -m64

C++ benchmarks:

    icpc -m64

Fortran benchmarks:

    ifort -m64

Benchmarks using both Fortran and C:

    icc -m64 ifort -m64

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
```

Continued on next page



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## Base Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

Continued on next page



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## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.2.xml>

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