



# SPEC® CINT2006 Result

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## Supermicro

SuperServer F628R3-R72BPT+  
(X10DRFR-T , Intel Xeon E5-2643 v3)

**SPECint®2006 = 65.6**

**SPECint\_base2006 = 62.3**

**CPU2006 license:** 001176

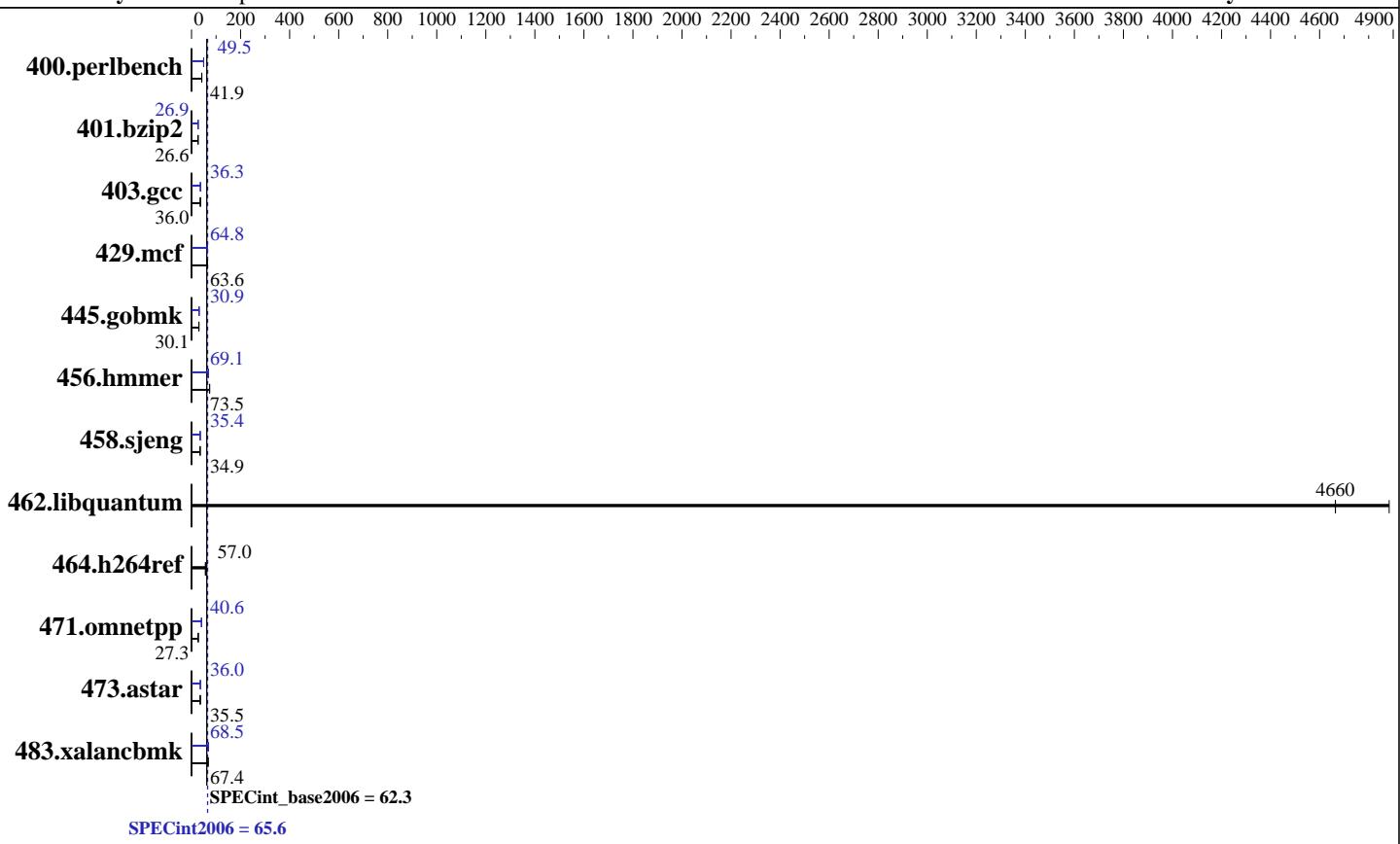
**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** Nov-2014

**Hardware Availability:** Nov-2014

**Software Availability:** Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2643 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
CPU MHz: 3400  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 1000 GB SATA III, 7200 RPM  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5, Kernel 2.6.32-431.23.3.el6.x86\_64  
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	234	41.7	<u>233</u>	<b>41.9</b>	233	41.9	197	49.5	198	49.4	<u>198</u>	<b>49.5</b>
401.bzip2	<b>363</b>	<b>26.6</b>	362	26.6	363	26.6	<u>359</u>	<b>26.9</b>	<u>359</u>	<b>26.9</b>	358	26.9
403.gcc	224	36.0	<b>224</b>	<b>36.0</b>	224	35.9	<u>221</u>	<b>36.3</b>	222	36.3	221	36.4
429.mcf	141	64.8	144	63.5	<u>143</u>	<b>63.6</b>	141	64.6	140	65.1	<u>141</u>	<b>64.8</b>
445.gobmk	<b>349</b>	<b>30.1</b>	349	30.0	348	30.1	339	30.9	340	30.9	<u>339</u>	<b>30.9</b>
456.hmmer	126	74.0	<u>127</u>	<b>73.5</b>	127	73.5	<u>135</u>	69.1	137	67.9	<u>135</u>	<b>69.1</b>
458.sjeng	<u>346</u>	<b>34.9</b>	346	34.9	346	35.0	<u>342</u>	35.4	<u>342</u>	<b>35.4</b>	342	35.4
462.libquantum	<b>4.44</b>	<b>4660</b>	4.44	4660	4.24	4880	<b>4.44</b>	<b>4660</b>	4.44	4660	4.24	4880
464.h264ref	<u>388</u>	<b>57.0</b>	389	56.8	388	57.1	<u>388</u>	<b>57.0</b>	389	56.8	388	57.1
471.omnetpp	228	27.4	<u>229</u>	<b>27.3</b>	230	27.2	<u>154</u>	<b>40.6</b>	154	40.6	154	40.7
473.astar	198	35.5	197	35.7	<u>198</u>	<b>35.5</b>	194	36.2	196	35.9	<u>195</u>	<b>36.0</b>
483.xalancbmk	<b>102</b>	<b>67.4</b>	102	67.5	103	67.3	<u>99.8</u>	69.1	101	68.1	<u>101</u>	<b>68.5</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Enforce POR - Disable

Memory Frequency - 2133

Early Snoop - Disable

COD Enable - Disable

Hyper-Threading (ALL) - Disable

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/root/cpu2006/libs/32:/root/cpu2006/libs/64:/root/cpu2006/sh"

OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enable

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## General Notes (Continued)

runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

    icc -m64

C++ benchmarks:

    icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

    -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
    -Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32  
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revF.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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