# SPEC® CINT2006 Result

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## Hewlett-Packard Company

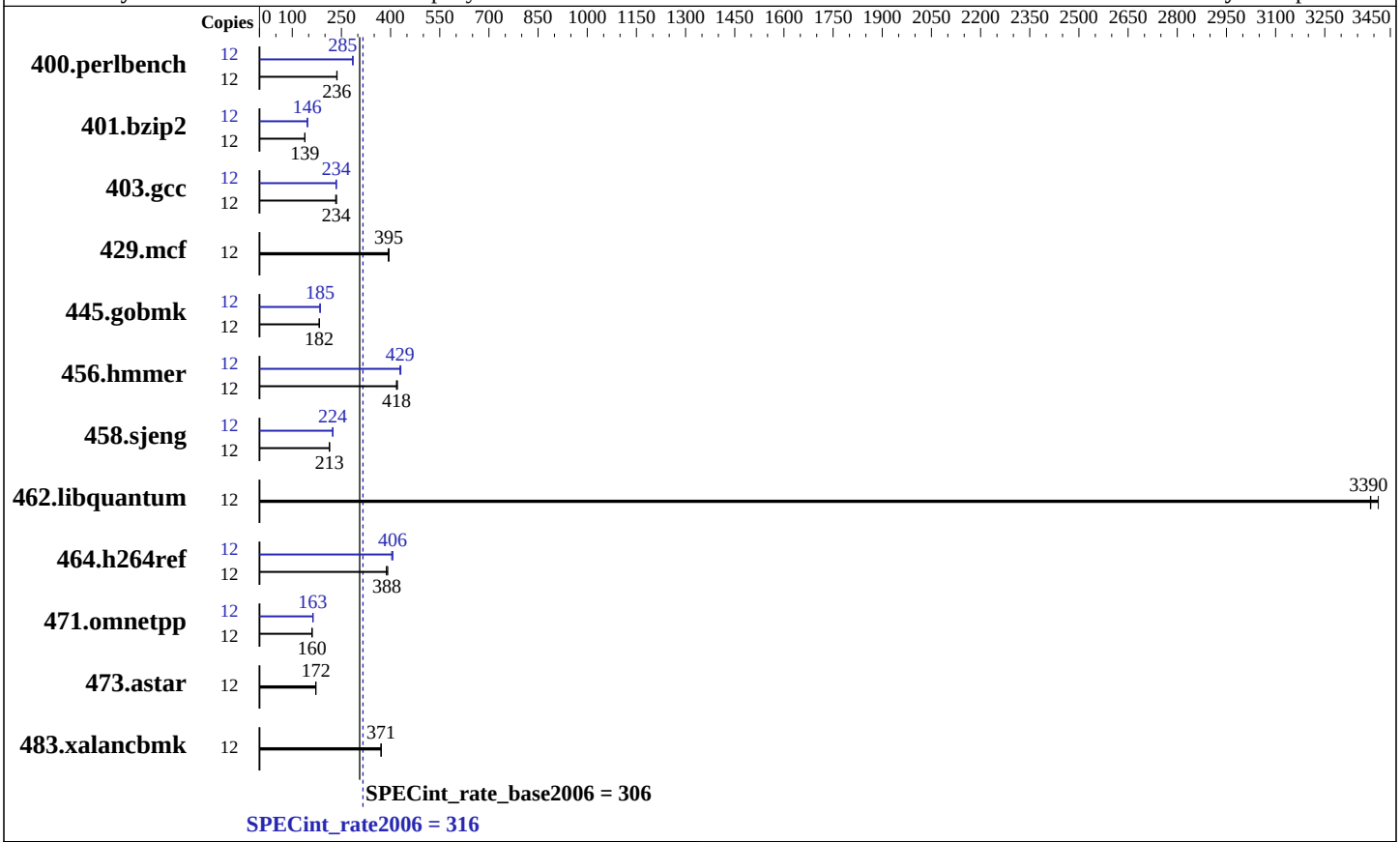
ProLiant DL360 Gen9
(1.90 GHz, Intel Xeon E5-2609 v3)

SPECint®_rate2006 = 316

SPECint_rate_base2006 = 306

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Nov-2014
**Hardware Availability:** Sep-2014
**Software Availability:** Sep-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2609 v3 |
| CPU Characteristics: | |
| CPU MHz: | 1900 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (16 x 8 GB 1Rx4 PC4-2133P-R, running at 1600 MHz) |
| Disk Subsystem: | 1 x 400 GB SSD SAS, RAID 0 |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.0 (Maipo)<br>Kernel 3.10.0-123.el7.x86_64 |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 12 | 497 | 236 | **496** | **236** | 495 | 237 | 12 | 412 | 285 | 411 | 285 | **412** | **285** |
| 401.bzip2 | 12 | **836** | **139** | 835 | 139 | 837 | 138 | 12 | 791 | 146 | **791** | **146** | 792 | 146 |
| 403.gcc | 12 | 412 | 235 | 416 | 232 | **412** | **234** | 12 | 412 | 235 | 413 | 234 | **412** | **234** |
| 429.mcf | 12 | **277** | **395** | 279 | 393 | 277 | 395 | 12 | **277** | **395** | 279 | 393 | 277 | 395 |
| 445.gobmk | 12 | 689 | 183 | 690 | 182 | **690** | **182** | 12 | **681** | **185** | 681 | 185 | 681 | 185 |
| 456.hmmer | 12 | 266 | 421 | 268 | 418 | **268** | **418** | 12 | **261** | **429** | 260 | 431 | 261 | 429 |
| 458.sjeng | 12 | 680 | 214 | **680** | **213** | 680 | 213 | 12 | **649** | **224** | 650 | 223 | 649 | 224 |
| 462.libquantum | 12 | 72.8 | 3410 | **73.3** | **3390** | 73.4 | 3390 | 12 | 72.8 | 3410 | **73.3** | **3390** | 73.4 | 3390 |
| 464.h264ref | 12 | 686 | 387 | 678 | 391 | **684** | **388** | 12 | 658 | 403 | 653 | 406 | **654** | **406** |
| 471.omnetpp | 12 | 469 | 160 | 467 | 161 | **468** | **160** | 12 | 459 | 163 | 460 | 163 | **460** | **163** |
| 473.astar | 12 | 490 | 172 | **490** | **172** | 491 | 172 | 12 | 490 | 172 | **490** | **172** | 491 | 172 |
| 483.xalancbmk | 12 | 223 | 371 | **223** | **371** | 223 | 371 | 12 | 223 | 371 | **223** | **371** | 223 | 371 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages disabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Platform Notes

```
 BIOS Configuration:
 HP Power Profile set to Custom
 HP Power Regulator to HP Static High Performance Mode
 Minimum Processor Idle Power Core State set to C6 State
 Minimum Processor Idle Power Package State set to Package C6 (non-retention) State
 QPI Snoop Configuration set to Early Snoop
 Thermal Configuration set to Maximum Cooling
 Collaborative Power Control set to Disabled
 Processor Power and Utilization Monitoring set to Disabled
 Memory Double Refresh Rate set to 1x Refresh
```

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## Platform Notes (Continued)

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on DL360-G9 Mon Nov  3 17:25:49 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz
      2 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 6
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       131734972 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux DL360-G9 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 3 17:13

SPEC is set to: /home/cpu2006
   Filesystem                     Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel_dl360--g9-home xfs   318G  156G  163G  49% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
```

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## Platform Notes (Continued)

```
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HP P89 07/11/2014
    Memory:
     16x HP NOT AVAILABLE 8 GB 1 rank 2133 MHz, configured at 1600 MHz
     8x UNKNOWN NOT AVAILABLE

  (End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 128 GB and the dmidecode description should have one line reading as:
 16x HP NOT AVAILABLE 8 GB 1 rank 2133 MHz, configured at 1600 MHz
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-auto-ilp32`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

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# Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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