



# SPEC® CFP2006 Result

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## Hewlett-Packard Company

SPECfp®\_rate2006 = **697**

ProLiant DL80 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

SPECfp\_rate\_base2006 = **678**

CPU2006 license: 3

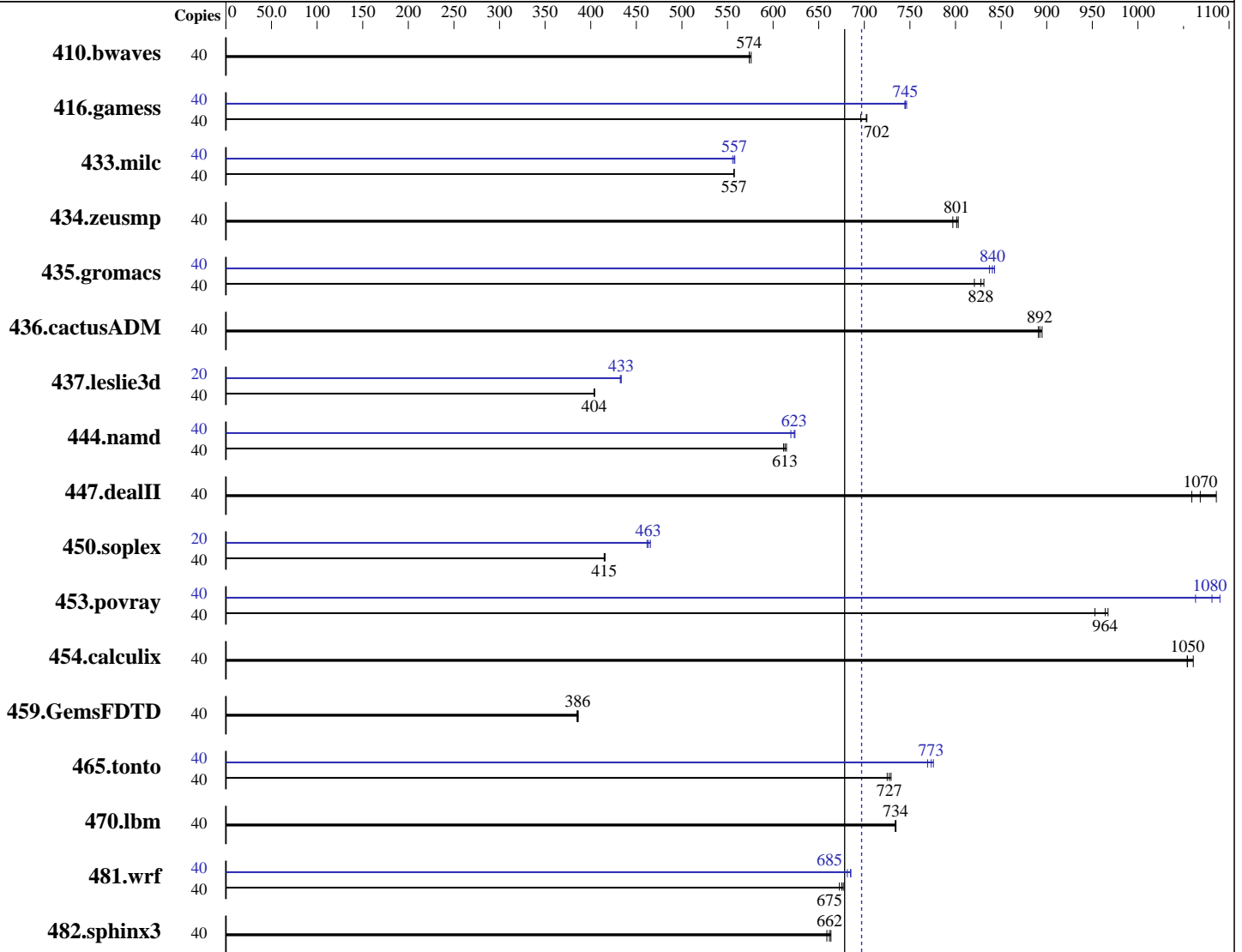
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014



SPECfp\_rate\_base2006 = 678

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### Hardware

CPU Name: Intel Xeon E5-2660 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

Continued on next page



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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 400 GB SATA SSD, RAID 0  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base   |            |             |             |            |             |             | Peak   |            |             |             |             |            |             |
|---------------|--------|------------|-------------|-------------|------------|-------------|-------------|--------|------------|-------------|-------------|-------------|------------|-------------|
|               | Copies | Seconds    | Ratio       | Seconds     | Ratio      | Seconds     | Ratio       | Copies | Seconds    | Ratio       | Seconds     | Ratio       | Seconds    | Ratio       |
| 410.bwaves    | 40     | 947        | 574         | <b>947</b>  | <b>574</b> | 944         | 576         | 40     | 947        | 574         | <b>947</b>  | <b>574</b>  | 944        | 576         |
| 416.gamess    | 40     | 1125       | 696         | 1114        | 703        | <b>1115</b> | <b>702</b>  | 40     | 1049       | 746         | <b>1051</b> | <b>745</b>  | 1052       | 744         |
| 433.milc      | 40     | 659        | 557         | <b>659</b>  | <b>557</b> | 659         | 557         | 40     | 661        | 556         | <b>659</b>  | <b>557</b>  | 658        | 558         |
| 434.zeusmp    | 40     | <b>454</b> | <b>801</b>  | 457         | 797        | 453         | 803         | 40     | <b>454</b> | <b>801</b>  | 457         | 797         | 453        | 803         |
| 435.gromacs   | 40     | 344        | 831         | 348         | 821        | <b>345</b>  | <b>828</b>  | 40     | 339        | 843         | 341         | 837         | <b>340</b> | <b>840</b>  |
| 436.cactusADM | 40     | 537        | 891         | <b>536</b>  | <b>892</b> | 534         | 895         | 40     | 537        | 891         | <b>536</b>  | <b>892</b>  | 534        | 895         |
| 437.leslie3d  | 40     | 930        | 404         | 931         | 404        | <b>931</b>  | <b>404</b>  | 20     | <b>434</b> | <b>433</b>  | 435         | 432         | 434        | 433         |
| 444.namd      | 40     | <b>523</b> | <b>613</b>  | 525         | 611        | 522         | 615         | 40     | <b>515</b> | <b>623</b>  | 518         | 619         | 514        | 624         |
| 447.dealII    | 40     | <b>428</b> | <b>1070</b> | 432         | 1060       | 421         | 1090        | 40     | <b>428</b> | <b>1070</b> | 432         | 1060        | 421        | 1090        |
| 450.soplex    | 40     | 802        | 416         | <b>804</b>  | <b>415</b> | 804         | 415         | 20     | 358        | 465         | 361         | 462         | <b>360</b> | <b>463</b>  |
| 453.povray    | 40     | <b>221</b> | <b>964</b>  | 220         | 967        | 223         | 953         | 40     | 200        | 1060        | <b>197</b>  | <b>1080</b> | 195        | 1090        |
| 454.calculix  | 40     | 313        | 1050        | 311         | 1060       | <b>313</b>  | <b>1050</b> | 40     | 313        | 1050        | 311         | 1060        | <b>313</b> | <b>1050</b> |
| 459.GemsFDTD  | 40     | 1099       | 386         | <b>1100</b> | <b>386</b> | 1102        | 385         | 40     | 1099       | 386         | <b>1100</b> | <b>386</b>  | 1102       | 385         |
| 465.tonto     | 40     | <b>541</b> | <b>727</b>  | 540         | 729        | 543         | 725         | 40     | 507        | 776         | 512         | 769         | <b>509</b> | <b>773</b>  |
| 470.lbm       | 40     | 748        | 735         | <b>748</b>  | <b>734</b> | 749         | 734         | 40     | 748        | 735         | <b>748</b>  | <b>734</b>  | 749        | 734         |
| 481.wrf       | 40     | 660        | 677         | 664         | 673        | <b>662</b>  | <b>675</b>  | 40     | <b>652</b> | <b>685</b>  | 656         | 681         | 652        | 685         |
| 482.sphinx3   | 40     | 1175       | 663         | <b>1178</b> | <b>662</b> | 1183        | 659         | 40     | 1175       | 663         | <b>1178</b> | <b>662</b>  | 1183       | 659         |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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ProLiant DL80 Gen9  
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SPECfp\_rate\_base2006 = 678

**CPU2006 license:** 3  
**Test sponsor:** Hewlett-Packard Company  
**Tested by:** Hewlett-Packard Company

**Test date:** Oct-2014  
**Hardware Availability:** Sep-2014  
**Software Availability:** Sep-2014

### Platform Notes

#### BIOS Configuration:

HP Power Profile set to Custom  
HP Power Regulator set to HP Static High Performance Mode  
Minimum Processor Idle Power Package C-State set to No Package State  
Energy/Performance Bias set to Maximum Performance  
QPI Snoop Configuration set to Cluster on Die  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on DL80-Gen9 Mon Oct 27 20:23:21 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.60GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 5
  siblings  : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 12800 KB
```

```
From /proc/meminfo
MemTotal: 131600496 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux DL80-Gen9 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
Continued on next page
```



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## Platform Notes (Continued)

x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Oct 27 09:30

SPEC is set to: /cpu2006

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda4  | xfs  | 368G | 11G  | 357G  | 3%   | /          |

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U15 08/26/2014

Memory:

8x HP 752369-081 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

Continued on next page



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## Base Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

```

Fortran benchmarks:

```

ifort -m64

```

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

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## Peak Optimization Flags (Continued)

450.soplex (continued):

-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2)

-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4

-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2

-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2)

-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)

-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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