SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

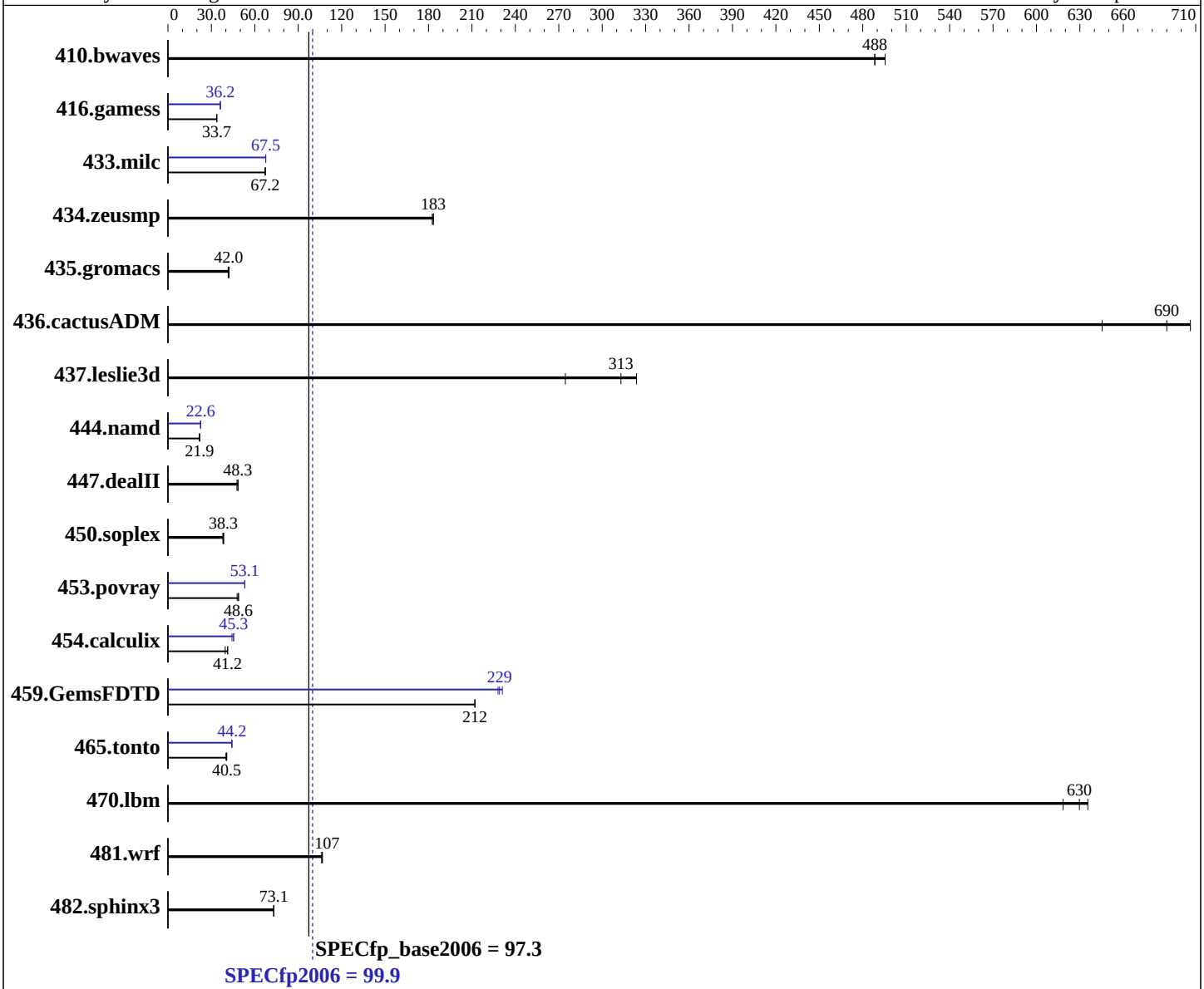
Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECfp®2006 = 99.9

SPECfp_base2006 = 97.3

CPU2006 license: 9046
Test sponsor: Sugon
Tested by: Sugon

Test date: Sep-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2640 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECfp2006 = 99.9

SPECfp_base2006 = 97.3

CPU2006 license: 9046
Test sponsor: Sugon
Tested by: Sugon

Test date: Sep-2014
Hardware Availability: Sep-2014
Software Availability: Sep-2013

L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x 2.0 TB SATA 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	27.8	488	27.4	495	**27.8**	**488**	27.8	488	27.4	495	**27.8**	**488**
416.gamess	581	33.7	**580**	**33.7**	580	33.7	541	36.2	**541**	**36.2**	542	36.1
433.milc	136	67.3	**137**	**67.2**	137	67.2	136	67.5	**136**	**67.5**	136	67.6
434.zeusmp	49.8	183	49.6	183	**49.6**	**183**	49.8	183	49.6	183	**49.6**	**183**
435.gromacs	172	41.6	**170**	**42.0**	169	42.3	172	41.6	**170**	**42.0**	169	42.3
436.cactusADM	18.5	645	16.9	706	**17.3**	**690**	18.5	645	16.9	706	**17.3**	**690**
437.leslie3d	34.2	275	**30.0**	**313**	29.0	324	34.2	275	**30.0**	**313**	29.0	324
444.namd	367	21.9	367	21.9	**367**	**21.9**	**356**	**22.6**	355	22.6	356	22.6
447.dealII	**237**	**48.3**	236	48.4	240	47.7	**237**	**48.3**	236	48.4	240	47.7
450.soplex	**218**	**38.3**	217	38.5	220	37.9	**218**	**38.3**	217	38.5	220	37.9
453.povray	**109**	**48.6**	111	47.8	109	48.8	100	53.2	100	53.0	**100**	**53.1**
454.calculix	200	41.2	**200**	**41.2**	209	39.6	182	45.4	**182**	**45.3**	187	44.2
459.GemsFDTD	50.0	212	**50.0**	**212**	50.0	212	45.9	231	**46.3**	**229**	46.5	228
465.tonto	**243**	**40.5**	242	40.6	246	40.1	223	44.1	**223**	**44.2**	223	44.2
470.lbm	22.2	618	**21.8**	**630**	21.6	635	22.2	618	**21.8**	**630**	21.6	635
481.wrf	105	107	105	106	**105**	**107**	105	107	105	106	**105**	**107**
482.sphinx3	**267**	**73.1**	266	73.2	267	73.0	**267**	**73.1**	266	73.2	267	73.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Configuration:
Enforce POR set to Enable
Early Snoop set to disabled
COD set to disable
Power Technology set to performance
 Sysinfo program /home/cpu2006/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on localhost Mon Sep 29 16:39:12 2014
```

Continued on next page

Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2640 v3 @ 2.60GHz
      2 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       264479556 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 29 10:06

SPEC is set to: /home/cpu2006
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/sda3      ext4    1.8T  411G  1.3T  24% /

Additional information from dmidecode:
  BIOS American Megatrends Inc. 067 08/13/2014
  Memory:
   16x   16 GB
   8x NO DIMM NO DIMM
   16x Samsung M393A2G40DB0-CPB 16 GB 1866 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

Submitted_by: Tian Yuwan <tianyw@sugon.com>
Submitted: Wed Nov  5 04:16:18 EST 2014
Submission: cpu2006-20141105-32761.sub

Submitted_by: Tian Yuwan <tianyw@sugon.com>
Submitted: Sun Nov 23 21:26:15 EST 2014
Submission: cpu2006-20141105-32761.sub

Submitted_by: Tian Yuwan <tianyw@sugon.com>
Submitted: Wed Nov 26 05:11:52 EST 2014
Submission: cpu2006-20141105-32761.sub

Submitted_by: Tian Yuwan <tianyw@sugon.com>
Submitted: Thu Nov 27 00:57:34 EST 2014
Submission: cpu2006-20141105-32761.sub
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

Continued on next page

Base Portability Flags (Continued)

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html`
`http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml`
`http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Dec 3 10:28:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 December 2014.