SPEC® CINT2006 Result

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Sugon

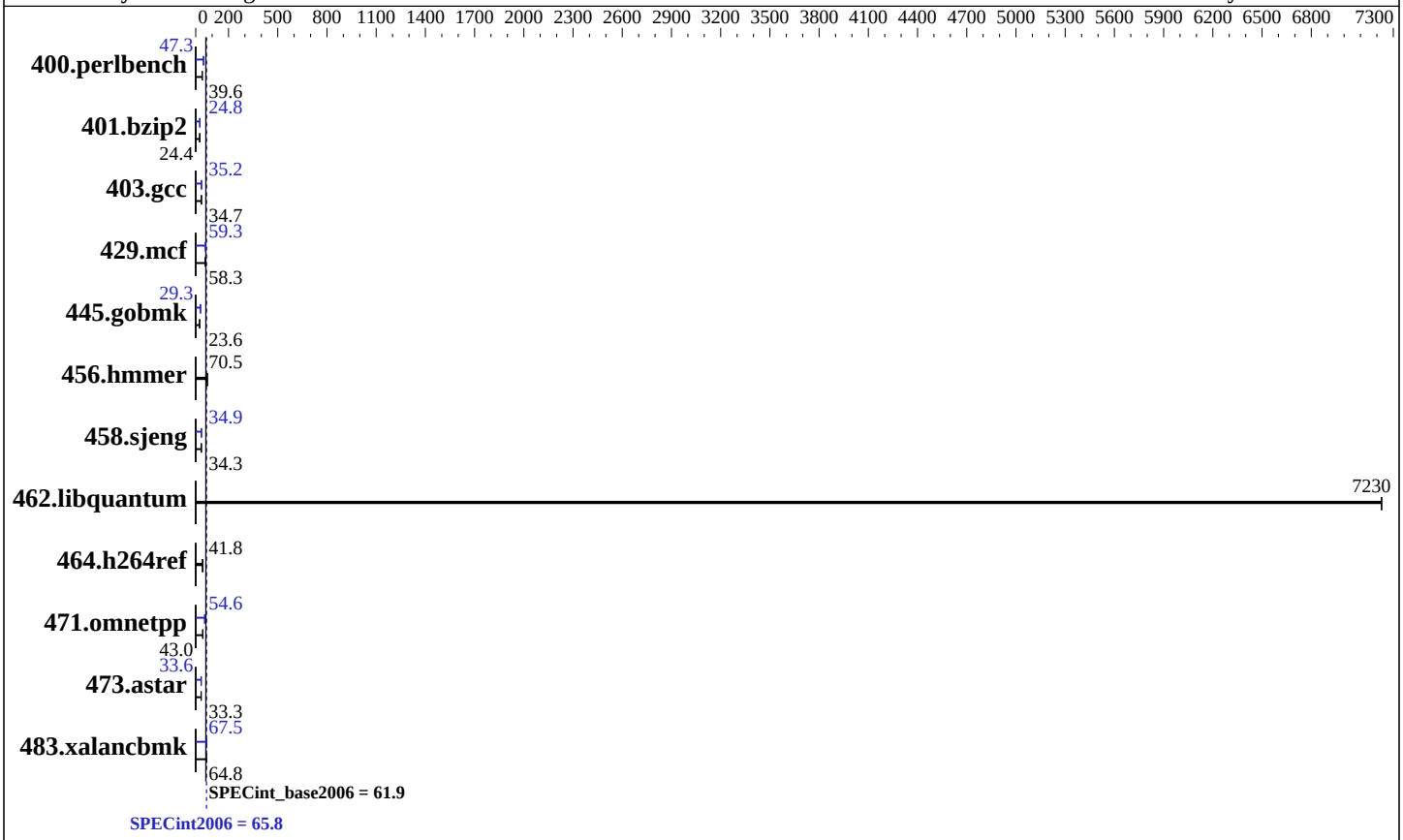
Sugon I620-G20 (Intel Xeon E5-2698 v3)

SPECint®2006 = 65.8

SPECint_base2006 = 61.9

CPU2006 license: 9046
Test sponsor: Sugon
Tested by: Sugon

Test date: Oct-2014
Hardware Availability: Sep-2014
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E5-2698 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	32 cores, 2 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	40 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 2.0 TB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	247	39.6	247	39.6	**247**	**39.6**	**206**	**47.3**	207	47.2	206	47.4
401.bzip2	**395**	**24.4**	395	24.4	395	24.4	**389**	**24.8**	389	24.8	389	24.8
403.gcc	**232**	**34.7**	231	34.8	232	34.6	228	35.2	**229**	**35.2**	229	35.1
429.mcf	159	57.4	**156**	**58.3**	154	59.2	154	59.2	152	60.0	**154**	**59.3**
445.gobmk	444	23.6	**444**	**23.6**	445	23.6	**359**	**29.3**	358	29.3	359	29.2
456.hmmer	131	71.0	**132**	**70.5**	132	70.4	131	71.0	**132**	**70.5**	132	70.4
458.sjeng	353	34.3	**353**	**34.3**	352	34.3	**347**	**34.9**	347	34.9	347	34.9
462.libquantum	2.87	7230	2.87	7230	**2.87**	**7230**	2.87	7230	2.87	7230	**2.87**	**7230**
464.h264ref	530	41.8	529	41.8	**529**	**41.8**	530	41.8	529	41.8	**529**	**41.8**
471.omnetpp	146	42.8	**145**	**43.0**	145	43.2	**115**	**54.6**	115	54.2	113	55.4
473.astar	214	32.9	**211**	**33.3**	210	33.4	211	33.3	**209**	**33.6**	209	33.7
483.xalancbmk	109	63.3	106	64.9	**106**	**64.8**	102	67.3	**102**	**67.5**	102	67.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Configuration:
Enforce POR set to disabled
DDR Speed set to 2133
Early Snoop set to disabled
COD set to disable
Power Technology set to performance
 Sysinfo program /home/cpu2006/config/sysinfo.rev6874
 $Rev: 6874 $ $Date:: 2013-11-20 #$ 654bd3fcf53b06faef0efe54ed011998
 running on localhost Fri Oct 24 04:23:22 2014

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2698 v3 @ 2.30GHz
       2 "physical id"s (chips)
       64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
```

Continued on next page

Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 16
         siblings  : 32
         physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
         physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
      cache size : 40960 KB

   From /proc/meminfo
      MemTotal:       264475492 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.5 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Oct 24 04:07

   SPEC is set to: /home/cpu2006
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda3      ext4  1.8T  586G  1.2T  35% /
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS American Megatrends Inc. 068 08/15/2014
     Memory:
      16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz
      8x NO DIMM NO DIMM

   (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "32"
```

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General Notes (Continued)

```
    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RedHat EL 6.4
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>

  Submitted_by: Tian Yuwan <tianyw@sugon.com>
  Submitted: Tue Nov  4 04:16:18 EST 2014
  Submission: cpu2006-20141104-32748.sub

  Submitted_by: Tian Yuwan <tianyw@sugon.com>
  Submitted: Wed Nov 19 02:37:11 EST 2014
  Submission: cpu2006-20141104-32748.sub

  Submitted_by: Tian Yuwan <tianyw@sugon.com>
  Submitted: Wed Nov 26 05:11:52 EST 2014
  Submission: cpu2006-20141104-32748.sub

  Submitted_by: Tian Yuwan <tianyw@sugon.com>
  Submitted: Thu Nov 27 00:57:34 EST 2014
  Submission: cpu2006-20141104-32748.sub
```

Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

C++ benchmarks:

```
icpc -m64
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
`-Wl,-z,muldefs -L/sh -lsmartheap64`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Dec 3 10:28:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 December 2014.