# SPEC® CINT2006 Result

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## Huawei

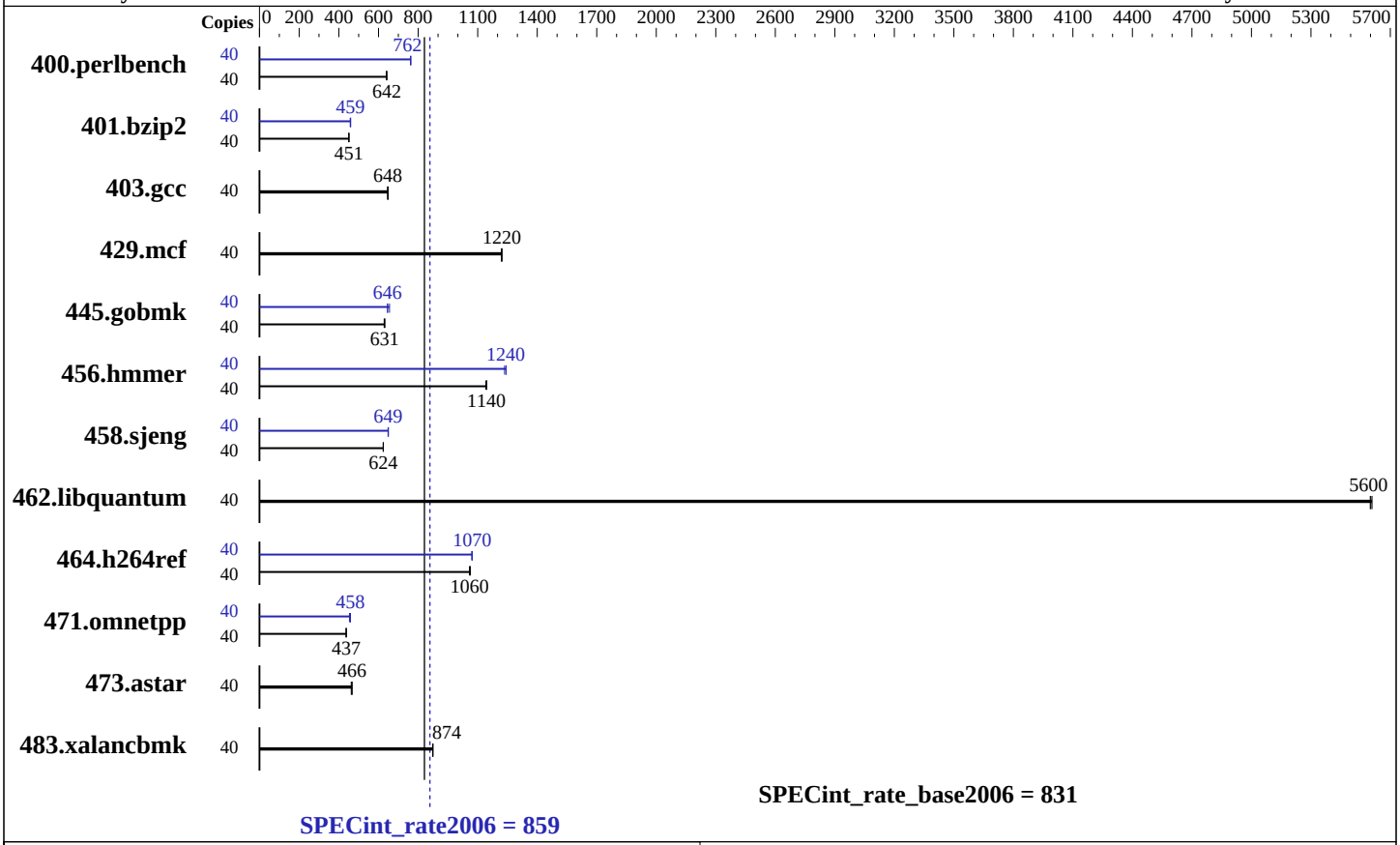
## Huawei CH221 (Intel Xeon E5-2680 v2)

**SPECint®_rate2006 = 859**

**SPECint_rate_base2006 = 831**

**CPU2006 license:** 3175
**Test sponsor:** Huawei
**Tested by:** Huawei

**Test date:** Sep-2014
**Hardware Availability:** Sep-2013
**Software Availability:** Nov-2013




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2680 v2 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.60 GHz |
| CPU MHz: | 2800 |
| FPU: | Integrated |
| CPU(s) enabled: | 20 cores, 2 chips, 10 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 25 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC) |
| Disk Subsystem: | 1 x 300 GB SAS, 10000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.5 (Santiago)<br>2.6.32-431.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 40 | 612 | 639 | **609** | **642** | 609 | 642 | 40 | **513** | **762** | 513 | 762 | 513 | 762 |
| 401.bzip2 | 40 | **857** | **451** | 856 | 451 | 857 | 450 | 40 | 844 | 457 | 840 | 459 | **841** | **459** |
| 403.gcc | 40 | **497** | **648** | 497 | 648 | 499 | 646 | 40 | **497** | **648** | 497 | 648 | 499 | 646 |
| 429.mcf | 40 | 299 | 1220 | 299 | 1220 | **299** | **1220** | 40 | 299 | 1220 | 299 | 1220 | **299** | **1220** |
| 445.gobmk | 40 | 664 | 631 | 668 | 629 | **665** | **631** | 40 | **650** | **646** | 650 | 646 | 640 | 655 |
| 456.hmmer | 40 | **326** | **1140** | 326 | 1150 | 327 | 1140 | 40 | **300** | **1240** | 302 | 1240 | 300 | 1240 |
| 458.sjeng | 40 | **775** | **624** | 775 | 625 | 777 | 623 | 40 | 745 | 650 | 747 | 648 | **746** | **649** |
| 462.libquantum | 40 | **148** | **5600** | 148 | 5610 | 148 | 5600 | 40 | **148** | **5600** | 148 | 5610 | 148 | 5600 |
| 464.h264ref | 40 | 833 | 1060 | **835** | **1060** | 836 | 1060 | 40 | **826** | **1070** | 828 | 1070 | 825 | 1070 |
| 471.omnetpp | 40 | **572** | **437** | 572 | 437 | 571 | 438 | 40 | 551 | 454 | **546** | **458** | 545 | 459 |
| 473.astar | 40 | **603** | **466** | 602 | 467 | 607 | 462 | 40 | **603** | **466** | 602 | 467 | 607 | 462 |
| 483.xalancbmk | 40 | **316** | **874** | 316 | 874 | 316 | 873 | 40 | **316** | **874** | 316 | 874 | 316 | 873 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec14/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Sun Feb 23 19:35:38 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2680 v2 @ 2.80GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
```

Continued on next page

## Platform Notes (Continued)

```
      caution.)
         cpu cores : 10
         siblings  : 20
         physical 0: cores 0 1 2 3 4 8 9 10 11 12
         physical 1: cores 0 1 2 3 4 8 9 10 11 12
      cache size : 25600 KB

   From /proc/meminfo
      MemTotal:       264478184 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.5 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
      EST 2013 x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Feb 20 22:52

   SPEC is set to: /spec14
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda2      ext4  254G   13G  229G   6% /

   Additional information from dmidecode:
     BIOS Insyde Corp. RMIBV629 05/12/2014
     Memory:
      8x NO DIMM NO DIMM
      16x Samsung M393B2G70DB0-CMA 16 GB 1866 MHz 2 rank

   (End of data from sysinfo program)
```

## General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/spec14/libs/32:/spec14/libs/64:/spec14/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32
```

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page

## Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml`


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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Nov 6 13:46:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 November 2014.