Hewlett-Packard Company
ProLiant DL360 Gen9
(1.60 GHz, Intel Xeon E5-2603 v3)

**SPECint_rate2006 = 269**

**SPECint_rate_base2006 = 260**

**CPU2006 license:** 3

**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

**Test date:** Sep-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Jun-2014

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**SPECint (Rate) Results**

<table>
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<th>Benchmark</th>
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<td>12</td>
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</tr>
</tbody>
</table>

**SPECint_rate2006 = 269**

**SPECint_rate_base2006 = 260**

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**Hardware**

- **CPU Name:** Intel Xeon E5-2603 v3
- **CPU Characteristics:**
  - CPU MHz: 1600
  - FPU: Integrated
  - CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
  - Primary Cache: 32 KB I + 32 KB D on chip per core
  - Secondary Cache: 256 KB I+D on chip per core
  - L3 Cache: 15 MB I+D on chip per chip
  - Other Cache: None
  - Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
  - Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
  - Other Hardware: None

**Software**

- **Operating System:** Red Hat Enterprise Linux Server release 7.0, (Maipo)
  - Kernel 3.10.0-123.el7.x86_64
- **Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
- **Auto Parallel:** No
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V10.0
Hewlett-Packard Company

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CPU2006 license: 3
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Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
    echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
Disabled unused Linux services through "stop_services.sh" before running.

Platform Notes

BIOS Configuration:
    HP Power Profile set to Custom
    HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to No-C State
Minimum Processor Idle Power Package State set to No Package State
QPI Snoop Configuration set to Cluster on Die
Thermal Configuration set to Maximum Cooling
    Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh

Continued on next page
SPEC CINT2006 Result

Hewlett-Packard Company
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Platform Notes (Continued)

Sysinfo program /home/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Sat Sep 13 18:04:09 2014

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz
  2 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
  cache size : 15360 KB

From /proc/meminfo
MemTotal: 263847304 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 13 17:55

SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 318G 9.6G 309G 4% /home

Additional information from dmidecode:
Continued on next page
Hewlett-Packard Company

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Platform Notes (Continued)

BIOS HP P89 07/11/2014
Memory:
16x HP NOT AVAILABLE 16 GB 1600 MHz 2 rank
8x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:
16x HP NOT AVAILABLE 16 GB 1600 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbmk: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
Hewlett-Packard Company

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Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
- icc -m32
- 400.perlbench: icc -m64
- 401.bzip2: icc -m64
- 458.sjeng: icc -m64

C++ benchmarks:
- icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3
Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
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For other inquiries, please contact webmaster@spec.org.

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