Huawei

Huawei CH121 V3 (Intel Xeon E5-2609 v3)

SPECint\_rate2006 = 308
SPECint\_rate\_base2006 = 298

CPU2006 license: 3175
Test date: Jul-2014
Test sponsor: Huawei
Hardware Availability: Sep-2014
Tested by: Huawei
Software Availability: Nov-2013

400.perlbench 12 282
401.bzip2 12 145
403.gcc 12 221
429.mcf 12 422
445.gobmk 12 183
456.hmmer 12 412
458.sjeng 12 217
462.libquantum 12 360
464.h264ref 12 410
471.omnetpp 12 154
473.astar 12 163
483.xalancbmk 12 360

**Hardware**

CPU Name: Intel Xeon E5-2609 v3
CPU Characteristics: 12 cores, 2 chips, 6 cores/chip
CPU MHz: 1900
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 256 GB SATA, SSD
Other Hardware: None

**Software**

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0
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SPECint_rate2006 = 308
SPECint_rate_base2006 = 298

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>12</td>
<td>506</td>
<td>232</td>
<td>505</td>
<td>232</td>
<td>507</td>
<td>231</td>
<td>12</td>
<td>416</td>
<td>283</td>
<td>416</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>12</td>
<td>849</td>
<td>136</td>
<td>850</td>
<td>136</td>
<td>852</td>
<td>136</td>
<td>12</td>
<td>798</td>
<td>145</td>
<td>795</td>
</tr>
<tr>
<td>403.gcc</td>
<td>12</td>
<td>438</td>
<td>221</td>
<td>439</td>
<td>220</td>
<td>438</td>
<td>221</td>
<td>12</td>
<td>438</td>
<td>221</td>
<td>439</td>
</tr>
<tr>
<td>429.mcf</td>
<td>12</td>
<td>705</td>
<td>178</td>
<td>706</td>
<td>178</td>
<td>707</td>
<td>178</td>
<td>12</td>
<td>688</td>
<td>183</td>
<td>688</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>12</td>
<td>695</td>
<td>209</td>
<td>696</td>
<td>209</td>
<td>696</td>
<td>209</td>
<td>12</td>
<td>669</td>
<td>217</td>
<td>670</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>12</td>
<td>504</td>
<td>149</td>
<td>501</td>
<td>150</td>
<td>506</td>
<td>148</td>
<td>12</td>
<td>486</td>
<td>154</td>
<td>487</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>12</td>
<td>516</td>
<td>163</td>
<td>517</td>
<td>163</td>
<td>516</td>
<td>163</td>
<td>12</td>
<td>516</td>
<td>163</td>
<td>517</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>12</td>
<td>683</td>
<td>389</td>
<td>683</td>
<td>389</td>
<td>683</td>
<td>389</td>
<td>12</td>
<td>648</td>
<td>410</td>
<td>645</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>12</td>
<td>504</td>
<td>149</td>
<td>501</td>
<td>150</td>
<td>506</td>
<td>148</td>
<td>12</td>
<td>486</td>
<td>154</td>
<td>487</td>
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<td>12</td>
<td>516</td>
<td>163</td>
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<td>163</td>
<td>516</td>
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<td>12</td>
<td>516</td>
<td>163</td>
<td>517</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>12</td>
<td>230</td>
<td>360</td>
<td>230</td>
<td>360</td>
<td>230</td>
<td>360</td>
<td>12</td>
<td>230</td>
<td>360</td>
<td>230</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
- Set Power Efficiency Mode to Custom
- Set Snoop Mode to HS
- Set Hyper-Threading to Disabled
- Baseboard Management Controller used to adjust the fan speed to 100%
- Sysinfo program /spec/config/sysinfo.rev6818
- $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
- running on xjt Tue Jul 22 14:24:23 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz
  2 "physical id"s (chips)
  12 "processors"

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

From /proc/meminfo
MemTotal: 131928996 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

uname -a:
Linux xjt 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux

run-level 3 Jul 22 13:22

SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 ext4 222G 7.7G 203G 4% /

Additional information from dmidecode:
BIOS Insyde Corp. 1.06 06/17/2014
Memory:
8x Micron 18ASF1G72PDZ-2G1A1 8 GB 1600 MHz 1 rank
8x Micron 18ASF1G72PDZ-2G1A1 8 GB 1600 MHz 2 rank
8x NO DIMM NO DIMM 3 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:

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General Notes (Continued)

```
echo 1>/proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3
are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

```
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

```
C benchmarks:
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

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Peak Compiler Invocation (Continued)

400.perlbench: icc -m64
401.bzip2: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3
456.hmmer: basepeak = yes
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias
Huawei

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**SPEC CINT2006 Result**

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**Peak Optimization Flags (Continued)**

C++ benchmarks:

471.omnetpp:
-xCORE-AVX2(pass 2)
-prof-gen(pass 1)
-ipo(pass 2)
-O3(pass 2)
-no-prec-div(pass 2)
-prof-use(pass 2)
-ansi-alias
-opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/sh
-lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

**Peak Other Flags**

C benchmarks:

403.gcc:
-Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml

http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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For other inquiries, please contact webmaster@spec.org.

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