



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 69.3

Huawei CH220 (Intel Xeon E5-2650L v2)

SPECfp\_base2006 = 66.8

CPU2006 license: 3175

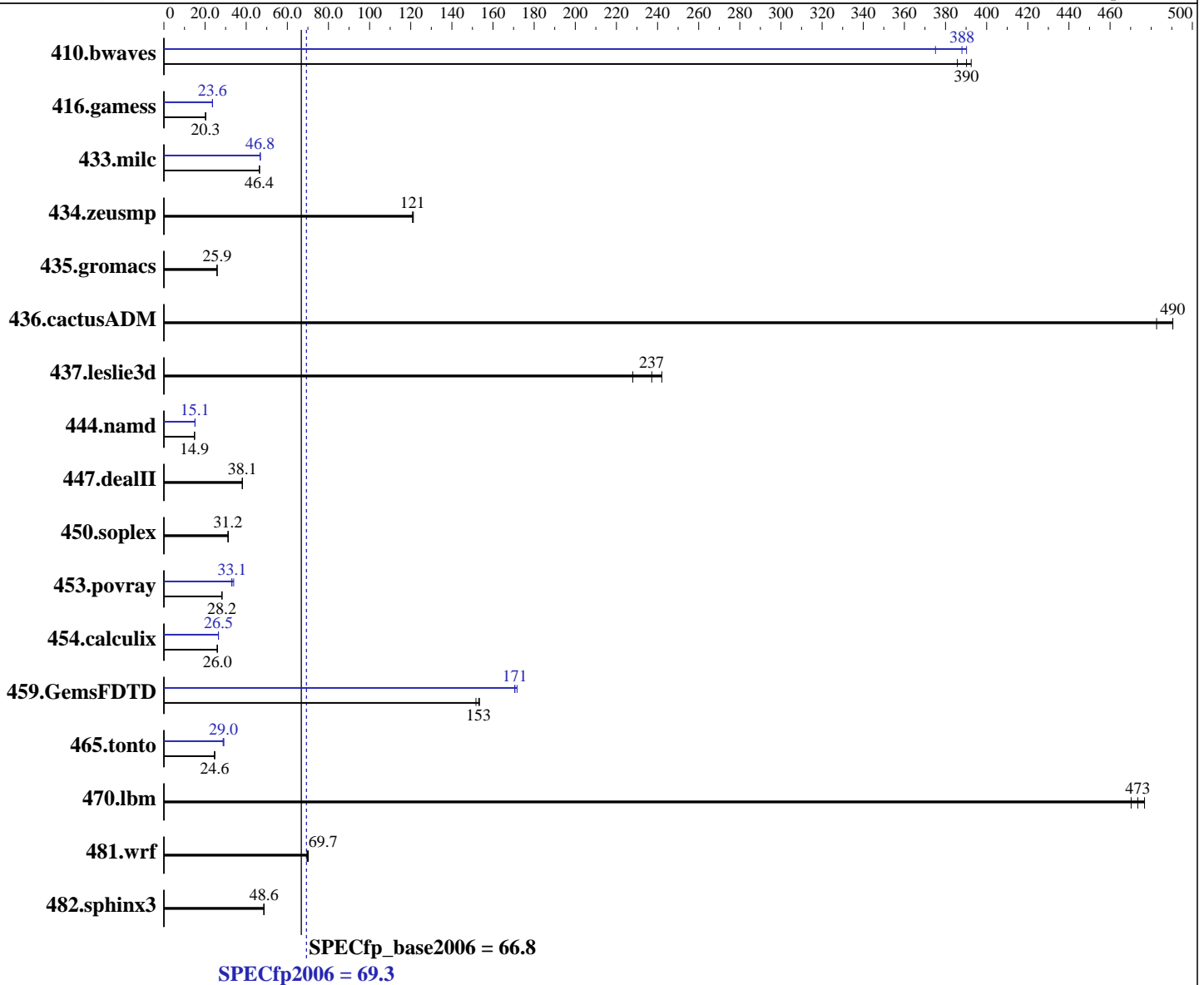
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2650L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>34.8</b>	<b>390</b>	35.2	386	34.6	392	34.8	390	<b>35.0</b>	<b>388</b>	36.2	375
416.gamess	966	20.3	<b>967</b>	<b>20.3</b>	967	20.2	828	23.7	830	23.6	<b>828</b>	<b>23.6</b>
433.milc	198	46.4	198	46.4	<b>198</b>	<b>46.4</b>	196	46.8	196	46.8	<b>196</b>	<b>46.8</b>
434.zeusmp	75.0	121	75.2	121	<b>75.2</b>	<b>121</b>	75.0	121	75.2	121	<b>75.2</b>	<b>121</b>
435.gromacs	276	25.9	277	25.8	<b>276</b>	<b>25.9</b>	276	25.9	277	25.8	<b>276</b>	<b>25.9</b>
436.cactusADM	24.8	483	24.4	491	<b>24.4</b>	<b>490</b>	24.8	483	24.4	491	<b>24.4</b>	<b>490</b>
437.leslie3d	<b>39.6</b>	<b>237</b>	41.2	228	38.8	242	<b>39.6</b>	<b>237</b>	41.2	228	38.8	242
444.namd	539	14.9	539	14.9	<b>539</b>	<b>14.9</b>	530	15.1	530	15.1	<b>530</b>	<b>15.1</b>
447.dealII	<b>300</b>	<b>38.1</b>	300	38.1	300	38.2	<b>300</b>	<b>38.1</b>	300	38.1	300	38.2
450.soplex	268	31.2	267	31.3	<b>267</b>	<b>31.2</b>	268	31.2	267	31.3	<b>267</b>	<b>31.2</b>
453.povray	189	28.2	<b>189</b>	<b>28.2</b>	188	28.3	157	33.9	<b>161</b>	<b>33.1</b>	162	32.9
454.calculix	318	26.0	<b>318</b>	<b>26.0</b>	318	26.0	311	26.5	311	26.5	<b>311</b>	<b>26.5</b>
459.GemsFDTD	<b>69.3</b>	<b>153</b>	69.9	152	69.1	153	<b>62.2</b>	<b>171</b>	61.8	172	62.2	171
465.tonto	399	24.6	399	24.7	<b>399</b>	<b>24.6</b>	338	29.1	<b>339</b>	<b>29.0</b>	342	28.8
470.lbm	29.2	470	<b>29.0</b>	<b>473</b>	28.8	477	29.2	470	<b>29.0</b>	<b>473</b>	28.8	477
481.wrf	159	70.2	<b>160</b>	<b>69.7</b>	161	69.6	159	70.2	<b>160</b>	<b>69.7</b>	161	69.6
482.sphinx3	401	48.6	<b>401</b>	<b>48.6</b>	400	48.7	401	48.6	<b>401</b>	<b>48.6</b>	400	48.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Baseboard Management Controller used to adjust the fan speed to 100%  
 Set Hyper-Threading to Disabled  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on wbspeccpu Fri Sep 5 03:52:34 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2650L v2 @ 1.70GHz

2 "physical id"s (chips)

20 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 10

siblings : 10

physical 0: cores 0 1 2 3 4 8 9 10 11 12

physical 1: cores 0 1 2 3 4 8 9 10 11 12

cache size : 25600 KB

From /proc/meminfo

MemTotal: 132103760 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux wbspeccpu 2.6.32-431.el6.x86\_64 #1 SMP Sun Nov 10 22:19:54 EST 2013  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Sep 2 10:15

SPEC is set to: /spec

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda1 ext4 270G 148G 109G 58% /

Additional information from dmidecode:

Memory:

16x Hynix HMT41GR7MFR8C-PB 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"

OMP\_NUM\_THREADS = "20"

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## General Notes (Continued)

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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