# SPEC® CFP2006 Result

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## Dell Inc.

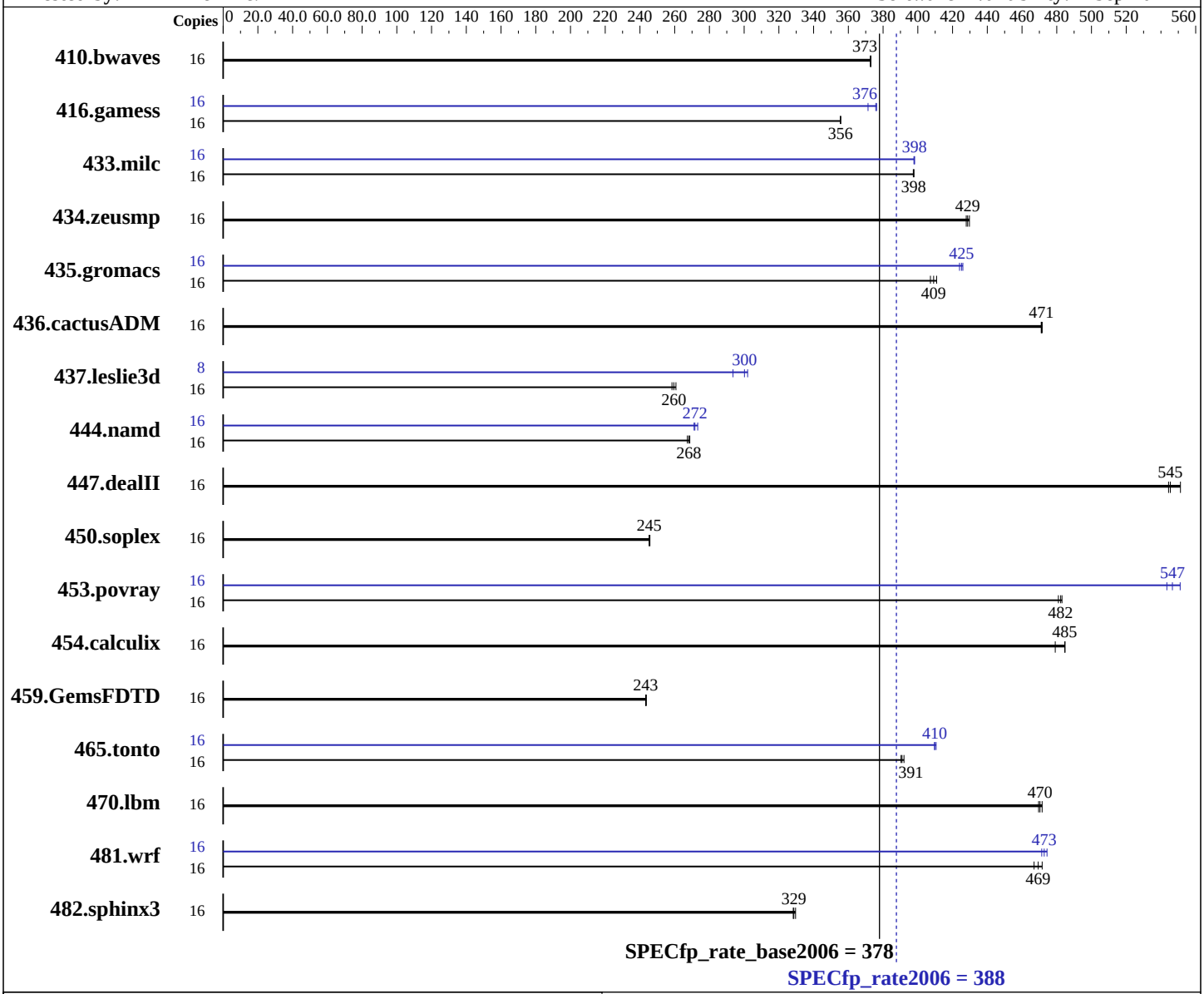
PowerEdge T630 (Intel Xeon E5-2623 v3, 3.00 GHz)

**SPECfp®_rate2006 = 388**

**SPECfp_rate_base2006 = 378**

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Jul-2014
**Hardware Availability:** Sep-2014
**Software Availability:** Sep-2014




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2623 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.50 GHz |
| CPU MHz: | 3000 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext2 |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x 300 GB 15000 RPM SAS
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 16 | **583** | **373** | 584 | 373 | 583 | 373 | 16 | **583** | **373** | 584 | 373 | 583 | 373 |
| 416.gamess | 16 | 881 | 355 | 881 | 356 | **881** | **356** | 16 | 833 | 376 | **834** | **376** | 844 | 371 |
| 433.milc | 16 | 370 | 397 | **369** | **398** | 369 | 398 | 16 | 369 | 398 | 369 | 398 | **369** | **398** |
| 434.zeusmp | 16 | 340 | 428 | **340** | **429** | 339 | 430 | 16 | 340 | 428 | **340** | **429** | 339 | 430 |
| 435.gromacs | 16 | 278 | 411 | **279** | **409** | 281 | 407 | 16 | 269 | 424 | **269** | **425** | 268 | 426 |
| 436.cactusADM | 16 | **406** | **471** | 405 | 472 | 406 | 471 | 16 | **406** | **471** | 405 | 472 | 406 | 471 |
| 437.leslie3d | 16 | 582 | 258 | 577 | 261 | **580** | **260** | 8 | 249 | 302 | **251** | **300** | 256 | 294 |
| 444.namd | 16 | 478 | 269 | **479** | **268** | 480 | 267 | 16 | 470 | 273 | **472** | **272** | 473 | 271 |
| 447.dealII | 16 | 332 | 551 | 336 | 544 | **336** | **545** | 16 | 332 | 551 | 336 | 544 | **336** | **545** |
| 450.soplex | 16 | **544** | **245** | 544 | 245 | 543 | 246 | 16 | **544** | **245** | 544 | 245 | 543 | 246 |
| 453.povray | 16 | 176 | 483 | 177 | 481 | **177** | **482** | 16 | 157 | 543 | 154 | 551 | **156** | **547** |
| 454.calculix | 16 | 272 | 485 | 275 | 479 | **272** | **485** | 16 | 272 | 485 | 275 | 479 | **272** | **485** |
| 459.GemsFDTD | 16 | **698** | **243** | 697 | 244 | 698 | 243 | 16 | **698** | **243** | 697 | 244 | 698 | 243 |
| 465.tonto | 16 | 403 | 390 | 402 | 392 | **403** | **391** | 16 | 383 | 411 | **384** | **410** | 384 | 410 |
| 470.lbm | 16 | 466 | 472 | 468 | 470 | **467** | **470** | 16 | 466 | 472 | 468 | 470 | **467** | **470** |
| 481.wrf | 16 | 379 | 472 | **381** | **469** | 383 | 467 | 16 | 377 | 474 | **378** | **473** | 379 | 471 |
| 482.sphinx3 | 16 | **949** | **329** | 946 | 330 | 950 | 328 | 16 | **949** | **329** | 946 | 330 | 950 | 328 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS settings:
Snoop Mode set to Early Snoop
Virtualization Technology disabled
```

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## Platform Notes (Continued)

```
Execute Disable disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux Thu Jul  3 18:25:13 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2623 v3 @ 3.00GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:       264572124 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 3 08:56 last=S

SPEC is set to: /root/cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext2  222G  9.8G  211G   5% /

Additional information from dmidecode:
  BIOS Dell Inc. 0.3.25 06/26/2014
  Memory:
   16x 00AD00B300AD HMA42GR7MFR4N-TFTD 16 GB 1866 MHz
   8x Not Specified Not Specified
```

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## Platform Notes (Continued)

```
(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

# Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
```

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## Base Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

# Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.xml`


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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