



# SPEC® CFP2006 Result

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**Huawei**

**SPECfp®2006 = 96.9**

**Huawei CH220 (Intel Xeon E5-2637 v2)**

**SPECfp\_base2006 = 93.9**

**CPU2006 license:** 3175

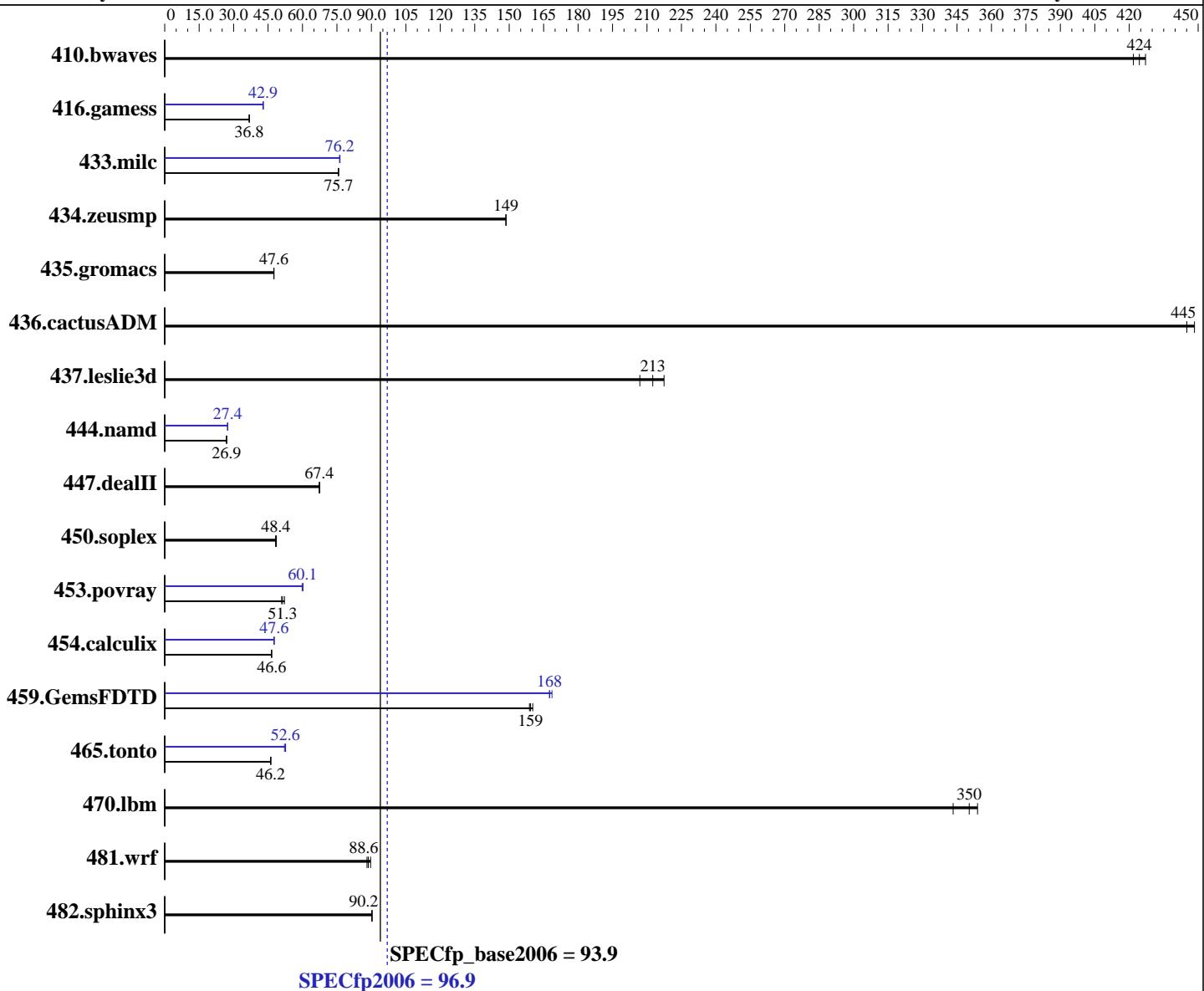
**Test date:** Aug-2014

**Test sponsor:** Huawei

**Hardware Availability:** Sep-2013

**Tested by:** Huawei

**Software Availability:** Nov-2013



## Hardware

CPU Name: Intel Xeon E5-2637 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

*Continued on next page*

## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Compiler: 2.6.32-431.el6.x86\_64  
 C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 X 300 GB SAS 10000 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	32.2	422	31.8	427	<b><u>32.0</u></b>	<b><u>424</u></b>	32.2	422	31.8	427	<b><u>32.0</u></b>	<b><u>424</u></b>
416.gamess	532	36.8	<b><u>532</u></b>	<b><u>36.8</u></b>	532	36.8	<b><u>457</u></b>	<b><u>42.9</u></b>	<b><u>456</u></b>	<b><u>42.9</u></b>	456	42.9
433.milc	<b><u>121</u></b>	<b><u>75.7</u></b>	121	75.7	121	75.6	<b><u>120</u></b>	<b><u>76.2</u></b>	120	76.2	120	76.3
434.zeusmp	<b><u>61.2</u></b>	<b><u>149</u></b>	61.2	149	61.2	149	<b><u>61.2</u></b>	<b><u>149</u></b>	61.2	149	61.2	149
435.gromacs	150	47.6	150	47.5	<b><u>150</u></b>	<b><u>47.6</u></b>	150	47.6	150	47.5	<b><u>150</u></b>	<b><u>47.6</u></b>
436.cactusADM	26.6	448	26.9	445	<b><u>26.8</u></b>	<b><u>445</u></b>	26.6	448	26.9	445	<b><u>26.8</u></b>	<b><u>445</u></b>
437.leslie3d	45.4	207	43.2	217	<b><u>44.2</u></b>	<b><u>213</u></b>	45.4	207	43.2	217	<b><u>44.2</u></b>	<b><u>213</u></b>
444.namd	298	26.9	<b><u>298</u></b>	<b><u>26.9</u></b>	298	26.9	293	27.4	293	27.4	<b><u>293</u></b>	<b><u>27.4</u></b>
447.dealII	<b><u>170</u></b>	<b><u>67.4</u></b>	170	67.3	170	67.4	<b><u>170</u></b>	<b><u>67.4</u></b>	170	67.3	170	67.4
450.soplex	173	48.3	<b><u>172</u></b>	<b><u>48.4</u></b>	171	48.6	<b><u>173</u></b>	48.3	<b><u>172</u></b>	<b><u>48.4</u></b>	171	48.6
453.povray	105	50.9	<b><u>104</u></b>	<b><u>51.3</u></b>	102	52.1	88.3	60.2	88.9	59.8	<b><u>88.5</u></b>	<b><u>60.1</u></b>
454.calculix	177	46.6	<b><u>177</u></b>	<b><u>46.6</u></b>	177	46.5	<b><u>173</u></b>	<b><u>47.6</u></b>	173	47.6	173	47.7
459.GemsFDTD	<b><u>66.6</u></b>	<b><u>159</u></b>	66.2	160	66.8	159	63.3	168	<b><u>63.3</u></b>	<b><u>168</u></b>	62.9	169
465.tonto	213	46.1	213	46.2	<b><u>213</u></b>	<b><u>46.2</u></b>	189	52.2	<b><u>187</u></b>	<b><u>52.6</u></b>	187	52.7
470.lbm	40.0	343	<b><u>39.2</u></b>	<b><u>350</u></b>	38.8	354	40.0	343	<b><u>39.2</u></b>	<b><u>350</u></b>	38.8	354
481.wrf	<b><u>126</u></b>	<b><u>88.6</u></b>	127	88.1	125	89.6	<b><u>126</u></b>	<b><u>88.6</u></b>	127	88.1	125	89.6
482.sphinx3	216	90.3	216	90.2	<b><u>216</u></b>	<b><u>90.2</u></b>	<b><u>216</u></b>	<b><u>90.3</u></b>	<b><u>216</u></b>	<b><u>90.2</u></b>	<b><u>216</u></b>	<b><u>90.2</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date::: 2011-10-11 #$
running on localhost.localdomain Sat Aug 30 03:03:51 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2637 v2 @ 3.50GHz
Continued on next page
```



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## Platform Notes (Continued)

```
2 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 4
    physical 0: cores 1 2 3 4
    physical 1: cores 1 2 3 4
cache size : 15360 KB

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 27 10:37

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  265G   15G  237G   6%  /


Additional information from dmidecode:
Memory:
10x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
6x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"

OMP\_NUM\_THREADS = "8"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory  
using RHEL 6.1

Transparent Huge Pages enabled with:

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## General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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