SPEC® CFP2006 Result

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IBM Corporation

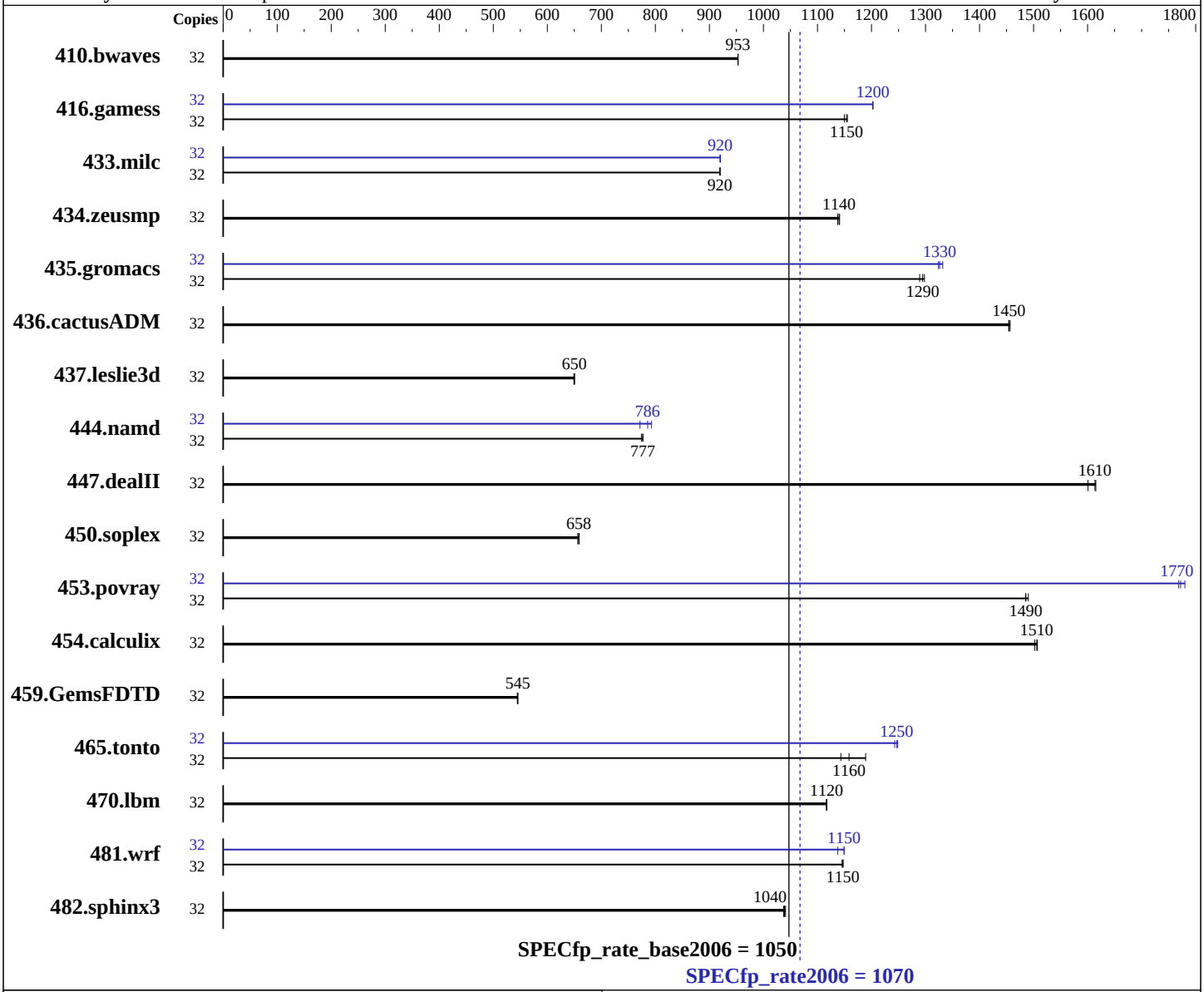
IBM System x3750 M4
(Intel Xeon E5-4627 v2, 3.30 GHz)

SPECfp®_rate2006 = 1070

SPECfp_rate_base2006 = 1050

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2014
Hardware Availability: Jun-2014
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E5-4627 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip
CPU(s) orderable:	4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 300 GB SAS, 10000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	456	953	**456**	**953**	456	953	32	456	953	**456**	**953**	456	953
416.gamess	32	545	1150	**543**	**1150**	542	1160	32	**521**	**1200**	521	1200	521	1200
433.milc	32	319	919	**319**	**920**	319	920	32	**319**	**920**	319	920	319	920
434.zeusmp	32	256	1140	255	1140	**255**	**1140**	32	256	1140	255	1140	**255**	**1140**
435.gromacs	32	176	1300	**176**	**1290**	177	1290	32	172	1330	173	1320	**172**	**1330**
436.cactusADM	32	263	1460	**263**	**1450**	263	1450	32	263	1460	**263**	**1450**	263	1450
437.leslie3d	32	**463**	**650**	463	649	463	650	32	**463**	**650**	463	649	463	650
444.namd	32	330	777	332	774	**330**	**777**	32	333	772	**327**	**786**	324	793
447.dealII	32	**227**	**1610**	227	1620	229	1600	32	**227**	**1610**	227	1620	229	1600
450.soplex	32	405	659	407	656	**405**	**658**	32	405	659	407	656	**405**	**658**
453.povray	32	115	1490	114	1490	**115**	**1490**	32	96.3	1770	**96.1**	**1770**	95.6	1780
454.calculix	32	175	1510	176	1500	**175**	**1510**	32	175	1510	176	1500	**175**	**1510**
459.GemsFDTD	32	622	545	**623**	**545**	624	544	32	622	545	**623**	**545**	624	544
465.tonto	32	275	1140	**272**	**1160**	265	1190	32	**253**	**1250**	252	1250	253	1240
470.lbm	32	393	1120	394	1120	**394**	**1120**	32	393	1120	394	1120	**394**	**1120**
481.wrf	32	312	1150	312	1150	**312**	**1150**	32	314	1140	**311**	**1150**	311	1150
482.sphinx3	32	601	1040	**600**	**1040**	599	1040	32	601	1040	**600**	**1040**	599	1040

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Operating Mode set to Maximum Perfomance in BIOS
Sysinfo program /cpu2006.1.2_14.0_aug2013/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
```

Continued on next page

Platform Notes (Continued)

```
running on Larry-KongIVB Tue Aug 12 22:53:13 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4627 v2 @ 3.30GHz
      4 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
      physical 2: cores 0 1 2 3 4 5 6 7
      physical 3: cores 0 1 2 3 4 5 6 7
   cache size : 16384 KB

From /proc/meminfo
   MemTotal:       529089016 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux Larry-KongIVB 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 12 16:35

SPEC is set to: /cpu2006.1.2_14.0_aug2013
   Filesystem                     Type  Size  Used Avail Use% Mounted on
   /dev/mapper/vg_larrykongivb-lv_root ext4  264G  100G  151G  40% /

Additional information from dmidecode:
  BIOS IBM -[KOE141AUS-1.50]- 05/03/2014
  Memory:
   31x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank
   16x Not Specified Not Specified
   1x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2_14.0_aug2013/libs/32:/cpu2006.1.2_14.0_aug2013/libs/64:/cpu2006.1.2_14.0_aug2013/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml

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For other inquiries, please contact webmaster@spec.org.

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