



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®_rate2006 = 80.9

Express5800/T110g-S (Intel Pentium G3240)

SPECint_rate_base2006 = 78.0

CPU2006 license: 9006

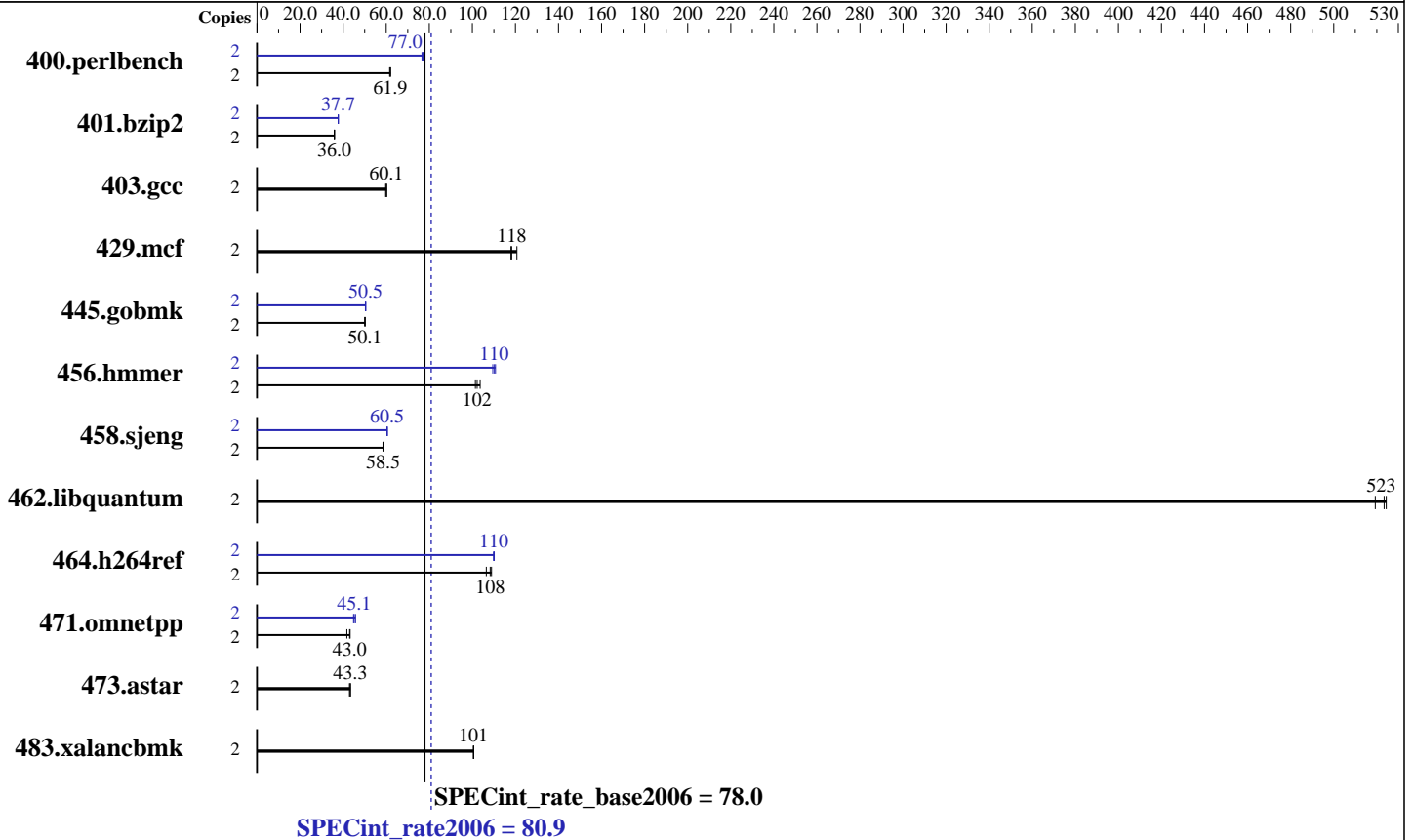
Test date: Jun-2014

Test sponsor: NEC Corporation

Hardware Availability: Jul-2014

Tested by: NEC Corporation

Software Availability: Jan-2014



Hardware

CPU Name: Intel Pentium G3240
 CPU Characteristics:
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	317	61.6	<u>316</u>	<u>61.9</u>	315	62.0	2	<u>254</u>	<u>77.0</u>	255	76.6	254	77.0
401.bzip2	2	538	35.9	<u>536</u>	<u>36.0</u>	536	36.0	2	<u>511</u>	<u>37.7</u>	512	37.7	511	37.8
403.gcc	2	267	60.2	<u>268</u>	<u>60.1</u>	269	59.8	2	267	60.2	<u>268</u>	<u>60.1</u>	269	59.8
429.mcf	2	<u>154</u>	<u>118</u>	155	118	151	121	2	<u>154</u>	<u>118</u>	155	118	151	121
445.gobmk	2	419	50.0	418	50.2	<u>418</u>	<u>50.1</u>	2	417	50.3	416	50.5	<u>416</u>	<u>50.5</u>
456.hammer	2	184	101	180	104	<u>183</u>	<u>102</u>	2	<u>169</u>	<u>110</u>	170	109	168	111
458.sjeng	2	414	58.5	<u>414</u>	<u>58.5</u>	413	58.5	2	400	60.5	<u>400</u>	<u>60.5</u>	401	60.4
462.libquantum	2	79.0	524	<u>79.2</u>	<u>523</u>	79.8	519	2	79.0	524	<u>79.2</u>	<u>523</u>	79.8	519
464.h264ref	2	<u>409</u>	<u>108</u>	406	109	416	106	2	403	110	<u>402</u>	<u>110</u>	402	110
471.omnetpp	2	<u>291</u>	<u>43.0</u>	300	41.7	289	43.2	2	273	45.7	<u>277</u>	<u>45.1</u>	279	44.8
473.astar	2	327	42.9	324	43.4	<u>324</u>	<u>43.3</u>	2	327	42.9	324	43.4	<u>324</u>	<u>43.3</u>
483.xalancbmk	2	137	100	137	101	<u>137</u>	<u>101</u>	2	137	100	137	101	<u>137</u>	<u>101</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/sh -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

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For other inquiries, please contact webmaster@spec.org.

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