SPEC® CINT2006 Result

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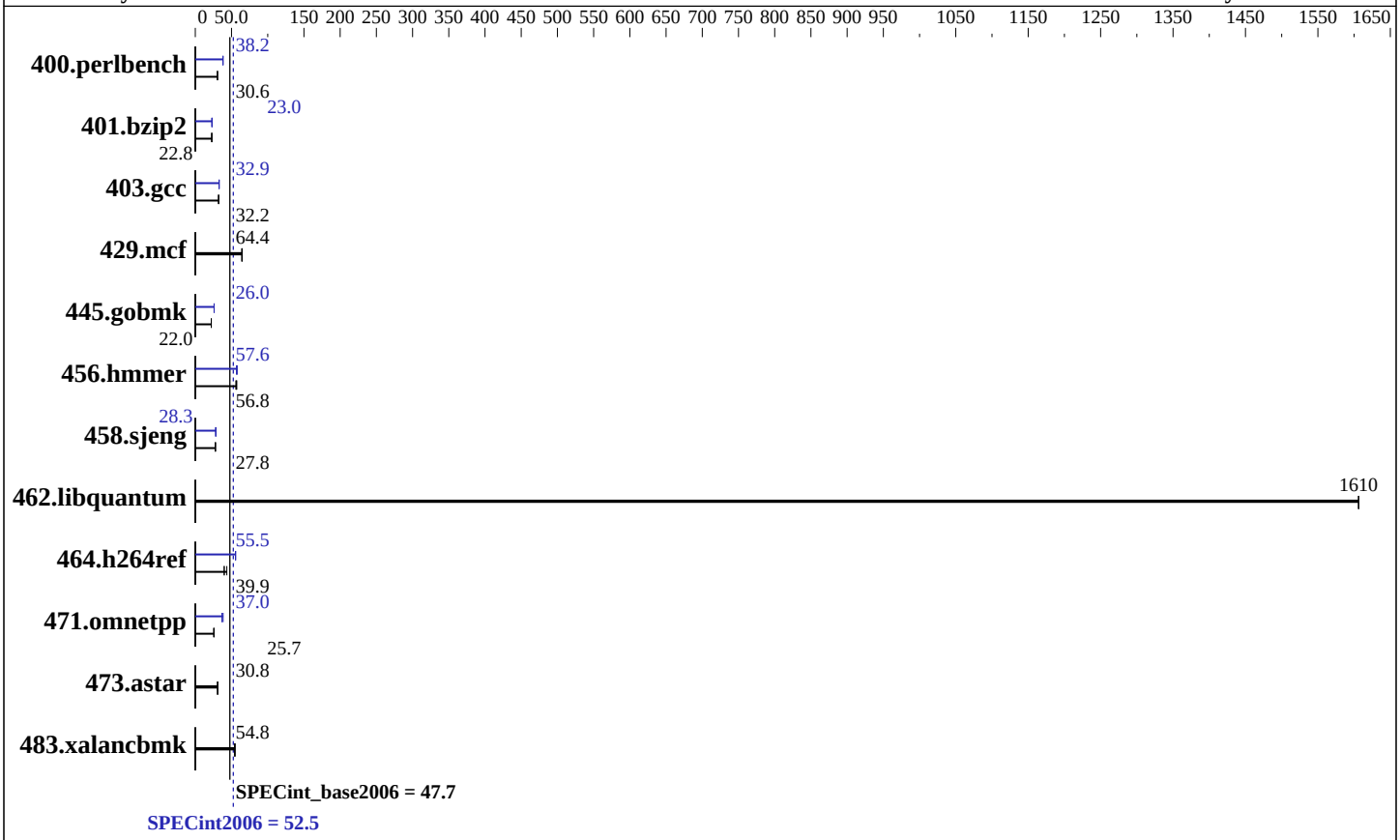
Huawei
Huawei Tecal RH2285 V2

SPECint®2006 = 52.5

SPECint_base2006 = 47.7

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2014
Hardware Availability: Jan-2014
Software Availability: Nov-2013



Hardware

CPU Name:	Intel Xeon E5-2450 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 500 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	319	30.6	**319**	**30.6**	319	30.6	**256**	**38.2**	255	38.2	256	38.2
401.bzip2	424	22.8	**424**	**22.8**	424	22.8	418	23.1	419	23.0	**419**	**23.0**
403.gcc	**250**	**32.2**	249	32.3	251	32.1	245	32.9	**245**	**32.9**	245	32.8
429.mcf	141	64.8	**142**	**64.4**	142	64.2	141	64.8	**142**	**64.4**	142	64.2
445.gobmk	**476**	**22.0**	473	22.2	476	22.0	404	26.0	**404**	**26.0**	404	26.0
456.hmmer	163	57.2	167	56.0	**164**	**56.8**	164	56.8	162	57.6	**162**	**57.6**
458.sjeng	436	27.8	**436**	**27.8**	436	27.8	**428**	**28.3**	428	28.3	427	28.3
462.libquantum	**12.9**	**1610**	12.9	1610	12.9	1610	**12.9**	**1610**	12.9	1610	12.9	1610
464.h264ref	555	39.9	**554**	**39.9**	509	43.5	**398**	**55.5**	398	55.5	398	55.6
471.omnetpp	243	25.7	**243**	**25.7**	243	25.7	163	38.5	170	36.8	**169**	**37.0**
473.astar	**228**	**30.8**	227	30.9	228	30.8	**228**	**30.8**	227	30.9	228	30.8
483.xalancbmk	128	54.0	125	55.0	**126**	**54.8**	128	54.0	125	55.0	**126**	**54.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Mon Mar 31 01:32:04 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2450 v2 @ 2.50GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB
```

Continued on next page

Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:       99010156 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
   EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 31 01:27

SPEC is set to: /spec
   Filesystem                                                              Type
   Size  Used Avail Use% Mounted on
   /dev/mapper/ddf1_4c53492020202020100000060l9e5d2034711471157d3ceb3p2 ext4
   260G   68G  179G  28% /

Additional information from dmidecode:
  BIOS Insyde Corp. RMIBV365 09/06/2013
  Memory:
   12x Micron 36JSF1G72PZ-1G6K1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 The Huawei RH2285H v2 and Huawei RH2285 v2 models are electronically equivalent.
 The results have been measured on a Huawei RH2285H v2 model.
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

Continued on next page

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2)
`-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Sep 2 13:39:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 September 2014.