IBM Corporation
IBM System x3650 M4 (Intel Xeon E5-2690 v2, 3.00 GHz)

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation


SPECint_rate2006 = 893
SPECint_rate_base2006 = 863

Hardware
CPU Name: Intel Xeon E5-2690 v2
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software
Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

Test date: May-2014
Hardware Availability: Dec-2013
Software Availability: Sep-2013
IBM Corporation
IBM System x3650 M4
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SPEC CINT2006 Result

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SPEC CINT2006 Result

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Base</td>
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<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>40</td>
<td>581</td>
<td>673</td>
<td>582</td>
<td>762</td>
<td>582</td>
<td>671</td>
<td>40</td>
<td>489</td>
<td>680</td>
<td>804</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>40</td>
<td>824</td>
<td>668</td>
<td>824</td>
<td>668</td>
<td>826</td>
<td>668</td>
<td>40</td>
<td>810</td>
<td>477</td>
<td>804</td>
</tr>
<tr>
<td>403.gcc</td>
<td>40</td>
<td>479</td>
<td>672</td>
<td>480</td>
<td>671</td>
<td>480</td>
<td>670</td>
<td>40</td>
<td>479</td>
<td>672</td>
<td>804</td>
</tr>
<tr>
<td>429.mcf</td>
<td>40</td>
<td>292</td>
<td>1250</td>
<td>292</td>
<td>1250</td>
<td>292</td>
<td>1250</td>
<td>40</td>
<td>292</td>
<td>1250</td>
<td>292</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>40</td>
<td>636</td>
<td>660</td>
<td>636</td>
<td>660</td>
<td>636</td>
<td>660</td>
<td>40</td>
<td>608</td>
<td>690</td>
<td>608</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>40</td>
<td>314</td>
<td>1190</td>
<td>314</td>
<td>1190</td>
<td>314</td>
<td>1190</td>
<td>40</td>
<td>290</td>
<td>1280</td>
<td>290</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>40</td>
<td>742</td>
<td>652</td>
<td>742</td>
<td>652</td>
<td>742</td>
<td>652</td>
<td>40</td>
<td>713</td>
<td>679</td>
<td>679</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>40</td>
<td>142</td>
<td>5840</td>
<td>142</td>
<td>5840</td>
<td>142</td>
<td>5840</td>
<td>40</td>
<td>142</td>
<td>5840</td>
<td>142</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>40</td>
<td>793</td>
<td>1120</td>
<td>794</td>
<td>1120</td>
<td>795</td>
<td>1110</td>
<td>40</td>
<td>791</td>
<td>1130</td>
<td>793</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>40</td>
<td>558</td>
<td>448</td>
<td>559</td>
<td>448</td>
<td>560</td>
<td>447</td>
<td>40</td>
<td>532</td>
<td>470</td>
<td>535</td>
</tr>
<tr>
<td>473.astar</td>
<td>40</td>
<td>583</td>
<td>482</td>
<td>585</td>
<td>480</td>
<td>582</td>
<td>482</td>
<td>40</td>
<td>583</td>
<td>482</td>
<td>585</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>40</td>
<td>309</td>
<td>893</td>
<td>309</td>
<td>892</td>
<td>310</td>
<td>891</td>
<td>40</td>
<td>309</td>
<td>893</td>
<td>309</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:
intel_idle.max_cstate=0

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6818
$Revision: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3c8e9f191
running on x3650M4 Sat May 17 08:19:30 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz
2 "physical id"s (chips)
IBM Corporation
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Platform Notes (Continued)

40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

  cpu cores : 10
  siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

From /proc/meminfo
MemTotal: 264339612 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)

uname -a:
Linux x3650M4 2.6.32-358.18.1.el6.x86_64 #1 SMP Fri Aug 2 17:04:38 EDT 2013
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 16 11:21

SPEC is set to: /home/SPECcpu-20140116-ic14.0

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_x3650m4-lv_home
  ext4 313G 114G 184G 39% /home

Additional information from dmidecode:
BIOS IBM -[VVE135VUS-1.60]- 12/05/2013
Memory:
8x Not Specified Not Specified
16x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "*/home/SPECcpu-20140116-ic14.0/libs/32:/home/SPECcpu-20140116-ic14.0/libs/64:/home/SPECcpu-20140116-ic14.0/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Continued on next page
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General Notes (Continued)

Filesystem page cache cleared with:
  echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc -m32

C++ benchmarks:
  icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
  -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m32

400.perlbench: icc -m64

Continued on next page
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Peak Compiler Invocation (Continued)

401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xSSE4.2 -ipo -03 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
462.libquantum: basepeak = yes

Continued on next page
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**SPEC int_rate2006 = 893**
**SPEC int_rate_base2006 = 863**

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**Peak Optimization Flags (Continued)**

464.h264ref:
- -xSSE4.2 (pass 2)
- -prof-gen (pass 1)
- -ipo (pass 2)
- -O3 (pass 2)
- -no-prec-div (pass 2)
- -prof-use (pass 2)
- -unroll2
- -ansi-alias

C++ benchmarks:

471.omnetpp:
- -xSSE4.2 (pass 2)
- -prof-gen (pass 1)
- -ipo (pass 2)
- -O3 (pass 2)
- -no-prec-div (pass 2)
- -prof-use (pass 2)
- -ansi-alias
- -opt-ra-region-strategy=block
- -L/sh
- -lsmartheap

473.astar: basepeak = yes

483.xalanchbk: basepeak = yes

---

**Peak Other Flags**

C benchmarks:

403.gcc:
- -Dalloca=_alloca

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The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-B.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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