### SPEC® CINT2006 Result

**Fujitsu**

PRIMERGY RX4770 M1, Intel Xeon E7-4820 v2, 2.00 GHz

**SPECint**\_rate2006 = NC

**SPECint\_rate\_base2006 = NC**

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**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Hardware**

<table>
<thead>
<tr>
<th>Copes</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
</tr>
<tr>
<td>401.bzip2</td>
</tr>
<tr>
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</tr>
<tr>
<td>429.mcf</td>
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<td>471.omnetpp</td>
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<tr>
<td>473.astar</td>
</tr>
<tr>
<td>483.xalancbmk</td>
</tr>
</tbody>
</table>

**Operating System:** Red Hat Enterprise Linux Server release 6.5 (Santiago)  
**Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux

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**Non-Compliant**

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Fujitsu or Intel.
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Copies</td>
<td>Seconds</td>
</tr>
<tr>
<td>400.perlbench</td>
<td>32</td>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
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Platform Notes
BIOS configuration:
Energy Performance = Performance

General Notes:
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006-MONITOR/libs/32:/SPECcpu2006-MONITOR/libs/64:/SPECcpu2006-MONITOR/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>/proc/sys/vm/drop_caches
runcspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation
C benchmarks:
  icc -m32
C++ benchmarks:
  icpc -m32

Base Portability Flags
400.perlbmk: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Non-Compliant
SPEC CINT2006 Result

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Base Optimization Flags

C benchmarks:
- xsSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
- xsSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
- Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icp -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
- 400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
- O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
- auto-ilp32
- 401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
- O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
- opt-reuse -auto-ilp32 -ansi-alias
- 403.gcc: basepeak = yes
- 429.mcf: basepeak = yes
- 45.gobm: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
- ansi-alias -opt-mem-layout-trans=3
- 456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
- 458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
- O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
- unroll4 = auto-ilp32
- 462.libquantum: basepeak = yes
- 464.h264ref: basepeak = yes

C++ benchmarks:
- 471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
- O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
- ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
- L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

C benchmarks:

403.gcc: -Dalloca=_alloca

This was files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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