Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

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<td>Software Availability: Sep-2013</td>
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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Cisco or Intel.

<table>
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<tr>
<td>410.bwaves</td>
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<tr>
<td>416.gamess</td>
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<tr>
<td>433.milc</td>
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<td>482.sphinx3</td>
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Cisco Systems
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Cisco Systems
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### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E7-4809 v2</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU MHz:</td>
<td>1900</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>12 cores, 2 chips, 6 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1,2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
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<tr>
<td>L3 Cache:</td>
<td>12 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache:</td>
<td>None</td>
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<tr>
<td>Memory:</td>
<td>256 GB (32 x 8 GB 2Rx4 162L-12800R-11, ECC, running at 1333 MHz and CL11)</td>
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<tr>
<td>Disk Subsystem:</td>
<td>1 X 300 GB 15000 RPM SAS</td>
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<td>Other Hardware:</td>
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</table>

### Software

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64</th>
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<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux</td>
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<td>Auto Parallel:</td>
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<td>File System:</td>
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<td>System State:</td>
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<td>Peak Pointers:</td>
<td>32/64-bit</td>
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<td>Other Software:</td>
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</table>
Cisco Systems  
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)  

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Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

| SPECfp_rate2006 | NC |
| SPECfp_rate_base2006 | NC |

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Cisco or Intel.

Platform Notes

Intel HT Technology = Enabled
CPU performance set to HPC
Power Technology set to Custom
CPU power state C6 set to Disabled
CPU Power State C1 Enhanced set to Disabled
Memory RAS configuration set to Maximum Performance
DRAM Clock Throttling set to Performance
Sysinfo program /opt/cpu2006-1.4/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on speccompcpu Tue Apr 22 17:54:51 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4809 v2 @ 1.90GHz
   2 "physical id" (chips)
   24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 12288 KB

meminfo
MemTotal: 264106280 kB
PagePages_Total: 0
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)

Continued on next page
Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

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Platform Notes (Continued)

uname -a:
Linux speccompcpu 2.6.32-358.el6.x86_64 #1 SMP Mon Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Apr 21 14:37

SPEC is set to: /opt/cpu2006-1.4
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 ext4 275G 29G 232G 11% /

Additional information from dmidecode:
BIOS Cisco Systems, Inc. /XM4-1.2.2.1.12.012920140234 01/29/2014
Memory:
32x 8 GB
32x 0xCE00 M393B1K70QB0-YK0 8 GB 1333 MHz 2 rank
16x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
"/sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
"/proc/sys/vm/drop_caches
runcmd command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Submitted by: "Sheshgiri I (shei)" <shei@cisco.com>
Submitted: Mon May 5 14:01:08 EDT 2014
Submission: cpu2006-20140505-29480.sub

Submitted by: "Sheshgiri I (shei)" <shei@cisco.com>
Submitted: Sat May 24 09:39:27 EDT 2014
Submission: cpu2006-20140505-29480.sub
Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

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CPU2006 license: 9019
Test sponsor: Cisco Systems
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### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

### Base Portability Flags

- `410.bwaves -DSPEC_CPU_LP64`
- `416.gamess -DSPEC_CPU_LP64`
- `433.milc -DSPEC_CPU_LP64`
- `434.zeusmp -DSPEC_CPU_LP64`
- `435.gromacs -DSPEC_CPU_LP64 -nofor_main`
- `436.cactusADM -DSPEC_CPU_LP64 -nofor_main`
- `437.leslie3d -DSPEC_CPU_LP64`
- `444.namd -DSPEC_CPU_LP64`
- `447.dealII -DSPEC_CPU_LP64`
- `450.soplex -DSPEC_CPU_LP64`
- `453.povray -DSPEC_CPU_LP64 -nofor_main`
- `459.GemsFDTD -DSPEC_CPU_LP64`
- `465.tonto -DSPEC_CPU_LP64`
- `470.lbm -DSPEC_CPU_LP64`
- `481.wrf -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- `482.sphinx3 -DSPEC_CPU_LP64`

### Base Optimization Flags

- **C benchmarks:**
  - `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias`
  - `-opt-mem-layout-trans=3`

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Cisco or Intel.

Base Optimization Flags (Continued)

C++ benchmarks:
- xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
- opt-mem-layout-trans=3

Fortran benchmarks:
- xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
- xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
- opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
SPEC CFP2006 Result

Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Cisco or Intel.

Peak Portability Flags (Continued)

C benchmarks:
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64
- 437.leshe3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64
- 447.dealII: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64 -nofor_main -nofor_main
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
- 433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
- prof-use(pass 2) -auto-ilp32
- no-prec-div(pass 2)
- prof-use(pass 2)
- auto-ilp32
- no-prec-div(pass 2)
- opt-mem-layout-trans=3(pass 2)
- prof-use(pass 2)
- auto-ilp32

C++ benchmarks:
- 447.dealII: basepeak = yes
- 450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
- prof-use(pass 2) -fno-alias -auto-ilp32
- no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
- prof-use(pass 2) -opt-malloc-options=3

Non-Compliant
Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Cisco or Intel.

Peak Optimization Flags (Continued)

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ips(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inlile-level = -scalar-reg

434.zeusmp: basepeak = yes
437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inlile-level = -scalar-rep

Benchmarks using both Fortran and C:

465.gromacs -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revB.html

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Non-Compliant
Cisco Systems
Cisco UCS B260 M4 (Intel Xeon E7-4809 v2, 1.90 GHz)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Apr-2014
Hardware Availability: May-2014
Software Availability: Sep-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Cisco or Intel.

You can also download the XML flags sources by saving these following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revB.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Fri Sep 19 15:52:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 June 2014.