



SPEC® CFP2006 Result

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IBM Corporation

IBM System x3850 X6
(Intel Xeon E7-4870 v2, 2.30 GHz)

SPECfp®_rate2006 = 1570

SPECfp_rate_base2006 = 1540

CPU2006 license: 11

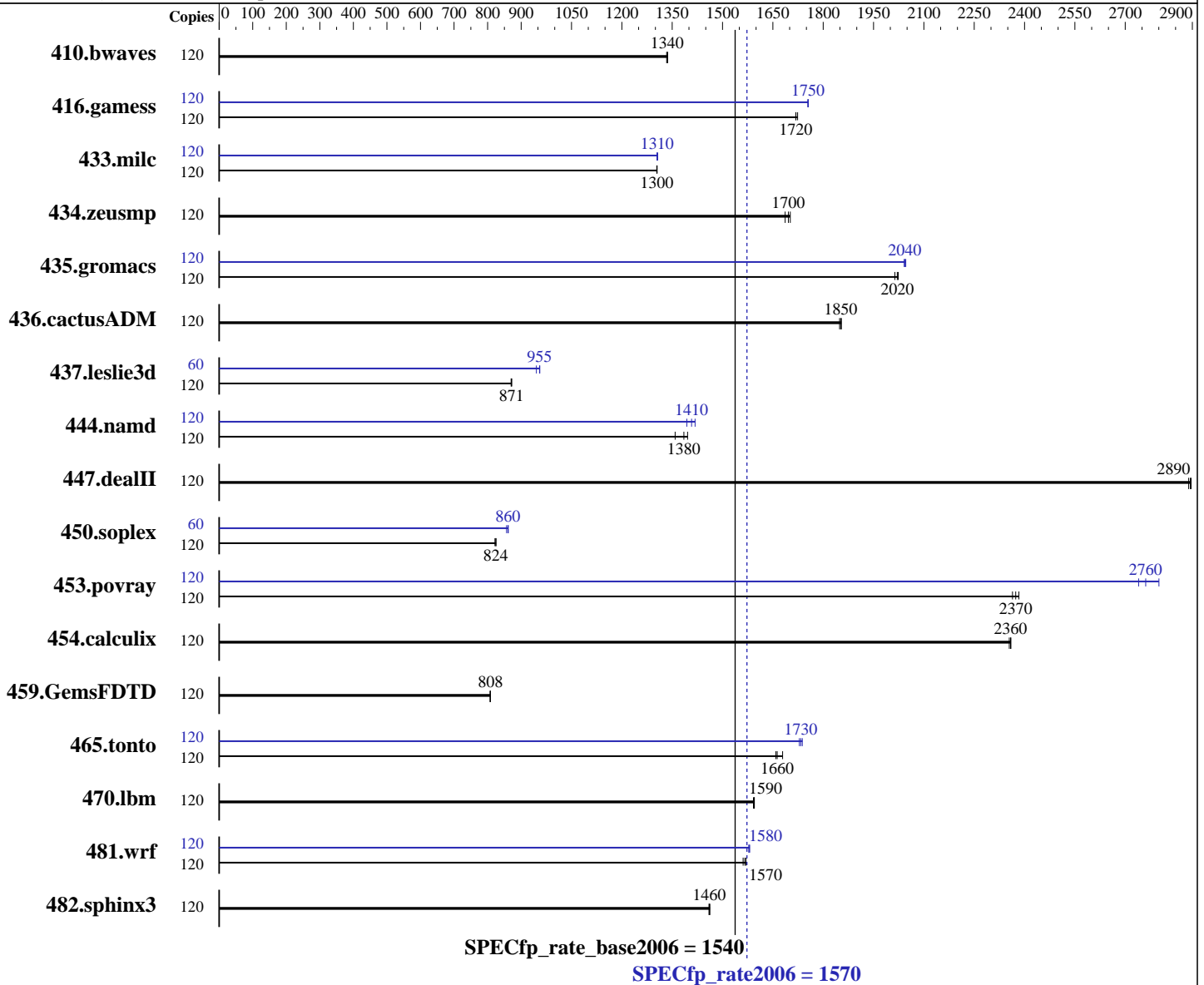
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E7-4870 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 60 cores, 4 chips, 15 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz)
Disk Subsystem: 1 x 400 GB SATA, SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	120	1223	1330	1220	1340	<u>1221</u>	<u>1340</u>	120	1223	1330	1220	1340	<u>1221</u>	<u>1340</u>		
416.gamess	120	1367	1720	<u>1367</u>	<u>1720</u>	1363	1720	120	1339	1750	1338	1760	<u>1339</u>	<u>1750</u>		
433.milc	120	845	1300	844	1310	<u>844</u>	<u>1300</u>	120	843	1310	<u>844</u>	<u>1310</u>	844	1300		
434.zeusmp	120	647	1690	<u>644</u>	<u>1700</u>	642	1700	120	647	1690	<u>644</u>	<u>1700</u>	642	1700		
435.gromacs	120	426	2010	<u>424</u>	<u>2020</u>	423	2020	120	<u>419</u>	<u>2040</u>	420	2040	419	2050		
436.cactusADM	120	776	1850	773	1850	<u>774</u>	<u>1850</u>	120	776	1850	773	1850	<u>774</u>	<u>1850</u>		
437.leslie3d	120	1294	872	<u>1295</u>	<u>871</u>	1297	870	60	<u>591</u>	<u>955</u>	590	956	597	945		
444.namd	120	<u>695</u>	<u>1380</u>	689	1400	708	1360	120	691	1390	679	1420	<u>684</u>	<u>1410</u>		
447.dealII	120	474	2900	<u>474</u>	<u>2890</u>	475	2890	120	474	2900	<u>474</u>	<u>2890</u>	475	2890		
450.soplex	120	<u>1214</u>	<u>824</u>	1213	825	1218	822	60	581	862	584	856	<u>582</u>	<u>860</u>		
453.povray	120	268	2380	270	2360	<u>269</u>	<u>2370</u>	120	228	2800	233	2740	<u>231</u>	<u>2760</u>		
454.calculix	120	420	2360	420	2350	<u>420</u>	<u>2360</u>	120	420	2360	420	2350	<u>420</u>	<u>2360</u>		
459.GemsFDTD	120	1575	808	1577	807	<u>1575</u>	<u>808</u>	120	1575	808	1577	807	<u>1575</u>	<u>808</u>		
465.tonto	120	703	1680	<u>710</u>	<u>1660</u>	712	1660	120	679	1740	<u>681</u>	<u>1730</u>	683	1730		
470.lbm	120	1034	1590	<u>1034</u>	<u>1590</u>	1036	1590	120	1034	1590	<u>1034</u>	<u>1590</u>	1036	1590		
481.wrf	120	859	1560	854	1570	<u>856</u>	<u>1570</u>	120	849	1580	848	1580	<u>849</u>	<u>1580</u>		
482.sphinx3	120	1599	1460	<u>1602</u>	<u>1460</u>	1602	1460	120	1599	1460	<u>1602</u>	<u>1460</u>	1602	1460		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

Operating Mode set to Maximum Performance in BIOS
Sysinfo program /cpu2006.1.2_14.0_aug2013/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191
running on Larry-Andromeda Wed Apr 30 07:00:26 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4870 v2 @ 2.30GHz
 4 "physical id"s (chips)
 120 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 15
siblings : 30
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal: 1058467616 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux Larry-Andromeda 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 29 16:35
```

```
SPEC is set to: /cpu2006.1.2_14.0_aug2013
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_larryandromed-lv_root ext4 357G 122G 217G 37% /
```

```
Additional information from dmidecode:
BIOS IBM -[A8E103KUS-1.00]- 11/21/2013
Memory:
32x NO DIMM Unknown
64x Samsung Unknown 16 GB 1333 MHz 2 rank
Continued on next page
```



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Platform Notes (Continued)

(End of data from sysinfo program)
Memory speed from dmidecode lists the downclocked speed of the run.

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2_14.0_aug2013/libs/32:/cpu2006.1.2_14.0_aug2013/libs/64:/cpu2006.1.2_14.0_aug2013/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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