



SPEC® CINT2006 Result

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Fujitsu

Fujitsu SPARC M10-4S

SPECint_rate2006 = 525

SPECint_rate_base2006 = 447

CPU2006 license: 19

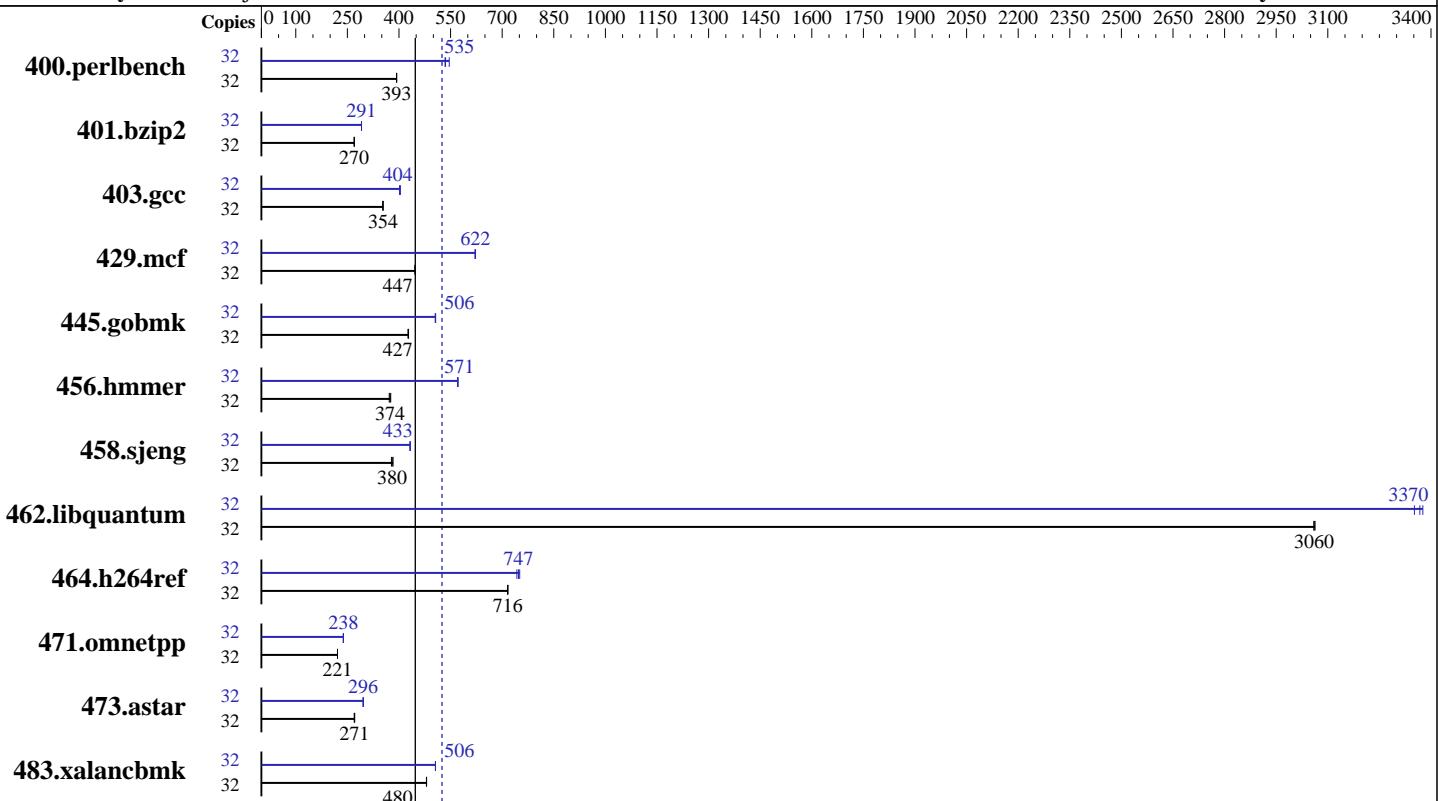
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2014

Hardware Availability: Apr-2014

Software Availability: Feb-2014



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Hardware

CPU Name:	SPARC64 X+
CPU Characteristics:	
CPU MHz:	3700
FPU:	Integrated
CPU(s) enabled:	16 cores, 1 chip, 16 cores/chip, 2 threads/core
CPU(s) orderable:	1 to 16 BBs; each BB contains 2 or 4 CPU chips; each CPU chip contains 4, 8, 12, 16 cores
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	24 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem:	tmpfs 600 GB 10,025 RPM Toshiba MBF2600RC SAS (for system disk)
Other Hardware:	None

Software

Operating System:	Solaris 11.1 SRU 15.4
Compiler:	C/C++: Version 12.3 of Oracle Solaris Studio 10/13 Patch Set
Auto Parallel:	No
File System:	tmpfs (output_root was used to put run directories in /tmp/cpu2006) zfs
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	795	393	796	393	795	393	32	572	546	586	534	584	535
401.bzip2	32	1145	270	1141	271	1144	270	32	1061	291	1061	291	1060	291
403.gcc	32	730	353	728	354	729	354	32	638	404	641	402	638	404
429.mcf	32	652	447	652	447	654	446	32	469	622	470	621	469	622
445.gobmk	32	785	428	787	427	786	427	32	664	506	663	507	664	506
456.hmmer	32	799	374	794	376	803	372	32	524	570	523	571	522	572
458.sjeng	32	1011	383	1018	380	1024	378	32	895	433	895	433	896	432
462.libquantum	32	217	3060	216	3060	217	3060	32	196	3380	197	3370	198	3350
464.h264ref	32	990	716	988	717	989	716	32	948	747	943	751	953	743
471.omnetpp	32	904	221	904	221	904	221	32	839	238	839	238	839	238
473.astar	32	829	271	828	271	832	270	32	759	296	759	296	759	296
483.xalancbmk	32	460	480	460	480	460	480	32	436	506	436	506	436	506

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

Processes were assigned to specific processors using 'pbind' commands.
The config file option 'submit' was used, along with a list of
processors in the 'BIND' variable, to generate the pbind commands.
(For details, please see the config file.)

Operating System Notes

Shell Environments:

ulimit -s 131072 was used to limit the space consumed by the stack
(and therefore make more space available to the heap).

The "Logical Domains Manager" service was turned off using the command "svcadm disable ldmd".

System Tunables:

(/etc/system parameters)

autoup = 1555200

Causes pages older than the listed number of seconds to be written by fsflush.

tune_t_fsflushr = 259200

Controls how many seconds elapse between runs of the page flush daemon, fsflush.

Platform Notes

Sysinfo program /export/cpu2006-v1.2/config/sysinfo
\$Rev: 6874 \$ \$Date::: 2013-11-20 #\$ 5ec117938769af2bf59ae0ed87ea9ccd
running on spec-bb01 Sun Mar 2 17:43:34 2014

This section contains SUT (System Under Test) info as seen by
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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /usr/sbin/psrinfo
    SPARC64-X+ (chipid 0, clock 3700 MHz)
    1 chips
    32 threads
    3700 MHz

From kstat:      16 cores

From prtconf: 129024 Megabytes

/etc/release:
    Oracle Solaris 11.1 SPARC
uname -a:
    SunOS spec-bb01 5.11 11.1 sun4v sparc sun4v

disk: df -h $SPEC
Filesystem          Size   Used  Available Capacity  Mounted on
rpool/export        547G   18G     453G     4%       /export

(End of data from sysinfo program)
```

Base Compiler Invocation

C benchmarks:

cc

C++ benchmarks:

CC

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_SOLARIS_SPARC
403.gcc: -DSPEC_CPU_SOLARIS
462.libquantum: -DSPEC_CPU_SOLARIS
483.xalancbmk: -DSPEC_CPU_SOLARIS
```

Base Optimization Flags

C benchmarks:

```
-fast -xtarget=sparc64x -fma=fused -xipo=2 -xpagesize=4M
-xalias_level=std -M map.bssalign
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -xtarget=sparc64x -fma=fused -xi0=2 -xpagesize=4M  
-xalias_level=compatible -library=stlport4 -M map.bssalign -lfast
```

Base Other Flags

C benchmarks:

```
-xjobs=8
```

C++ benchmarks:

```
-xjobs=8
```

Peak Compiler Invocation

C benchmarks:

```
cc
```

C++ benchmarks:

```
CC
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_SOLARIS_SPARC

```
403.gcc: -DSPEC_CPU_SOLARIS
```

```
462.libquantum: -DSPEC_CPU_SOLARIS
```

```
483.xalancbmk: -DSPEC_CPU_SOLARIS
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x  
-fma=fused -xppagesize=4M -xi0=1 -xalias_level=std  
-xrestrict -xprefetch=no%auto -x04 -M map.256M.align  
-lfast
```

```
401.bzip2: -xprofile=collect:./feedback(pass 1)  
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x  
-fma=fused -xppagesize=4M -xalias_level=strong  
-xprefetch=no%auto -W2,-Ainline:rs=1000 -W2,-Ainline:cs=500  
-W2,-Ainline:inc=60 -M map.256M.align -lfast
```

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Peak Optimization Flags (Continued)

403.gcc: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -xipo=2 -xprefetch=no%auto
-M map.256M.align

429.mcf: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -xipo=2 -xalias_level=std
-xprefetch_level=1 -xprefetch=latx:0.2 -W2,-Asac
-M map.256M.align

445.gobmk: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -x04 -xalias_level=std
-xrestrict -xprefetch=no%auto -Wc,-Qiselect-funcalign=64
-M map.256M.align

456.hmmr: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -xipo=1 -xalias_level=std
-xunroll=6 -xprefetch=latx:3.0
-Wc,-Qpeep-Ex:minmax_use_cmov=2 -Wc,-Qms_pipe+ulmscc=1
-M map.256M.align

458.sjeng: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -x04 -xipo=2 -xalias_level=std
-xprefetch=no%auto -Wc,-Qlu-en=1-t=4 -M map.256M.align

462.libquantum: -fast -xtarget=sparc64x -fma=fused -xppagesize=4M -xipo=2
-xalias_level=std -xunroll=8 -xprefetch=no%auto
-Wc,-Qlu-en=1-t=4 -M map.256M.align -lbsdmalloc

464.h264ref: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -xalias_level=strong -xipo=1
-Wc,-Qiselect-funcalign=64 -M map.256M.align

C++ benchmarks:

471.omnetpp: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -xipo=1 -xalias_level=compatible
-xunroll=2 -xprefetch_level=3 -W2,-Asac -library=stlport4
-M map.256M.align -lfast

473.astar: -xprofile=collect:./feedback(pass 1)
-xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
-fma=fused -xppagesize=4M -xalias_level=compatible
-xprefetch=no%auto -library=stlport4 -M map.256M.align
-lfast

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Peak Optimization Flags (Continued)

```
483.xalancbmk: -xprofile=collect:./feedback(pass 1)
               -xprofile=use:./feedback(pass 2) -fast -xtarget=sparc64x
               -fma=fused -xpagesize=4M -xiwo=2 -xalias_level=compatible
               -xdepend -xprefetch_level=3 -xprefetch=latx:0.4
               -library=stlport4 -Wc,-Qpeep-Ex:minmax_use_cmov=2
               -Wc,-Qms_pipe+ulmscc=1 -W2,-Asac -M map.256M.align -lfast
```

Peak Other Flags

C benchmarks:
-xjobs=8

C++ benchmarks:
-xjobs=8

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Oracle-Solaris-Studio12.3-SPARC64X.20140423.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Oracle-Solaris-Studio12.3-SPARC64X.20140423.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

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