



SPEC® CFP2006 Result

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HITACHI

BladeSymphony BS2000 (Intel Xeon E5-2670 v2)

SPECfp®2006 = 97.3

SPECfp_base2006 = 92.9

CPU2006 license: 35

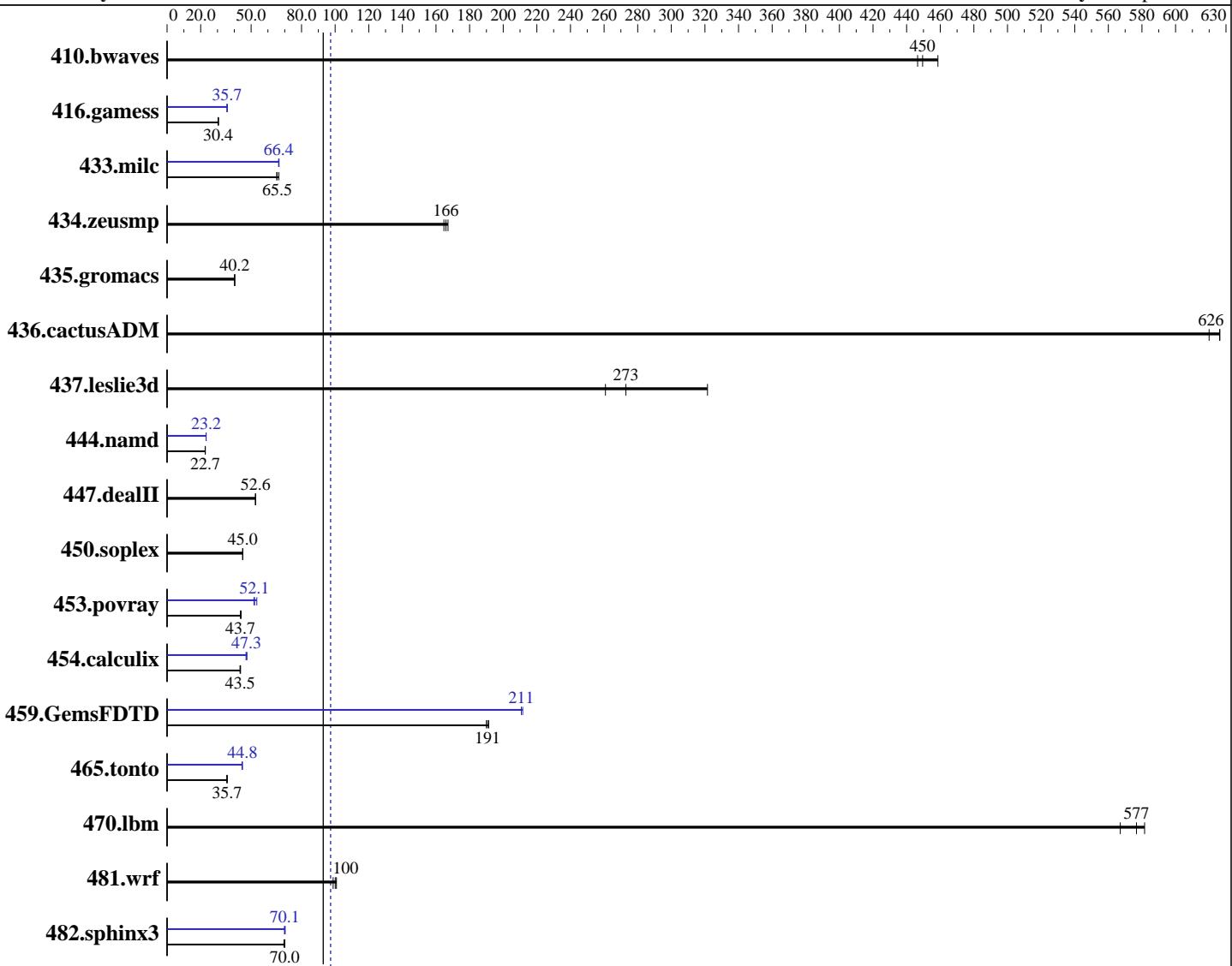
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Mar-2014

Hardware Availability: Nov-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2670 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Compiler: 2.6.32-358.23.2.el6.x86_64
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: none

Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
Other Hardware: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	30.2	450	30.4	447	29.6	459	30.2	450	30.4	447	29.6	459
416.gamess	639	30.7	644	30.4	645	30.3	547	35.8	549	35.7	549	35.7
433.milc	138	66.5	141	65.2	140	65.5	138	66.4	138	66.6	138	66.4
434.zeusmp	54.4	167	54.8	166	55.2	165	54.4	167	54.8	166	55.2	165
435.gromacs	178	40.2	178	40.2	177	40.3	178	40.2	178	40.2	177	40.3
436.cactusADM	19.1	626	19.3	620	19.1	626	19.1	626	19.3	620	19.1	626
437.leslie3d	34.4	273	36.0	261	29.2	322	34.4	273	36.0	261	29.2	322
444.namd	353	22.7	354	22.7	352	22.8	345	23.2	346	23.2	345	23.2
447.dealII	217	52.6	217	52.6	217	52.6	217	52.6	217	52.6	217	52.6
450.soplex	185	45.0	185	45.1	185	45.0	185	45.0	185	45.1	185	45.0
453.povray	121	44.1	122	43.7	122	43.7	103	51.7	102	52.1	99.6	53.4
454.calculix	190	43.5	190	43.5	189	43.7	174	47.5	176	47.0	174	47.3
459.GemsFDTD	55.9	190	55.6	191	55.4	191	50.1	212	50.3	211	50.3	211
465.tonto	276	35.7	277	35.6	275	35.8	220	44.7	220	44.8	220	44.8
470.lbm	23.8	577	23.6	582	24.2	567	23.8	577	23.6	582	24.2	567
481.wrf	111	101	112	100	113	98.8	111	101	112	100	113	98.8
482.sphinx3	279	70.0	280	69.5	278	70.1	278	70.1	277	70.3	279	69.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date::: 2012-07-17 #$
running on DPx4-SPECCPU Sat Mar 15 04:29:36 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2670 v2 @ 2.50GHz
        2 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings : 20
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:       132193392 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DPx4-SPECCPU 2.6.32-358.23.2.el6.x86_64 #1 SMP Sat Sep 14 05:32:37 EDT
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 12 11:36
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_dpx4speccpu-lv_home
                ext4   81G   5.3G   72G   7% /home
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. 4.6.5 11/28/2013
Memory:
16x None None
8x Sams M393 16 GB 1866 MHz 1 rank
```

```
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY="granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "20"

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled  
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

BladeSymphony BS2000 and Hitachi Compute Blade 2000 are electronically equivalent.
The results have been measured on a BladeSymphony BS2000

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>
<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>
<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>



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