



# SPEC® CFP2006 Result

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**HITACHI**

**SPECfp®2006 = 99.9**

Compute Blade 2000 (Intel Xeon E5-2697 v2)

**SPECfp\_base2006 = 95.3**

CPU2006 license: 35

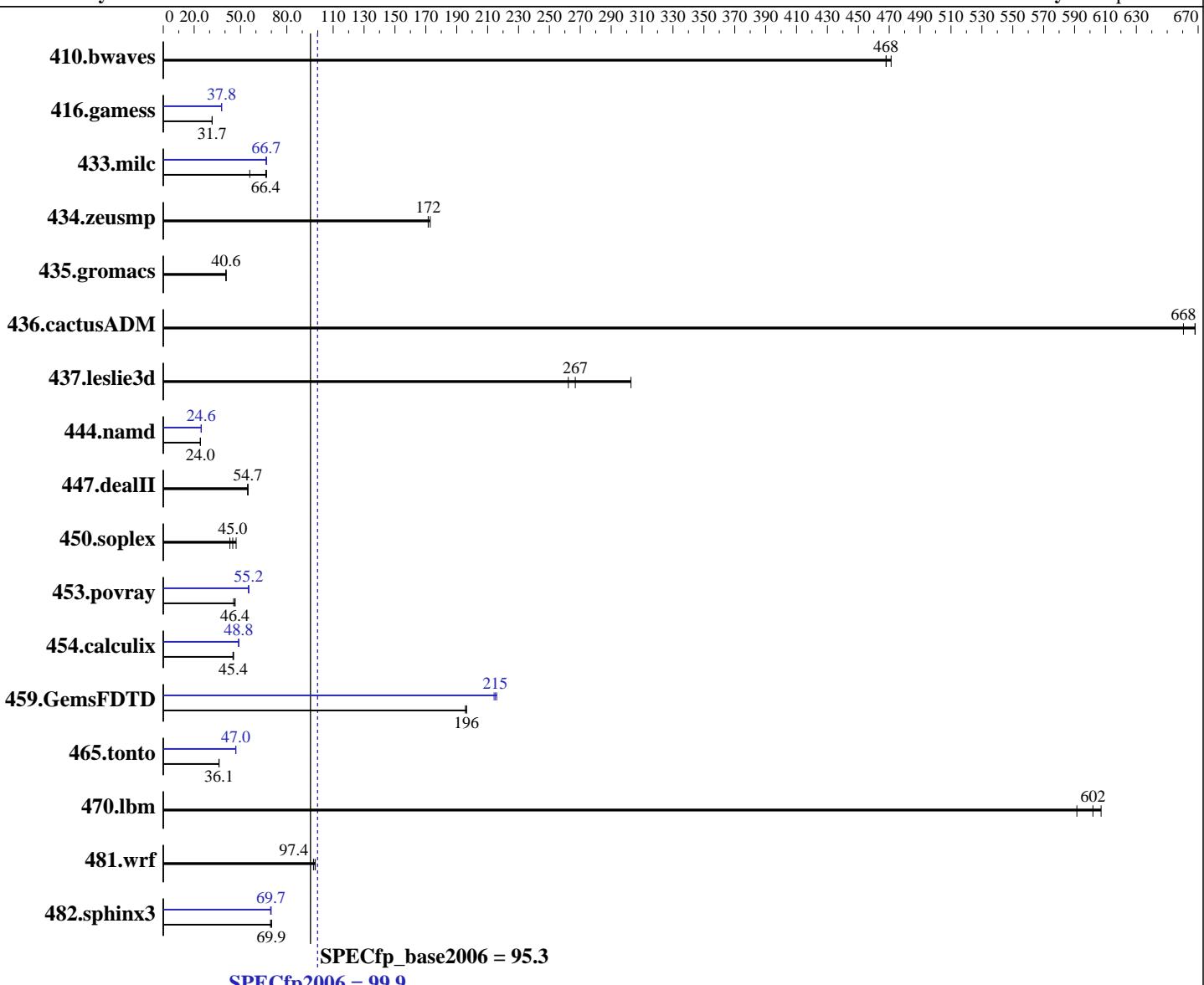
Test date: Mar-2014

Test sponsor: HITACHI

Hardware Availability: Nov-2013

Tested by: HITACHI

Software Availability: Sep-2013



## Hardware

CPU Name: Intel Xeon E5-2697 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 Compiler: 2.6.32-358.23.2.el6.x86\_64  
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: none

Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.8	471	29.0	468	<b>29.0</b>	<b>468</b>	28.8	471	29.0	468	<b>29.0</b>	<b>468</b>
416.gamess	619	31.6	<b>618</b>	<b>31.7</b>	618	31.7	<b>518</b>	<b>37.8</b>	518	37.8	520	37.7
433.milc	137	66.9	<b>138</b>	<b>66.4</b>	164	56.0	<b>137</b>	<b>66.9</b>	138	66.6	<b>138</b>	<b>66.7</b>
434.zeusmp	52.6	173	53.1	172	<b>53.0</b>	<b>172</b>	52.6	173	53.1	172	<b>53.0</b>	<b>172</b>
435.gromacs	176	40.7	176	40.6	<b>176</b>	<b>40.6</b>	176	40.7	176	40.6	<b>176</b>	<b>40.6</b>
436.cactusADM	18.1	660	17.9	668	<b>17.9</b>	<b>668</b>	18.1	660	17.9	668	<b>17.9</b>	<b>668</b>
437.leslie3d	31.0	303	35.8	262	<b>35.2</b>	<b>267</b>	31.0	303	35.8	262	<b>35.2</b>	<b>267</b>
444.namd	<b>334</b>	<b>24.0</b>	333	24.1	334	24.0	<b>326</b>	<b>24.6</b>	327	24.5	<b>326</b>	<b>24.6</b>
447.dealII	209	54.7	209	54.8	<b>209</b>	<b>54.7</b>	209	54.7	209	54.8	<b>209</b>	<b>54.7</b>
450.soplex	193	43.1	177	47.2	<b>185</b>	<b>45.0</b>	193	43.1	177	47.2	<b>185</b>	<b>45.0</b>
453.povray	117	45.5	<b>115</b>	<b>46.4</b>	114	46.5	<b>96.0</b>	<b>55.4</b>	<b>96.3</b>	<b>55.2</b>	96.5	55.1
454.calculix	<b>182</b>	<b>45.4</b>	183	45.1	181	45.5	<b>169</b>	<b>48.8</b>	169	48.9	169	48.8
459.GemsFDTD	<b>54.0</b>	<b>196</b>	54.2	196	54.0	196	<b>49.1</b>	216	49.5	214	<b>49.3</b>	<b>215</b>
465.tonto	272	36.1	<b>273</b>	<b>36.1</b>	273	36.0	<b>209</b>	47.0	<b>210</b>	<b>47.0</b>	210	46.9
470.lbm	22.6	607	23.2	591	<b>22.8</b>	<b>602</b>	22.6	607	23.2	591	<b>22.8</b>	<b>602</b>
481.wrf	<b>115</b>	<b>97.4</b>	115	97.3	113	98.5	<b>115</b>	<b>97.4</b>	115	97.3	113	98.5
482.sphinx3	281	69.4	278	70.1	<b>279</b>	<b>69.9</b>	<b>280</b>	<b>69.7</b>	281	69.5	279	69.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date::: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on DPx4-SPECCPU Tue Mar 18 16:48:05 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Hardware Availability: Nov-2013

Tested by: HITACHI

Software Availability: Sep-2013

## Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
        2 "physical id"s (chips)
        48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 12
    siblings : 24
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:       132192304 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DPx4-SPECCPU 2.6.32-358.23.2.el6.x86_64 #1 SMP Sat Sep 14 05:32:37 EDT
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 18 11:07
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_dpx4speccpu-lv_home
                    ext4   81G   5.3G   72G   7% /home
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. 4.6.5 11/28/2013
Memory:
16x None None
8x Sams M393 16 GB 1866 MHz 1 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY="granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "24"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled  
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

BladeSymphony BS2000 and Hitachi Compute Blade 2000 are electronically equivalent.  
The results have been measured on a BladeSymphony BS2000

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel
```

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**Software Availability:** Sep-2013

**SPECfp\_base2006 = 95.3**

## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>  
<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>  
<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>



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