SPEC® CINT2006 Result

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IBM Corporation

IBM System x3500 M4
(Intel Xeon E5-2609 v2, 2.50 GHz)

SPECint®_rate2006 = 249
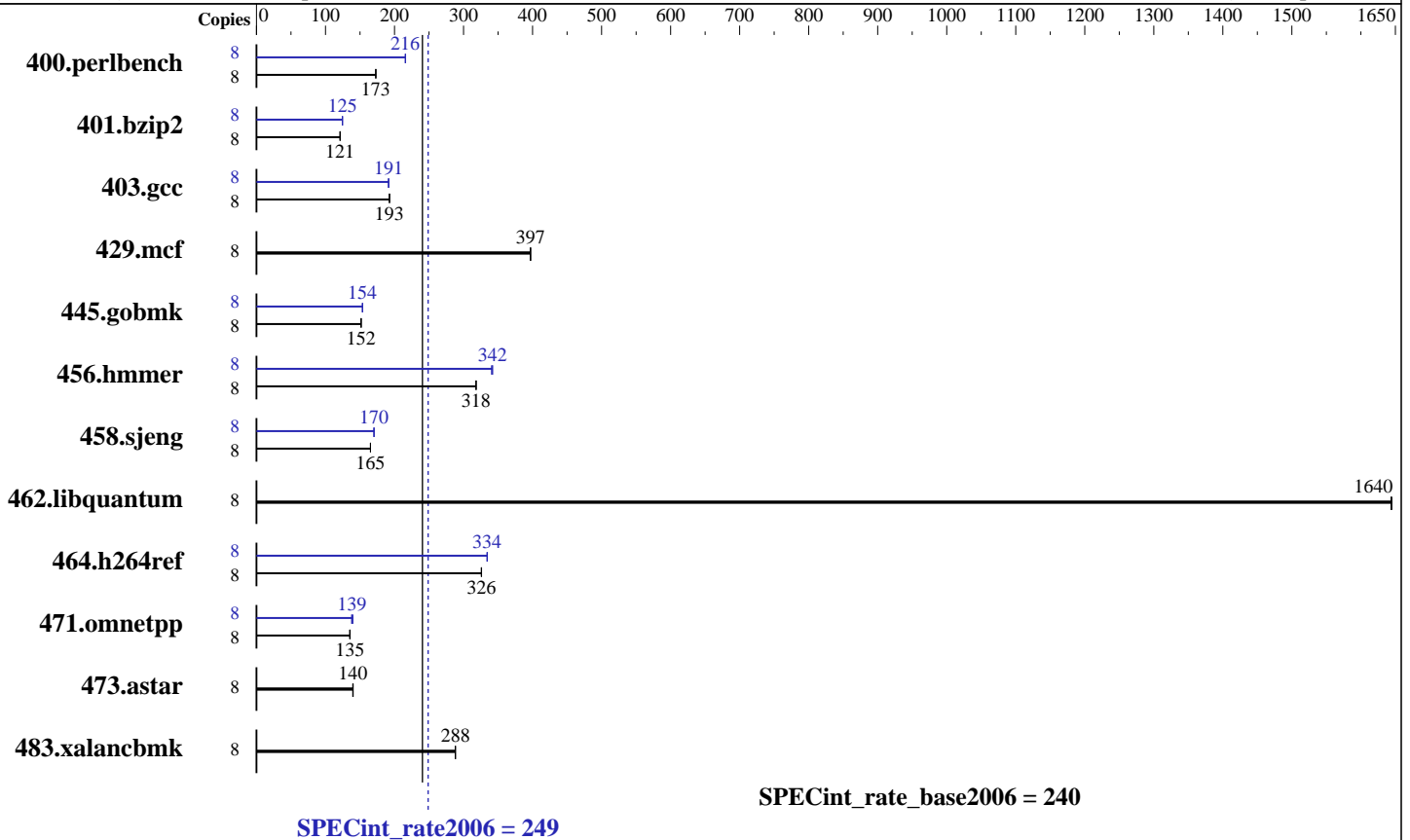
SPECint_rate_base2006 = 240

CPU2006 license: 11	**Test date:** Mar-2014
Test sponsor: IBM Corporation	**Hardware Availability:** Dec-2013
Tested by: IBM Corporation	**Software Availability:** Sep-2013

Copies

Benchmark	Copies	Value
400.perlbench	8	216
	8	173
401.bzip2	8	125
	8	121
403.gcc	8	191
	8	193
429.mcf	8	397
445.gobmk	8	154
	8	152
456.hmmer	8	342
	8	318
458.sjeng	8	170
	8	165
462.libquantum	8	1640
464.h264ref	8	334
	8	326
471.omnetpp	8	139
	8	135
473.astar	8	140
483.xalancbmk	8	288

SPECint_rate_base2006 = 240

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Hardware

CPU Name:	Intel Xeon E5-2609 v2
CPU Characteristics:	
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz)
Disk Subsystem:	1 x 1 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	452	173	**452**	**173**	452	173	8	362	216	**362**	**216**	362	216
401.bzip2	8	636	121	**637**	**121**	638	121	8	619	125	620	124	**620**	**125**
403.gcc	8	334	193	**334**	**193**	334	193	8	337	191	**337**	**191**	336	192
429.mcf	8	184	397	184	397	**184**	**397**	8	184	397	184	397	**184**	**397**
445.gobmk	8	553	152	554	152	**554**	**152**	8	546	154	547	153	**546**	**154**
456.hmmer	8	**235**	**318**	235	318	235	318	8	**218**	**342**	218	342	219	341
458.sjeng	8	586	165	**586**	**165**	586	165	8	**568**	**170**	568	170	569	170
462.libquantum	8	**101**	**1640**	101	1640	101	1640	8	**101**	**1640**	101	1640	101	1640
464.h264ref	8	**543**	**326**	543	326	543	326	8	530	334	**530**	**334**	529	335
471.omnetpp	8	368	136	370	135	**369**	**135**	8	363	138	358	140	**361**	**139**
473.astar	8	403	140	401	140	**402**	**140**	8	403	140	401	140	**402**	**140**
483.xalancbmk	8	191	288	192	288	**192**	**288**	8	191	288	192	288	**192**	**288**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on x3500M4 Mon Mar 10 14:34:48 2014

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2609 v2 @ 2.50GHz
       2 "physical id"s (chips)
       8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The

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Platform Notes (Continued)

```
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

 From /proc/meminfo
   MemTotal:       264657388 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

 From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
   Linux x3500M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 Mar 10 14:32

 SPEC is set to: /home/SPECcpu-new
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/mapper/vg_intelcrb-lv_home
                   ext4    863G   15G  804G   2% /home

 Additional information from dmidecode:
   BIOS IBM -[TKE133FUS-1.50]- 07/26/2013
   Memory:
    8x Not Specified Not Specified
   16x Samsung M393B2G70QH0-CMA 16 GB 1333 MHz 2 rank

 (End of data from sysinfo program)
 "Not Specified" memory information from dmidecode indicates unused DIMM slots.
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
```

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General Notes (Continued)

```
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

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Peak Compiler Invocation (Continued)

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
  `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll2 -ansi-alias`

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Peak Optimization Flags (Continued)

C++ benchmarks:

    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/sh -lsmartheap

    473.astar: basepeak = yes

    483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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