SPEC® CINT2006 Result

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Lenovo Group Limited

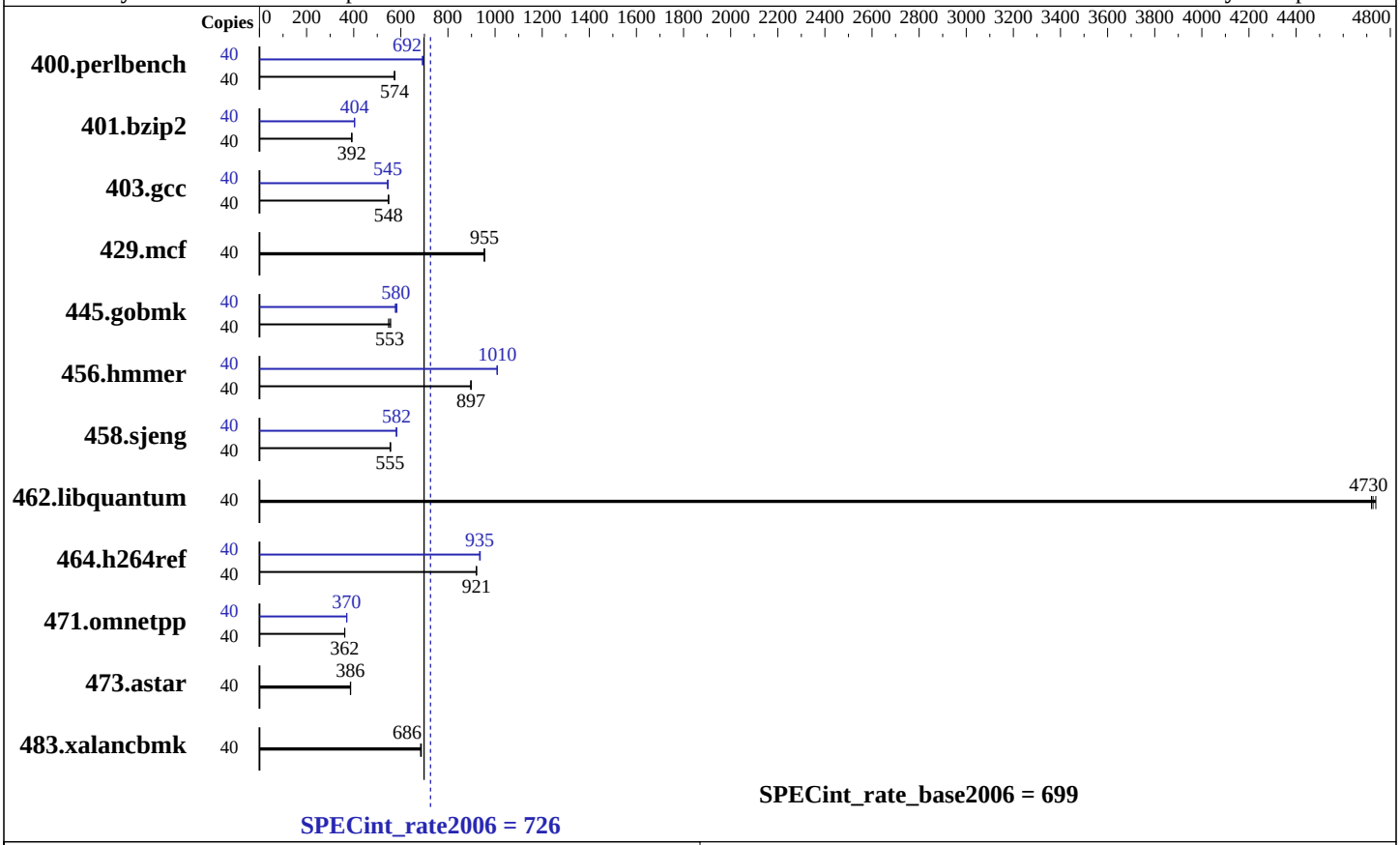
Lenovo ThinkServer TD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

SPECint®_rate2006 = 726

SPECint_rate_base2006 = 699

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Dec-2013
Hardware Availability: Feb-2014
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2470 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx8 PC3L-12800R-11, ECC)
Disk Subsystem:	1 x 400 GB SATA SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	683	572	**681**	**574**	681	574	40	**565**	**692**	565	691	562	695
401.bzip2	40	986	391	984	392	**986**	**392**	40	**956**	**404**	958	403	956	404
403.gcc	40	**588**	**548**	588	548	588	548	40	590	546	593	543	**591**	**545**
429.mcf	40	382	954	382	956	**382**	**955**	40	382	954	382	956	**382**	**955**
445.gobmk	40	766	548	**759**	**553**	753	557	40	728	576	719	583	**724**	**580**
456.hmmer	40	**416**	**897**	415	900	416	897	40	**370**	**1010**	370	1010	370	1010
458.sjeng	40	**872**	**555**	873	555	869	557	40	834	580	831	582	**831**	**582**
462.libquantum	40	175	4740	176	4720	**175**	**4730**	40	175	4740	176	4720	**175**	**4730**
464.h264ref	40	961	921	**961**	**921**	961	921	40	947	935	**947**	**935**	945	937
471.omnetpp	40	691	362	**691**	**362**	692	361	40	675	371	676	370	**675**	**370**
473.astar	40	728	386	**727**	**386**	725	387	40	728	386	**727**	**386**	725	387
483.xalancbmk	40	402	687	404	684	**402**	**686**	40	402	687	404	684	**402**	**686**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on TD340 Fri Dec 13 20:27:47 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 20
```

Continued on next page

Platform Notes (Continued)

```
        physical 0: cores 0 1 2 3 4 8 9 10 11 12
        physical 1: cores 0 1 2 3 4 8 9 10 11 12
     cache size : 25600 KB

  From /proc/meminfo
     MemTotal:       99029440 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux TD340 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
     x86_64 x86_64 GNU/Linux

  run-level 3 Dec 13 20:22

  SPEC is set to: /usr/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/ddf1_4c53492020202020100000791000926147114711c201014ep1
                    ext4    363G  250G   94G  73% /

  Additional information from dmidecode:
    BIOS LENOVO A3TS0FA 12/05/2013
    Memory:
     12x   8 GB
     12x Samsung M393B1G73QH0-YK0 8 GB 1600 MHz 2 rank

  (End of data from sysinfo program)
  TD340 support 3 channels and 6 DIMMS per CPU, total 6 channels and
  12 DIMMS.  All 12 DIMM slots installed with 8 GB DIMM for this run.
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

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