



SPEC[®] CFP2006 Result

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Lenovo Group Limited

Lenovo ThinkServer TD340 (Intel Xeon E5-2470 v2, 2.40 GHz)

SPECfp[®]_rate2006 = 496

SPECfp_rate_base2006 = 483

CPU2006 license: 9017

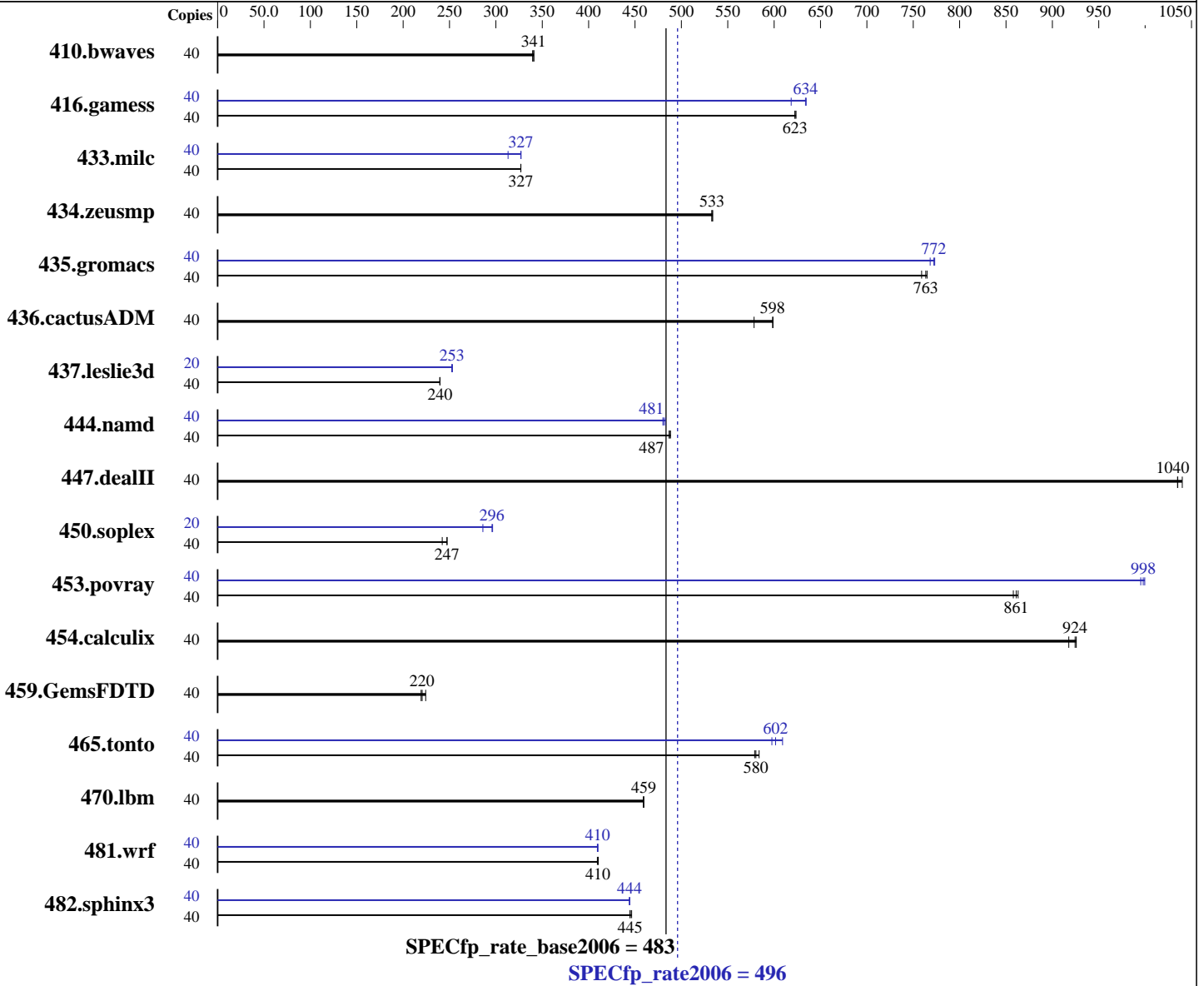
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Dec-2013

Hardware Availability: Feb-2014

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2470 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx8 PC3L-12800R-11, ECC)
Disk Subsystem: 1 x 400 GB SATA SSD
Other Hardware: None

System State: Run level 3 (Full multiuser with network)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	<u>1595</u>	<u>341</u>	1601	340	1594	341	40	<u>1595</u>	<u>341</u>	1601	340	1594	341
416.gamess	40	1258	622	1256	624	<u>1258</u>	<u>623</u>	40	1267	618	1234	635	<u>1236</u>	<u>634</u>
433.milc	40	1123	327	1123	327	<u>1123</u>	<u>327</u>	40	<u>1123</u>	<u>327</u>	1123	327	1173	313
434.zeusmp	40	<u>683</u>	<u>533</u>	683	533	682	534	40	<u>683</u>	<u>533</u>	683	533	682	534
435.gromacs	40	373	765	<u>374</u>	<u>763</u>	376	759	40	372	768	<u>370</u>	<u>772</u>	369	773
436.cactusADM	40	<u>799</u>	<u>598</u>	798	599	826	578	40	<u>799</u>	<u>598</u>	798	599	826	578
437.leslie3d	40	1569	240	<u>1569</u>	<u>240</u>	1568	240	20	743	253	<u>744</u>	<u>253</u>	744	253
444.namd	40	659	487	657	488	<u>658</u>	<u>487</u>	40	668	480	<u>667</u>	<u>481</u>	665	483
447.dealII	40	<u>442</u>	<u>1040</u>	442	1040	440	1040	40	<u>442</u>	<u>1040</u>	442	1040	440	1040
450.soplex	40	1378	242	<u>1349</u>	<u>247</u>	1348	247	20	583	286	<u>564</u>	<u>296</u>	563	296
453.povray	40	<u>247</u>	<u>861</u>	247	863	248	858	40	213	1000	214	995	<u>213</u>	<u>998</u>
454.calculix	40	<u>357</u>	<u>924</u>	356	926	360	918	40	<u>357</u>	<u>924</u>	356	926	360	918
459.GemsFDTD	40	1935	219	<u>1925</u>	<u>220</u>	1892	224	40	1935	219	<u>1925</u>	<u>220</u>	1892	224
465.tonto	40	674	584	<u>678</u>	<u>580</u>	680	579	40	659	598	<u>654</u>	<u>602</u>	646	609
470.lbm	40	<u>1196</u>	<u>459</u>	1196	459	1197	459	40	<u>1196</u>	<u>459</u>	1196	459	1197	459
481.wrf	40	1091	409	1089	410	<u>1090</u>	<u>410</u>	40	1091	409	1090	410	<u>1090</u>	<u>410</u>
482.sphinx3	40	1753	445	<u>1753</u>	<u>445</u>	1747	446	40	<u>1755</u>	<u>444</u>	1755	444	1756	444

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on TD340 Sat Dec 14 09:22:44 2013

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
 2 "physical id"s (chips)
 40 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores      : 10
siblings       : 20
physical 0:    : cores 0 1 2 3 4 8 9 10 11 12
physical 1:    : cores 0 1 2 3 4 8 9 10 11 12
cache size     : 25600 KB
```

From /proc/meminfo

```
MemTotal:      99029440 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux TD340 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Dec 13 20:22

SPEC is set to: /usr/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/df1_4c53492020202020100000791000926147114711c201014ep1
  ext4          363G    250G   94G   73% /
```

Additional information from dmidecode:

BIOS LENOVO A3TS0FA 12/05/2013

Memory:

```
12x      8 GB
12x Samsung M393B1G73QH0-YK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

TD340 support 3 channels and 6 DIMMS per CPU, total 6 channels and 12 DIMMS. All 12 DIMM slots installed with 8 GB DIMM for this run.



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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.deallI: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
 -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

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Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 22:45:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 April 2014.