SPEC® CFP2006 Result

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Lenovo Group Limited

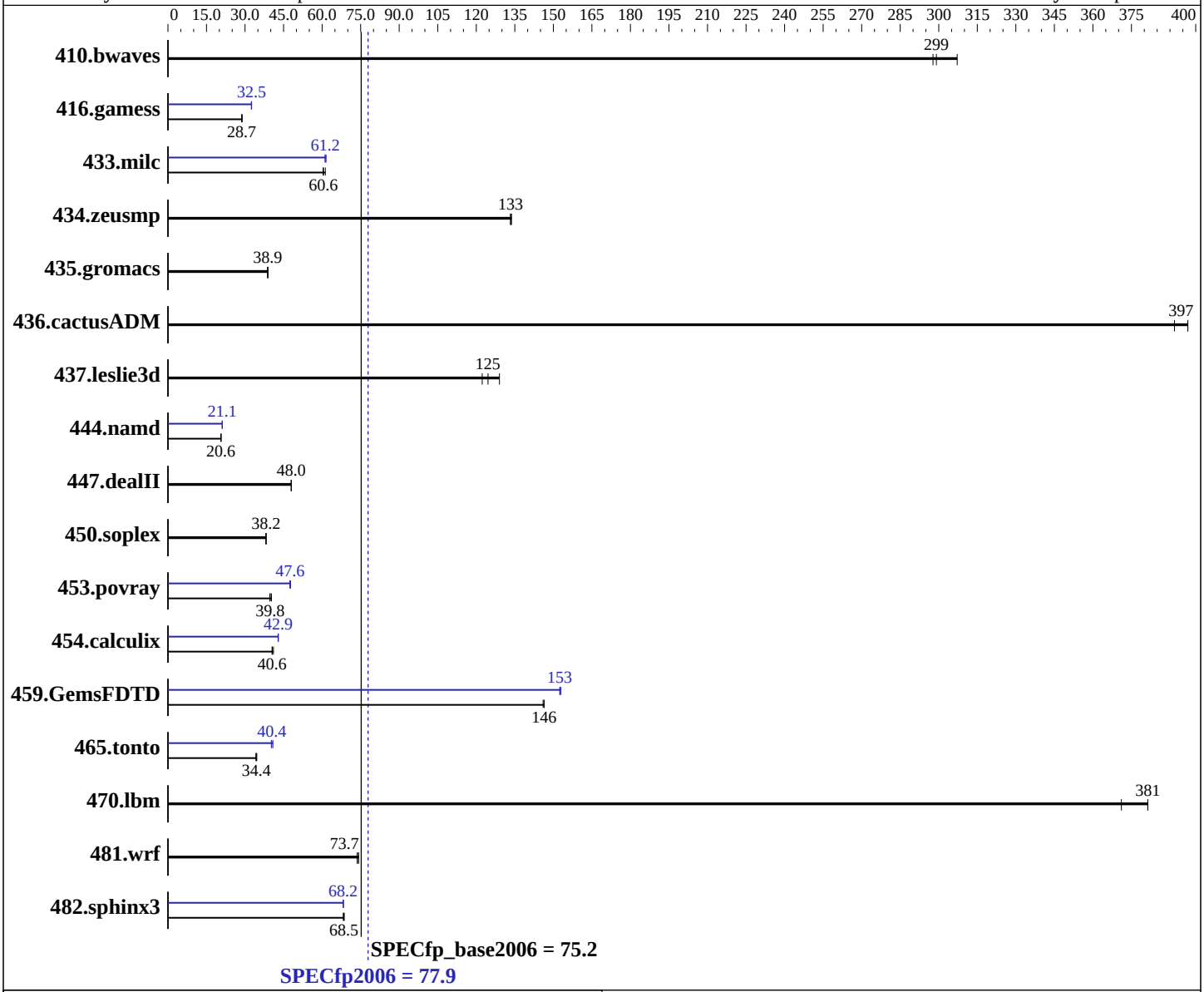
Lenovo ThinkServer TD340 (Intel Xeon E5-2430 v2, 2.50 GHz)

SPECfp®2006 = 77.9

SPECfp_base2006 = 75.2

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Mar-2014
Hardware Availability: Feb-2014
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2430 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx8 PC3L-12800R-11, ECC)
Disk Subsystem: 1 x 400 GB SATA SSD
Other Hardware: None

System State: Run level 3 (Full multiuser with network)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	45.6	298	44.2	307	**45.4**	**299**	45.6	298	44.2	307	**45.4**	**299**
416.gamess	**682**	**28.7**	682	28.7	678	28.9	604	32.4	602	32.5	**602**	**32.5**
433.milc	152	60.4	**152**	**60.6**	150	61.3	**150**	**61.2**	149	61.5	150	61.2
434.zeusmp	68.0	134	68.2	133	**68.2**	**133**	68.0	134	68.2	133	**68.2**	**133**
435.gromacs	184	38.8	**184**	**38.9**	184	38.9	184	38.8	**184**	**38.9**	184	38.9
436.cactusADM	30.1	397	**30.1**	**397**	30.5	392	30.1	397	**30.1**	**397**	30.5	392
437.leslie3d	**75.5**	**125**	72.9	129	76.9	122	**75.5**	**125**	72.9	129	76.9	122
444.namd	388	20.7	389	20.6	**388**	**20.6**	**379**	**21.1**	381	21.1	379	21.2
447.dealII	**238**	**48.0**	238	48.0	238	48.0	**238**	**48.0**	238	48.0	238	48.0
450.soplex	**218**	**38.2**	219	38.1	218	38.2	**218**	**38.2**	219	38.1	218	38.2
453.povray	**134**	**39.8**	134	39.7	132	40.3	112	47.7	112	47.4	**112**	**47.6**
454.calculix	204	40.5	**203**	**40.6**	201	41.1	**192**	**42.9**	193	42.9	192	43.0
459.GemsFDTD	72.7	146	**72.5**	**146**	72.5	146	69.6	153	69.4	153	**69.6**	**153**
465.tonto	**286**	**34.4**	288	34.2	285	34.6	241	40.9	245	40.2	**243**	**40.4**
470.lbm	**36.0**	**381**	36.0	381	37.0	371	**36.0**	**381**	36.0	381	37.0	371
481.wrf	**151**	**73.7**	150	74.2	152	73.6	**151**	**73.7**	150	74.2	152	73.6
482.sphinx3	**285**	**68.5**	286	68.2	284	68.6	**286**	**68.2**	285	68.3	286	68.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /root/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on TD340 Mon Mar 10 17:35:59 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2430 v2 @ 2.50GHz
```

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Platform Notes (Continued)

```
        2 "physical id"s (chips)
        24 "processors"
     cores, siblings (Caution: counting these is hw and system dependent.  The
     following excerpts from /proc/cpuinfo might not be reliable.  Use with
     caution.)
        cpu cores : 6
        siblings  : 12
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
     cache size : 15360 KB

  From /proc/meminfo
     MemTotal:       99031464 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux TD340 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64
     x86_64 x86_64 GNU/Linux

  run-level 3 Mar 10 10:55

  SPEC is set to: /root/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/sda2      ext4    363G   56G  289G  17% /

  Additional information from dmidecode:
    BIOS LENOVO A3TS0FA 12/05/2013
    Memory:
     12x   8 GB
     12x Samsung M393B1G73QH0-YK0 8 GB 1600 MHz 2 rank

  (End of data from sysinfo program)
  TD340 support 3 channels and 6 DIMMS per CPU, total 6 channels and
  12 DIMMS.  All 12 DIMM slots installed with 8 GB DIMM for this run.
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/root/cpu2006/libs/32:/root/cpu2006/libs/64:/root/cpu2006/sh"
OMP_NUM_THREADS = "12"
```

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General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-revA.xml

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For other inquiries, please contact webmaster@spec.org.

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